

1. Realize the following functions using minimum numbers of two inputs NAND gates:
  - (a) AND, OR, NOR, XOR and XNOR.
  - (b) Half Adder, Full adders, Half subtractor, and full subtractor.
  - (c) Repeat experiment (b) using XOR gates and two inputs NAND gates.
2. Implement BCD-to-Gray code convertor using minimum numbers of two inputs NAND gates.