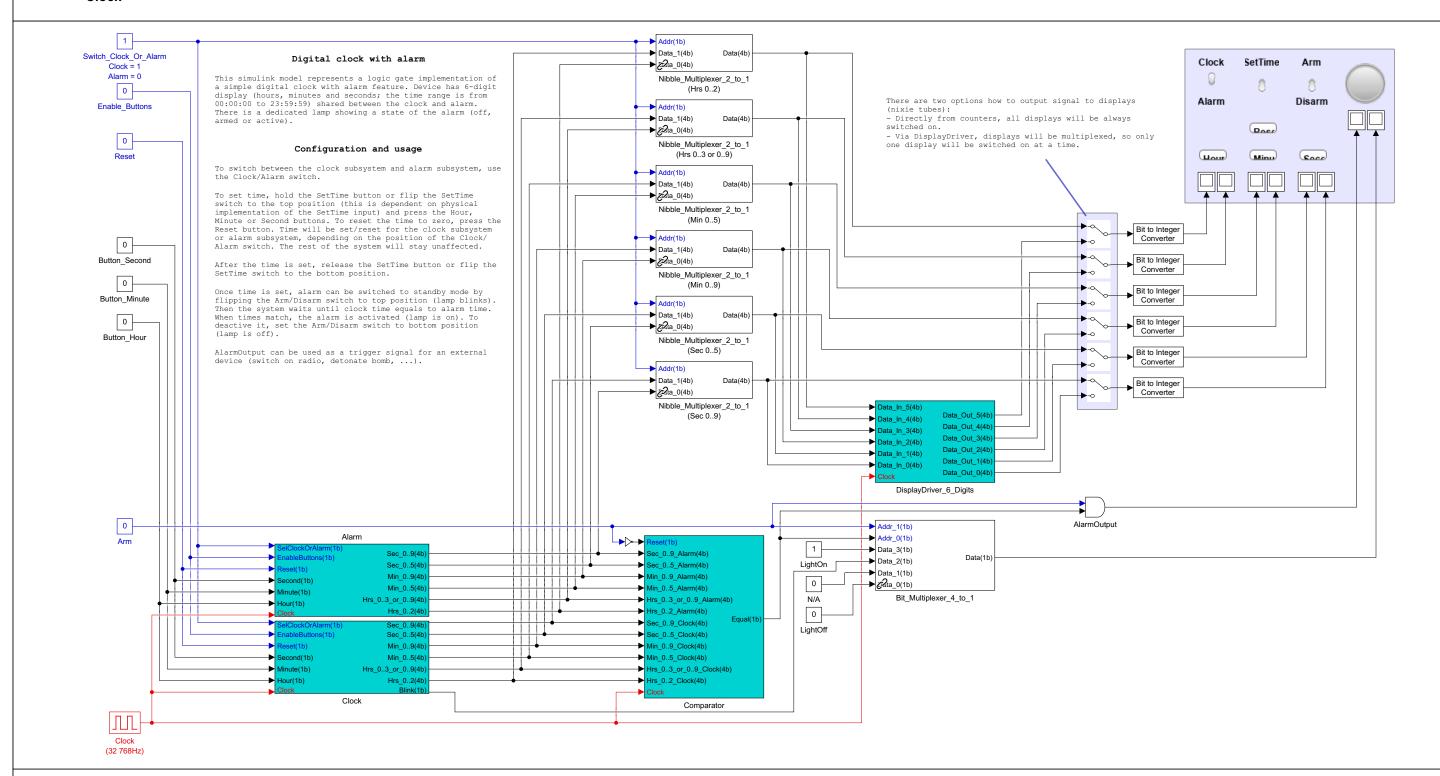
Clock



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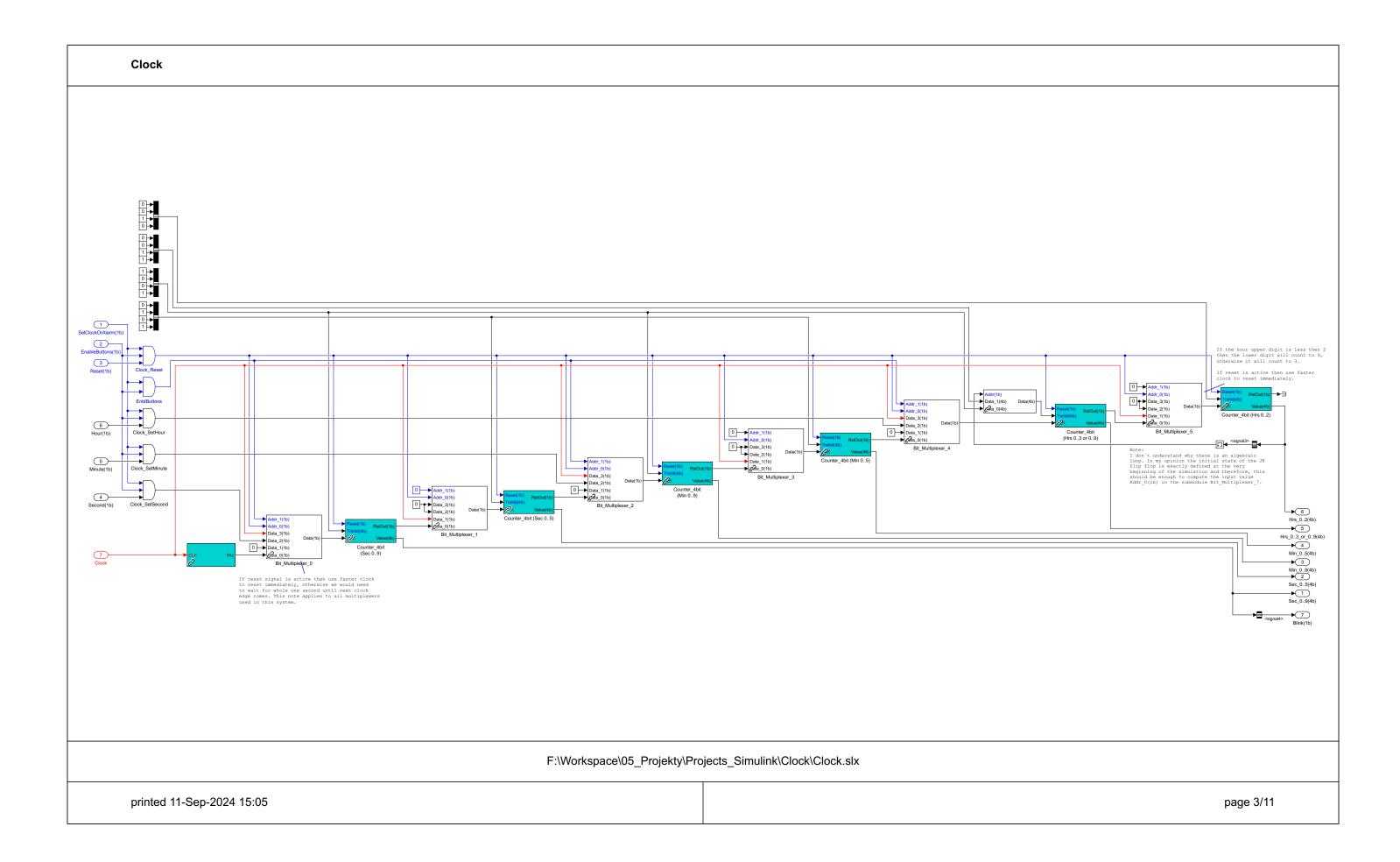
Clock/Alarm Clock 0 1 → 0 SelClockOrAlarm(1b) 2 EnableButtons(1b) RstOut(1b) 3 **2** Alarm_Reset Reset(1b) Value(4b) RstOut(1b) Sec_0..5(4b) Counter_4bit (Sec 0..5) **▶**1 Value(4b) Sec_0..9(4b) Counter_4bit 4 (Sec 0..9) Alarm_SetSecond Second(1b) Reset(1b) Trshld(4b) RstOut(1b) **4** RstOut(1b) Min_0..5(4b) Counter_4bit (Min 0..5) Trshld(4b) **→** 3 Value(4b) Min_0..9(4b) Counter_4bit 5 (Min 0..9) Alarm_SetMinute Minute(1b) RstOut(1b) **▶** 6 Value(4b) Addr(1b) Reset(1b) RstOut(1b) Hrs_0..2(4b) Counter_4bit (Hrs 0..2) **→** Data_1(4b) Data(4b) Trshld(4b) **► 2**2ta_0(4b) **→** 5 Value(4b) Hrs_0..3_or_0..9(4b) Counter_4bit (Hrs 0..3 or 0..9)

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Alarm_SetHour

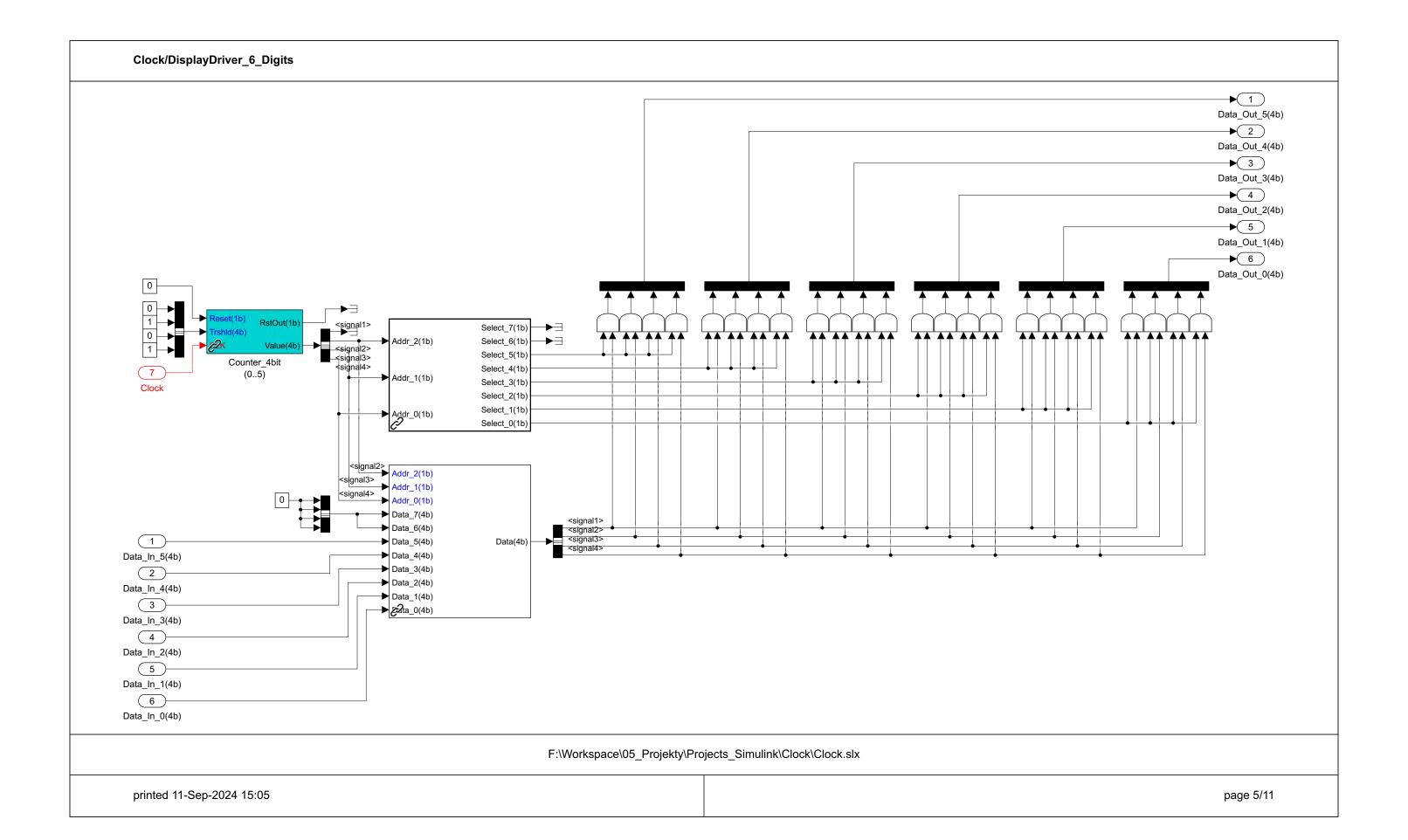
6

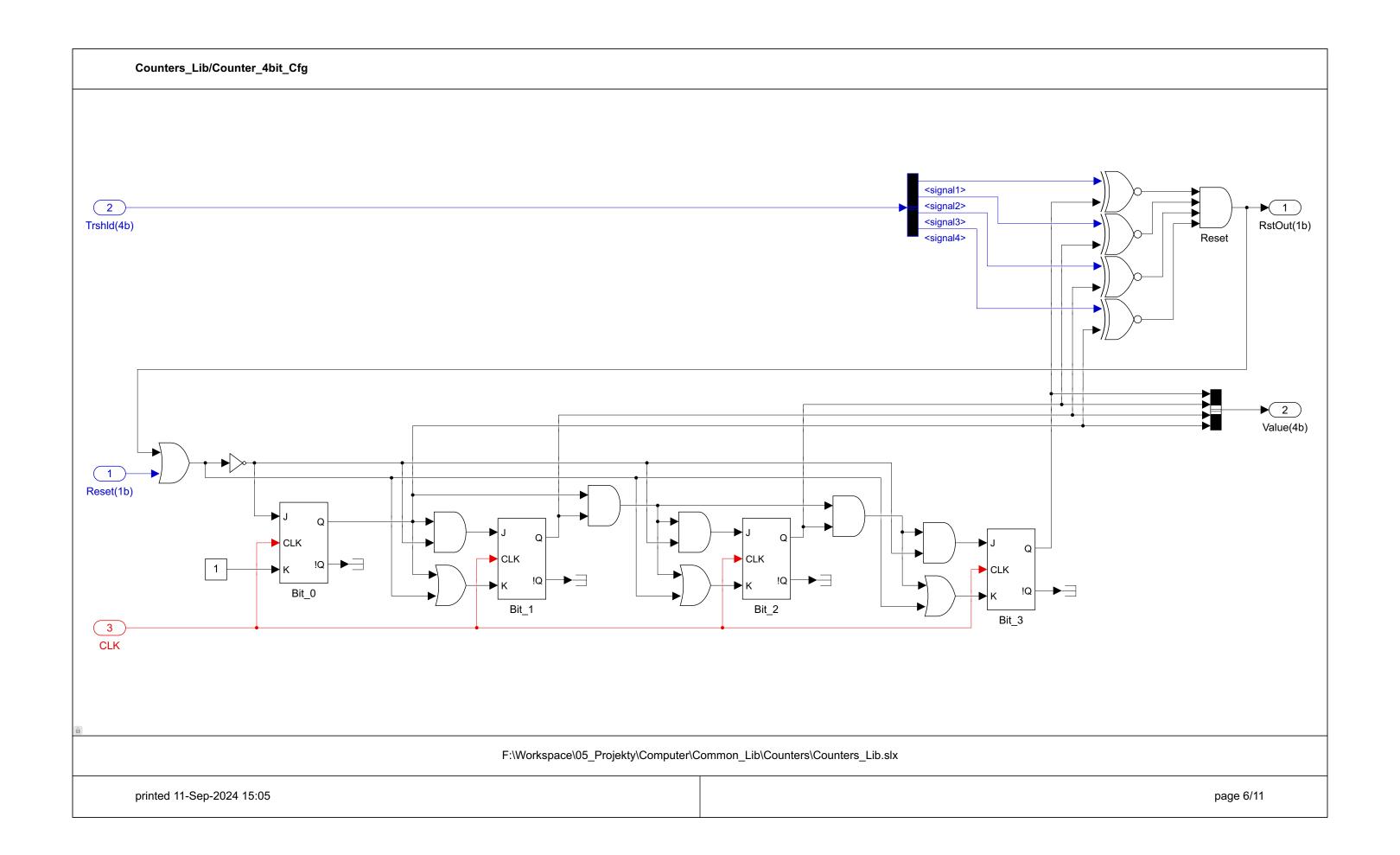
Hour(1b)



Clock/Comparator 3 Sec_0..5_Alarm(4b)

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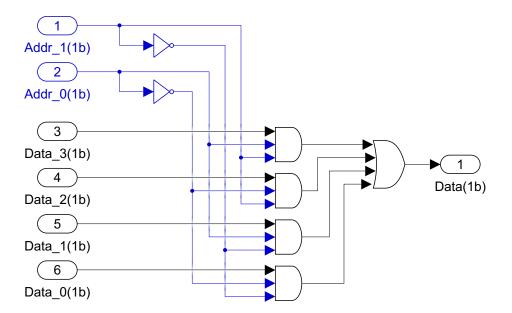
Nibble_Multiplexers_Lib/Nibble_Multiplexers_Lib/Nibble_Multiplexer_2_to_1 Addr(1b) **▶** Data_1(1b) Addr(1b) Data(1b) **▶ 2**(a_0(1b) **▶**1 Bit_Multiplexer_2_to_1(Bit_3) Data(4b) Addr(1b) **▶** Data_1(1b) Data(1b) **→** 22/2(1b) Bit_Multiplexer_2_to_1(Bit_2) Addr(1b) **▶** Data_1(1b) Data(1b) **→** 22/2(a_0(1b) Bit_Multiplexer_2_to_1(Bit_1) Addr(1b) **▶** Data_1(1b) Data(1b) **→** (a_0(1b) Bit_Multiplexer_2_to_1(Bit_0) 2 <signal2> Data_1(4b) <signal3> <signal4> <signal1> <signal2> 3 <signal3> Data_0(4b) <signal4>

 $F: \label{lem:likelihood} F: \label{likelihood} F: \label{likelihood} Wultiplexers \label{likelihood} It is a constant, which is a constant, which is a constant, which is a constant, which is a constant, and the constant is a constant, which is a constant, and the constant is a constant in the constant is a constant. In the constant is a constant in the constant is a constant in the constant is a constant in the constant in the constant is a constant in the constant in th$

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page 7/11

Bit_Multiplexers_Lib/Bit_Multiplexers_Lib/Bit_Multiplexer_4_to_1



Counters_Lib/Counter_15bit 1-CLK F:\Workspace\05_Projekty\Computer\Common_Lib\Counters\Counters_Lib.slx printed 11-Sep-2024 15:05 page 9/11

