input

		0	1	2	3	4	5	6	7
	0	0000	1000	0100	1100	0010	1010	0110	1110
	1	0100	1100	0010	1010	0110	1110	0000	1000
	2	0001	1001	0101	1101	0011	1011	0111	1111
))	3	0101	1101	0011	1011	0111	1111	0001	1001
	4	0011	1011	0111	1111	0001	1001	0101	1101
	5	0111	1111	0001	1001	0101	1101	0011	1011
	6	0010	1010	0110	1110	0000	1000	0100	1100
	7	0110	1110	0000	1000	0100	1100	0010	1010

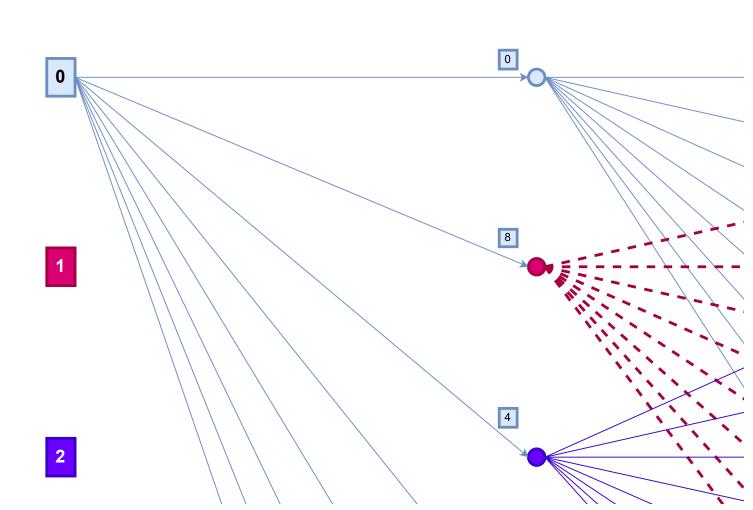
FSM state

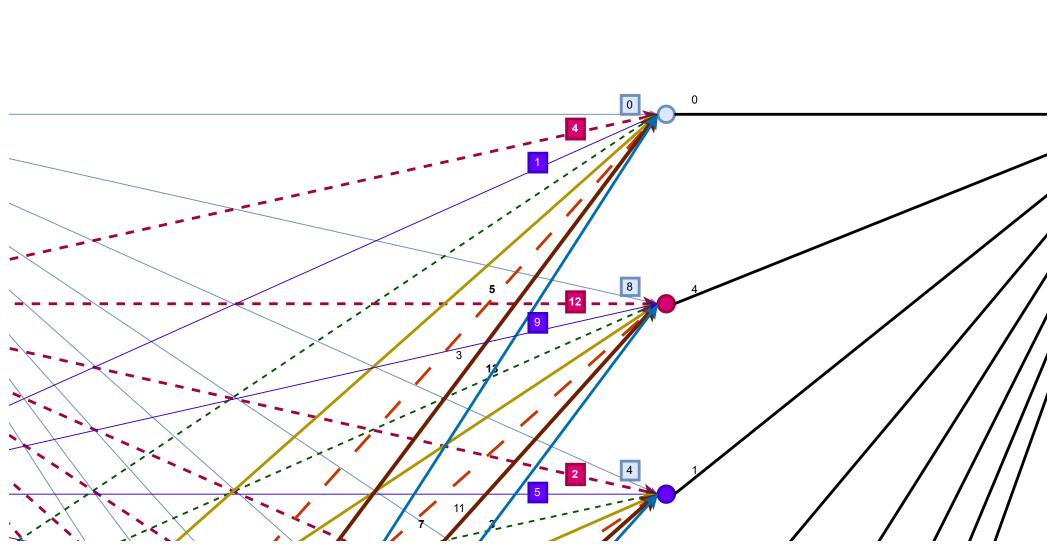
input

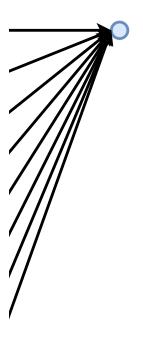
		0	1	2	3	4	5	6	7
	0	0	8	4	12	2	10	6	14
	1	4	12	2	10	6	14	0	8
	2	1	9	5	13	3	11	7	15
FSM state	3	5	13	3	11	7	15	1	9
	4	3	11	7	15	1	9	5	13
	5	7	15	1	9	5	13	3	11
	6	2	10	6	14	0	8	4	12
	7	6	14	0	8	4	12	2	10

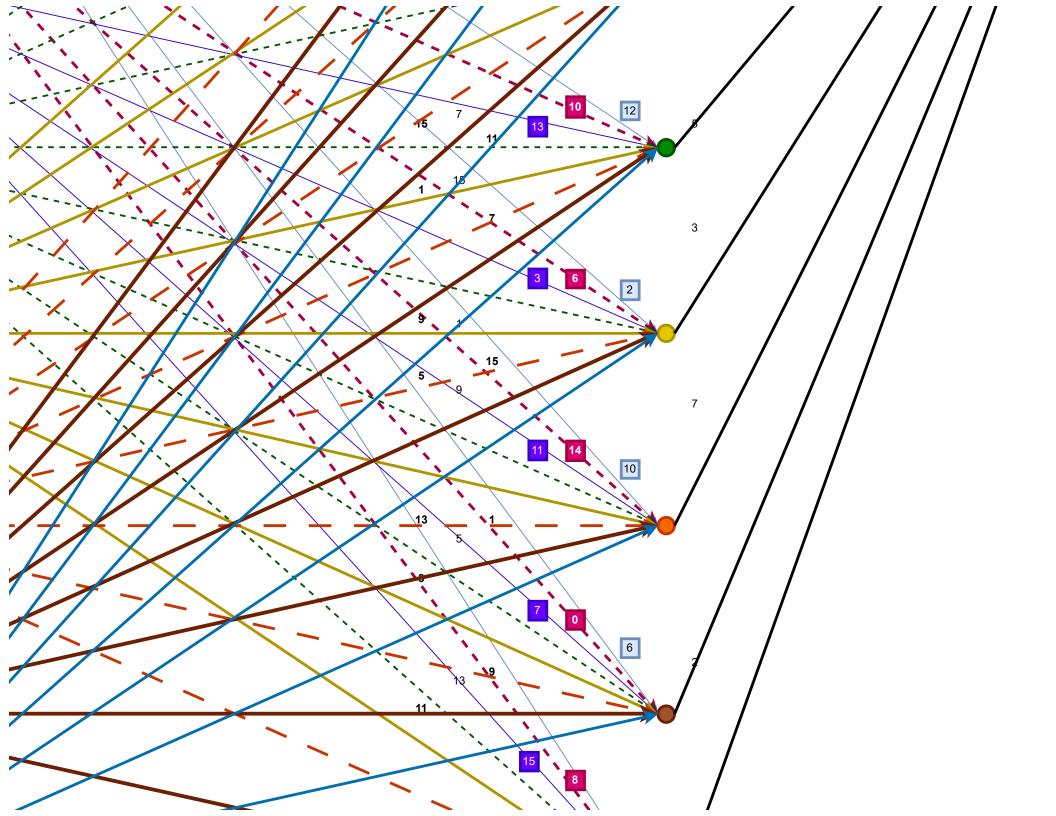
input

		0	1	2	3	4	5	6	7
	0	0000	1000	0100	1100	0010	1010	0110	1110
	1	0100	1100	0010	1010	0110	1110	0000	1000
	2	0001	1001	0101	1101	0011	1011	0111	1111
FSM state	3	0101	1101	0011	1011	0111	1111	0001	1001
	4	0011	1011	0111	1111	0001	1001	0101	1101
	5	0111	1111	0001	1001	0101	1101	0011	1011
	6	0010	1010	0110	1110	0000	1000	0100	1100
	7	0110	1110	0000	1000	0100	1100	0010	1010
	5	0111 0010	1111 1010	0001 0110	1001 1110	0101 0000	1101 1000	0011 0100	1011 1100



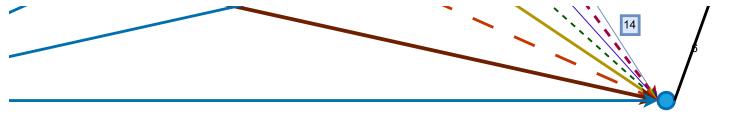








 t_0



$$t_n \leq t_{i-1}$$

 t_i