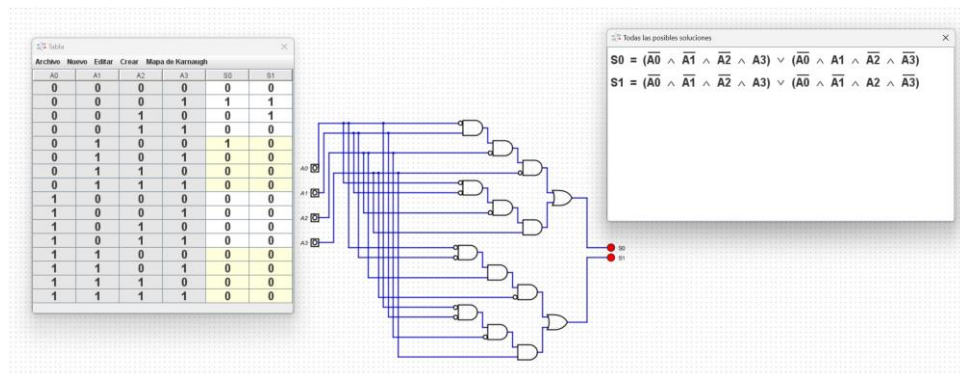


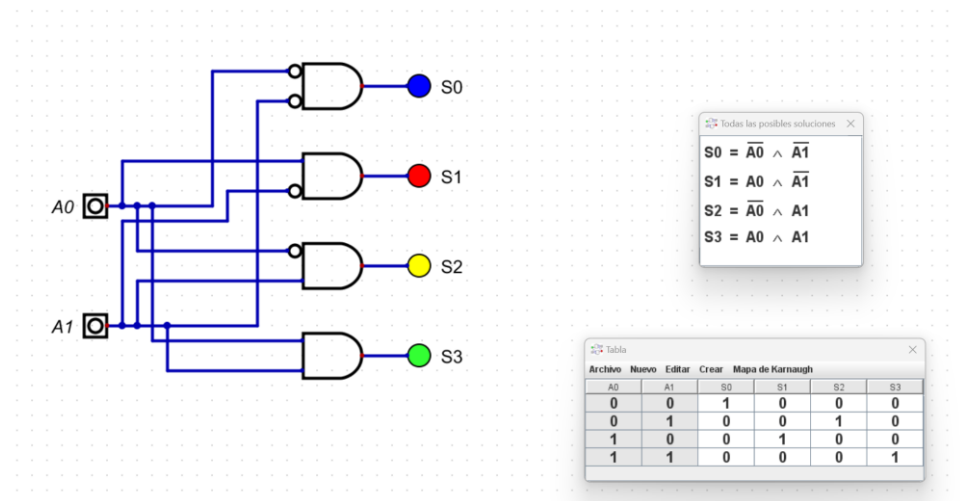
# T.P. N°1 - Arquitectura de las computadoras

Juan Cruz Ambrosini

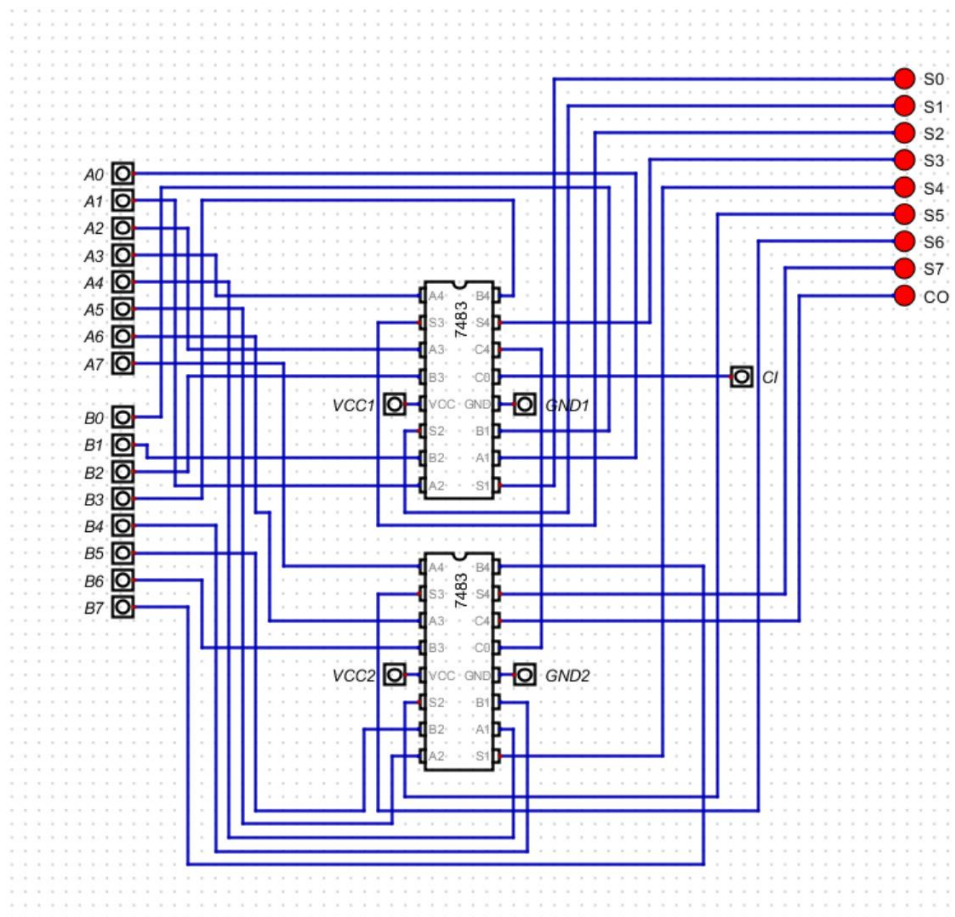
## Ejercicio 1:



## Ejercicio 2:



## Ejercicio 6:



**Resto de los ejercicios:**

Alumnos:

A.C.  
I.P.N.º 1

1  
19/3/24

Ej 1. 4 entradas a binario.

$A_0$	$A_1$	$A_2$	$A_3$	$S_1$	$S_0$
1	0	0	0	0	0
0	1	0	0	0	1
0	0	1	0	1	0
0	0	0	1	1	1

$$S_0 = \bar{A}_0 A_1 \bar{A}_2 \bar{A}_3 + \bar{A}_0 A_1 A_2 A_3$$

$$S_1 = \bar{A}_0 A_1 A_2 \bar{A}_3 + \bar{A}_0 A_1 \bar{A}_2 A_3$$

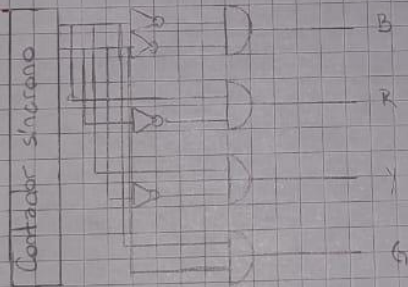
Ej 2. 2 entradas a 4

$A_1$	$A_0$	$S_0$	$S_1$	$S_2$	$S_3$
0	0	1	0	0	0
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	1

$$S_0 = \bar{A}_0 \bar{A}_1; S_1 = A_0 \bar{A}_1;$$

$$S_2 = \bar{A}_0 A_1; S_3 = A_0 A_1$$

Ej 3.



Ej 4.

Multiplexor:

1 1 0

$E_2$   $E_1$   $E_0$

Demultiplexor:

0 1 1

$E_2$   $E_1$   $E_0$

Ej. 5.

A B C<sub>1</sub> C<sub>2</sub> S (El acuerdo inicial es 0)

0 0 0 0 0

0 1 0 0 1

1 0 0 0 1

0 1 1 1 0

1 0 1 1 0

1 1 0 1 0

1 1 1 1 1