

nome dos fios:

saída da mem. \rightarrow 16 bits

15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

RgTO RgIN JMP ULA disponíveis operandos

$\rightarrow q[15..0]$

saída de busbar \rightarrow clock

master clear \rightarrow mclear

Unidade de controle (UC)

\rightarrow Rgin [1..0]

ULAp [1..0]

rclear

sclear

jump

regist [3..0]

Saída ULA

$\rightarrow R[3..0]$

Saída mux registradores

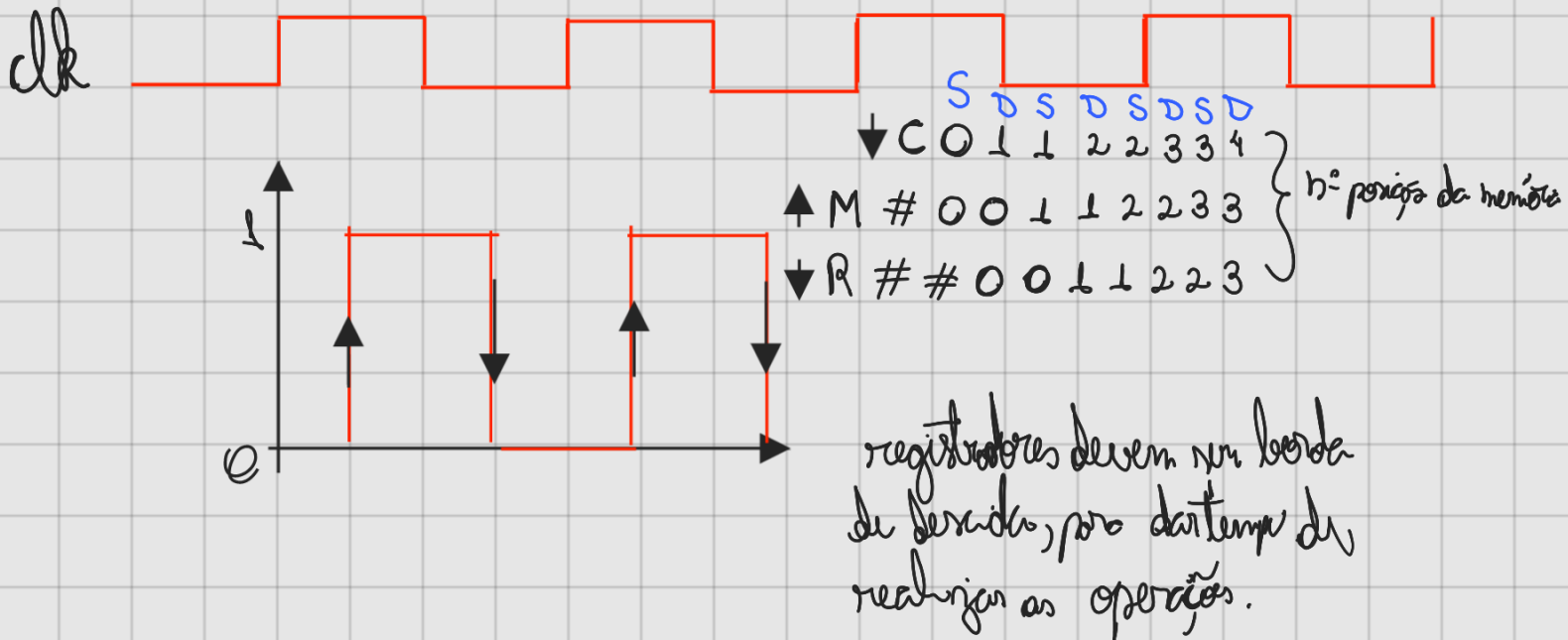
(4 \rightarrow 1)

$\rightarrow F[3..0]$

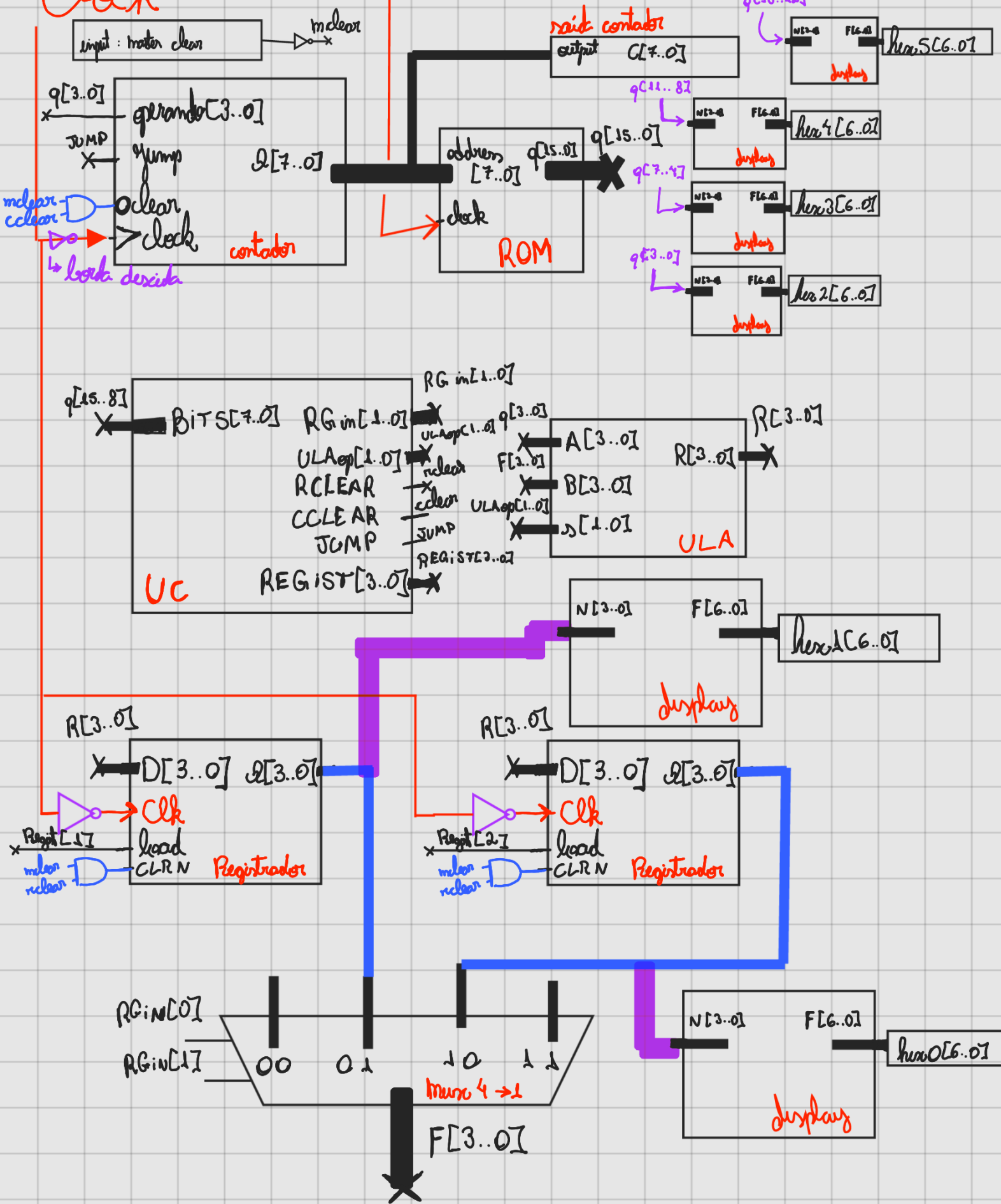
Contador

$\rightarrow C[7..0]$

Clock das operações:



Clock



MEF 1

	R1	R2
$R1 = R2 + 5$	5	0
$R2 = 1 \times 2$	5	2
$R1 = R2 - 2$	0	2
$R2 = 4/2$	2	2
Jump #8	2	2
clear	0	0
$R1 = R2 + 1$	1	0
$R2 = R1 + 1$	1	2
$R2 = R1 + 2$	1	3
$R1 = 2/2$	1	3
$R1 = 2 \times 2$	4	3
$R2 = 2/2$	4	1
$R1 = R2 - 0$	1	1
$R1 = R2 + 1$	2	1
CLR	2	1