



Grade Sheet for Lab #1

Fall 2015.

Group Number: 36

Group Member Name: Juan Morency Trudel

Student Number: _____

Group Member Name: Mathieu Sylvestre

Student Number: 260333709

Marks

2
2
2
2
2
2
2
2

1.	Schematic diagram for the 6-bit comparator	<u>Naunth</u>
2.	VHDL file for the 6-bit comparator	<u>Naunth</u>
3.	Initial partial simulation results for the 6-bit comparator	<u>Naunth</u>
4.	Complete simulation results for the 6-bit comparator	<u>Naunth</u>
5.	Boolean equations for the 1's counter circuit	<u>Naunth</u>
6.	VHDL description for the 1's counter circuit	<u>Naunth</u>
7.	Simulation Testbench VHDL for the 1's counter circuit	<u>Naunth</u>
8.	Simulation results for the 1's counter circuit	<u>Naunth</u>
		TA Signatures

Each part should be demonstrated to one of the TAs who will then give a grade and sign the grade sheet. Grades for each part will be either 0, 1, or 2. A mark of 2 will be given if everything is done correctly. A grade of 1 will be given if there are significant problems, but an attempt was made. A grade of 0 will be given for parts that were not done at all, or for which there is no TA signature.