

TALLER DE MICROCONTROLADORES CON DSPIC33FJ32MC202 CLASE5

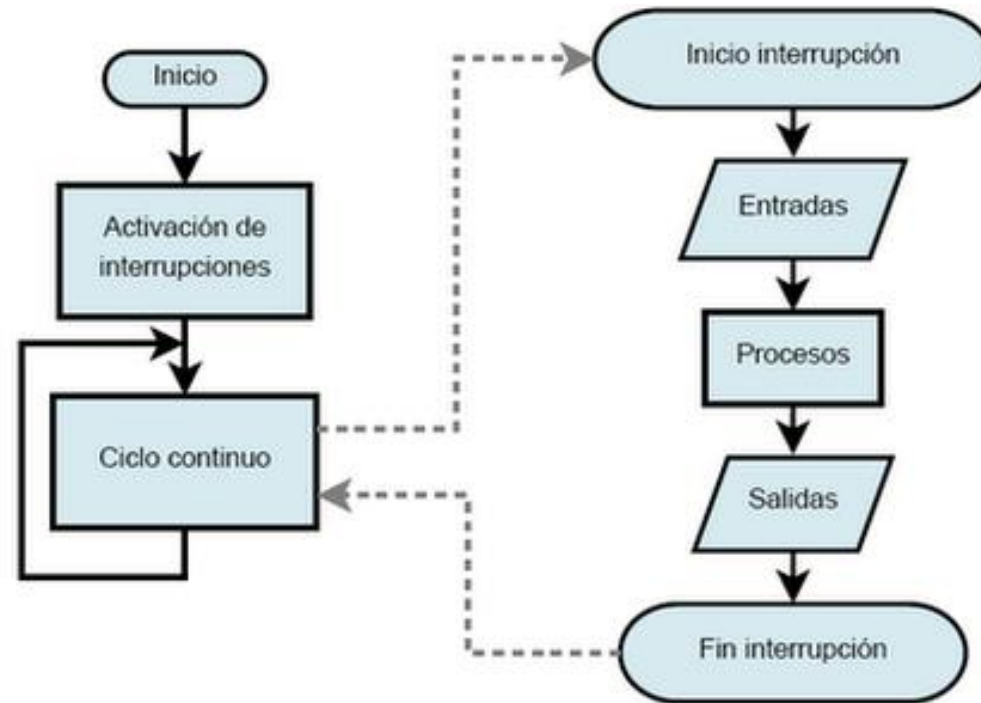
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INTERRUPCION EXTERNA INT0



28-PIN SPDIP, SOIC, SSOP

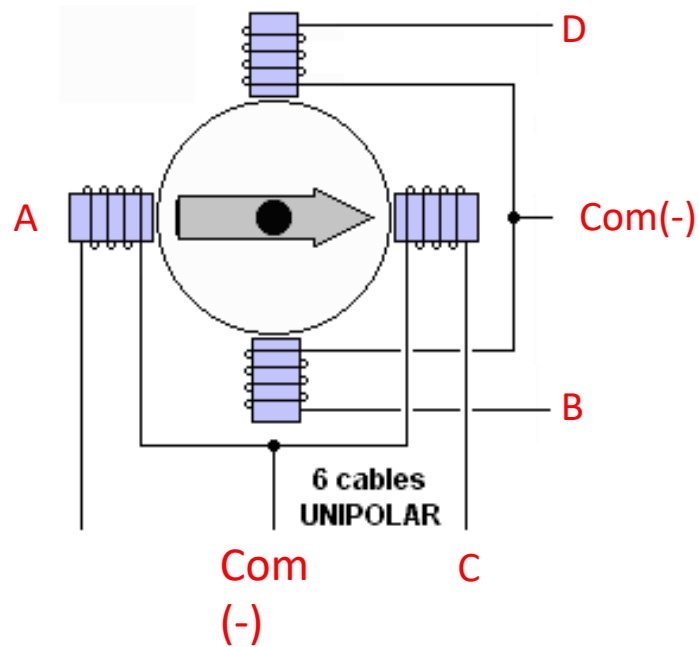
■ = Pins are up to 5V tolerant

MCLR	1	28	AVDD
AN0/VREF+/CN2/RA0	2	27	AVSS
AN1/VREF-/CN3/RA1	3	26	PWM1L1/RP15 ⁽¹⁾ /CN11/RB15
PGED1/AN2/C2IN-/RP0 ⁽¹⁾ /CN4/RB0	4	25	PWM1H1/RP14 ⁽¹⁾ /CN12/RB14
PGEC1/AN3/C2IN+/RP1 ⁽¹⁾ /CN5/RB1	5	24	PWM1L2/RP13 ⁽¹⁾ /CN13/RB13
AN4/RP2 ⁽¹⁾ /CN6/RB2	6	23	PWM1H2/RP12 ⁽¹⁾ /CN14/RB12
AN5/RP3 ⁽¹⁾ /CN7/RB3	7	22	PGEC2/TMS/PWM1L3/RP11 ⁽¹⁾ /CN15/RB11
VSS	8	21	PGED2/TDI/PWM1H3/RP10 ⁽¹⁾ /CN16/RB10
OSC1/CLKI/CN30/RA2	9	20	VCAP
OSC2/CLKO/CN29/RA3	10	19	VSS
SOSCI/RP4 ⁽¹⁾ /CN1/RB4	11	18	TDO/PWM2L1/SDA1/RP9 ⁽¹⁾ /CN21/RB9
SOSCO/T1CK/CN0/RA4	12	17	TCK/PWM2H1/SCL1/RP8 ⁽¹⁾ /CN22/RB8
VDD	13	16	INT0/RP7/CN23/RB7
PGED3/ASDA1/RP5 ⁽¹⁾ /CN27/RB5	14	15	PGEC3/ASCL1/RP6 ⁽¹⁾ /CN24/RB6

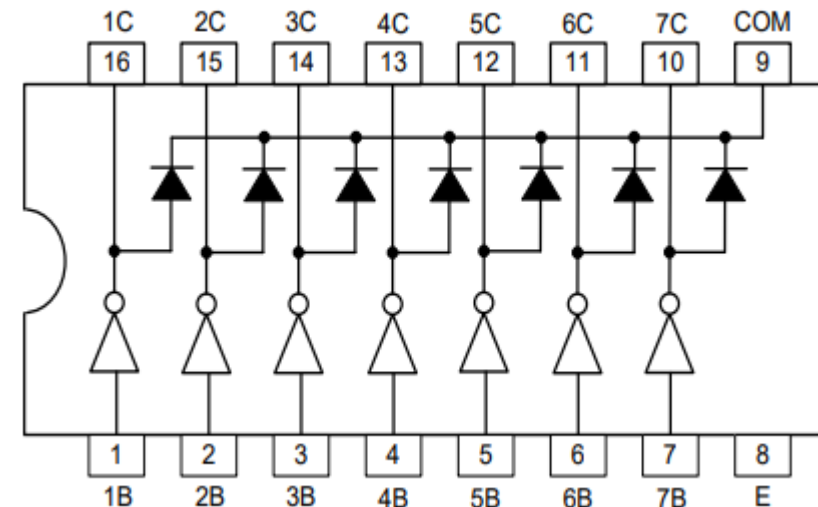
Ejercicio - Motor PAP UNIPOLAR con Interrupción

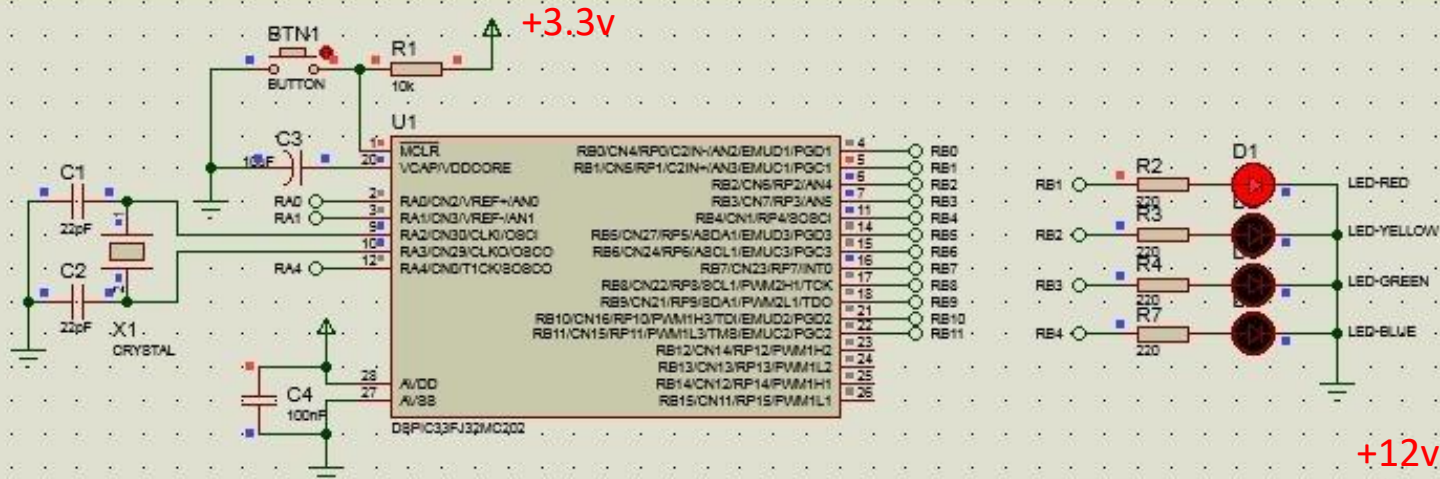
Hacer girar un motor PAP unipolar en sentido antihorario y que se detenga por 3 seg. cuando se detecte una interrupción por flanco de subida en el pin RB7.

(t_{min} entre cada secuencia = 10ms)



ULN2003A





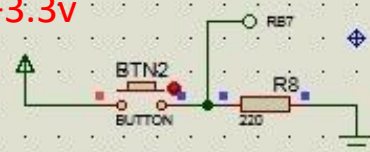
Paso completo Simple

Paso	A	B	C	D
1	1	0	0	0
2	0	1	0	0
3	0	0	1	0
4	0	0	0	1

Paso completo con 2 bobinas

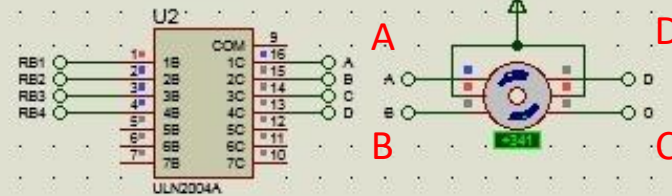
Paso	A	B	C	D
1	1	1	0	0
2	0	1	1	0
3	0	0	1	1
4	1	0	0	1

+3.3v



Medio Paso

Paso	A	B	C	D
1	1	0	0	0
2	1	1	0	0
3	0	1	0	0
4	0	1	1	0
5	0	0	1	0
6	0	0	1	1
7	0	0	0	1
8	1	0	0	1



+12v

BIBLIOGRAFIA

- <https://www.diarioelectronicohoy.com/blog/motores-pap-unipolares>
- <https://www.youtube.com/watch?v=2-nVV9S7leM&t=651s>