

Note: When VM is present and the buck-converter supplies the LDO, USB power is disabled through the AP2161W. Active LOW







J4
 Conn_01x04_Female

Conn_02x07_Odd_Even

Pin	Signal
1	3.3V
2	ADC12_IN2
3	GLS4
4	OUT4
5	GLS4
6	FAN_PWN
7	SPI1_NSS
8	TEMP_3.3v
9	CAL_MT6B35
10	OUT1
11	VM
12	GHS1
13	GND
14	GLS1

Pinout diagram for J5 connector (Conn_01x09_Female):

- Pin 1: RESET
- Pin 2: GND
- Pin 3: +3.3v
- Pin 4: VBUS
- Pin 5: ADC1_IN4
- Pin 6: SPLSS
- Pin 7: SWO/SPI3_SCK
- Pin 8: SPI3_MISO
- Pin 9: SPI3_MOSI

USB_BUS





J9
 Conn_01x04_Female