

Organização de Computadores

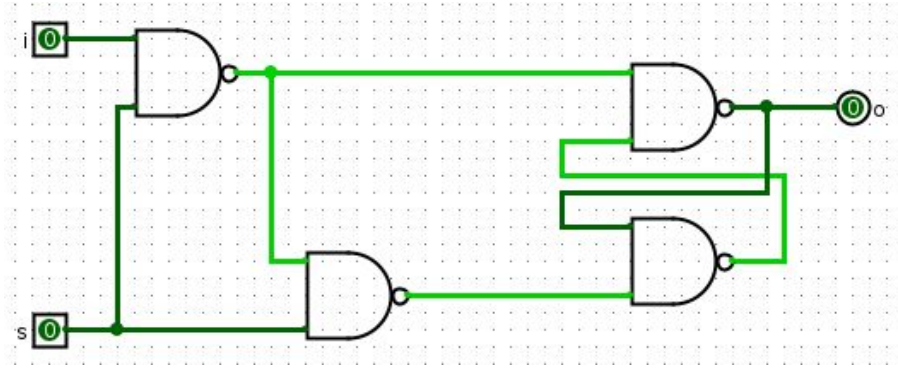
Versão Hands-on com Logisim

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Lembrando 1 bit

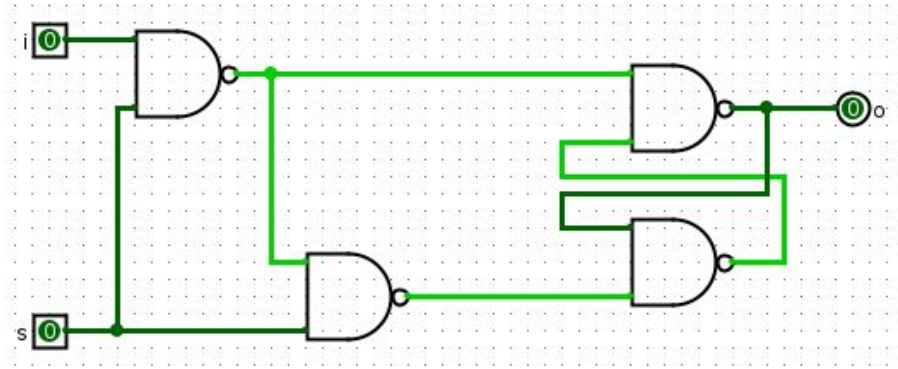
Circuito



0	0	1
0	1	1
1	0	1
1	1	0

input	set	output

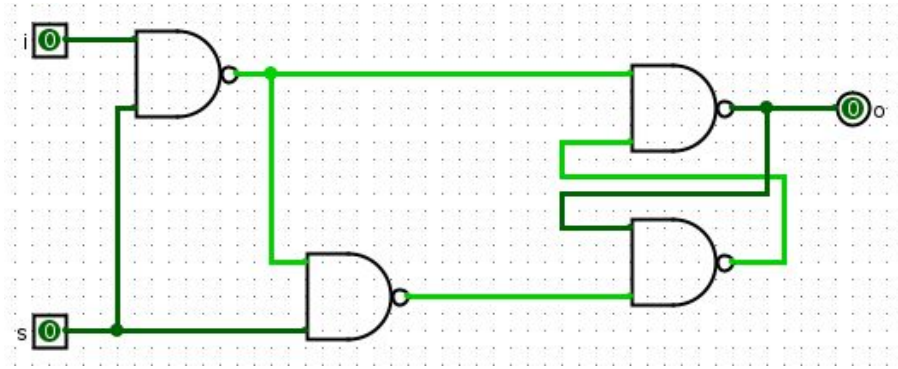
Circuito



0	0	1
0	1	1
1	0	1
1	1	0

input	set	output
0	0	x (armazenado)
1	0	x (armazenado)
0	1	0 (input)
1	1	1 (input)

Circuito



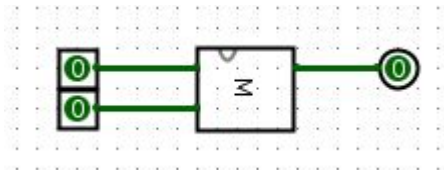
0	0	1
0	1	1
1	0	1
1	1	0

Testar no logisim!

input	set	output
0	0	x (armazenado)
1	0	x (armazenado)
0	1	0 (input)
1	1	1 (input)

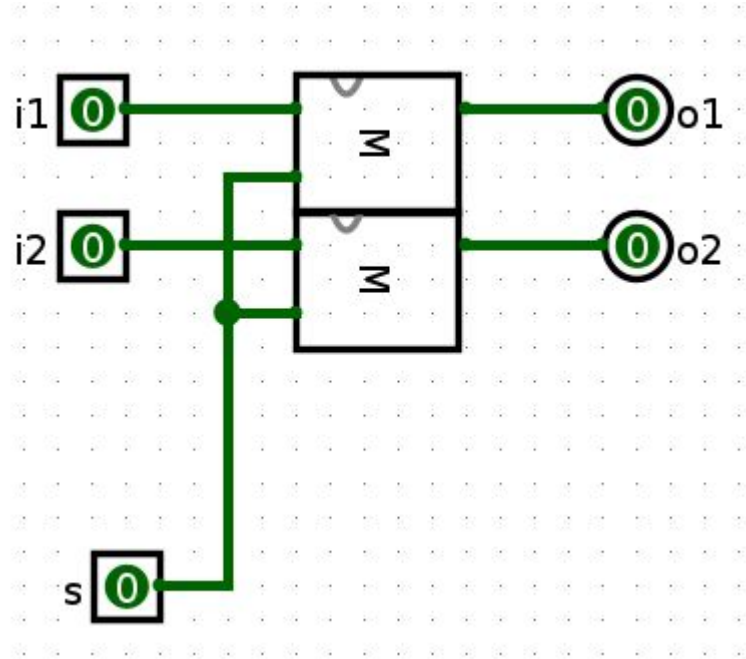
Construindo um registrador

Memória de 1 bit

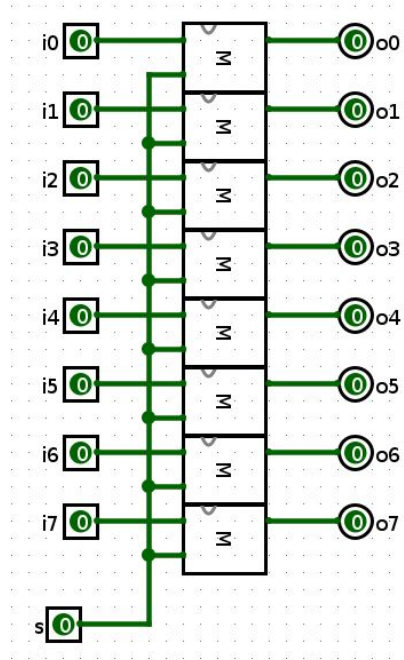


input	set	output
0	0	x (armazenado)
1	0	x (armazenado)
0	1	0 (input)
1	1	1 (input)

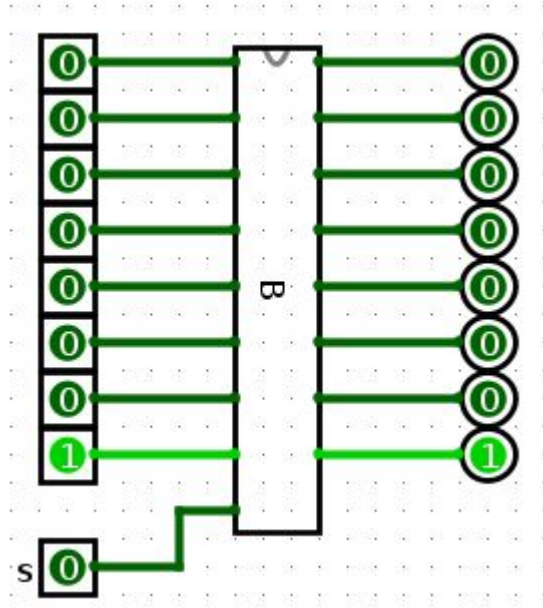
Memória de 2 bits



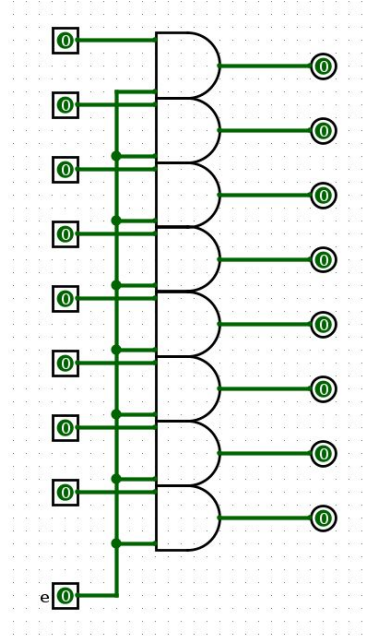
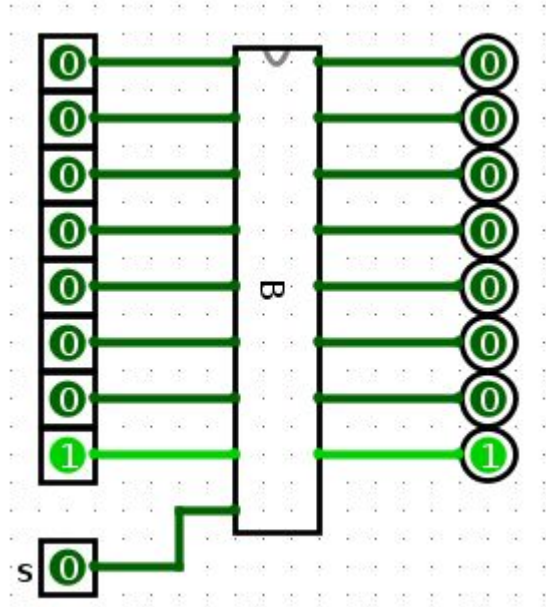
Memória de 8 bits (1 byte)



Memória de 1 byte com set bit

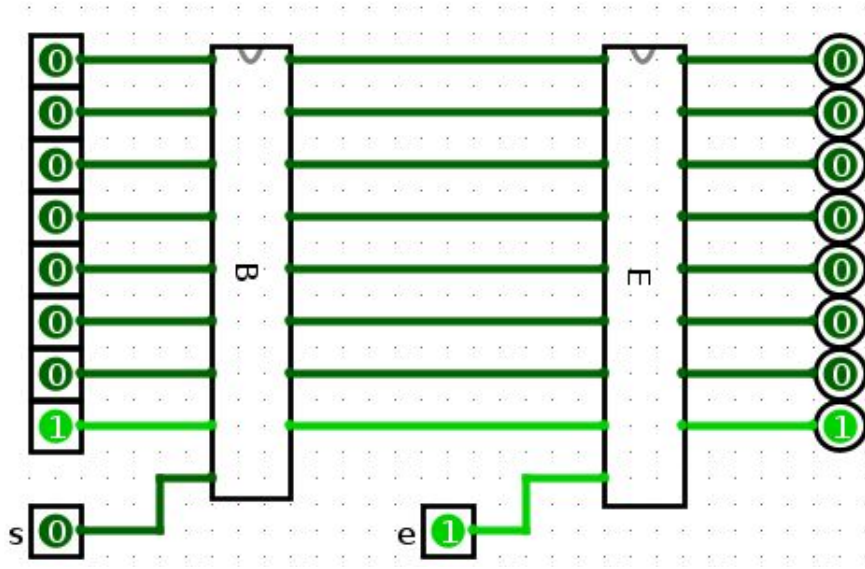


Memória: 1 byte com *set* e *enable* bits

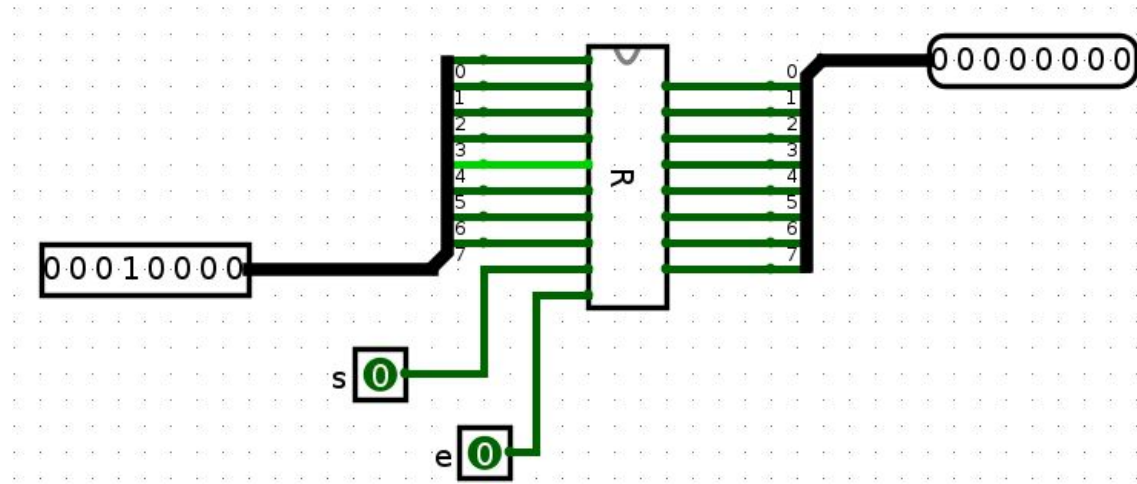


0	0	0
0	1	0
1	0	0
1	1	1

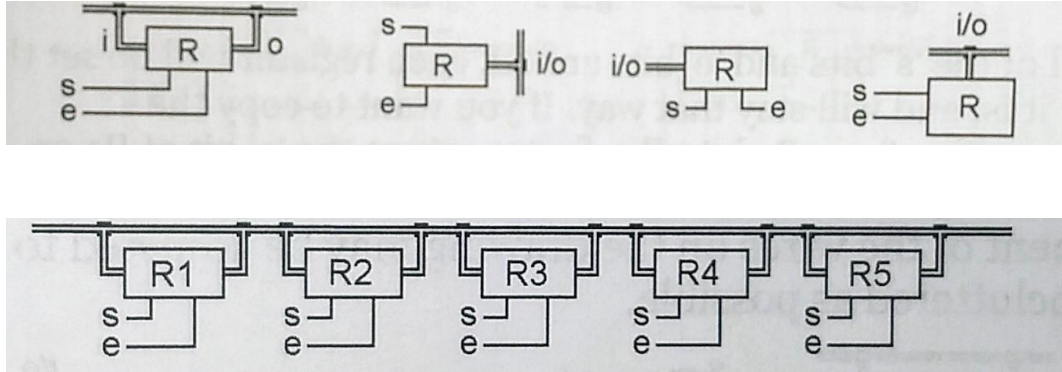
Memória: 1 byte com *set* e *enable* bits



Registrador de 1 byte com *set* e *enable*



Registrador de 1 byte com *set* e *enable*



Atividades

- Enviar o circuito (exemplo nome de arquivo: *aula_3_nome_sobrenome.circ*)

