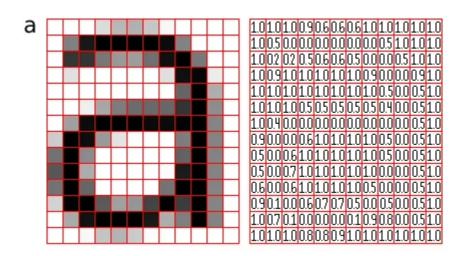
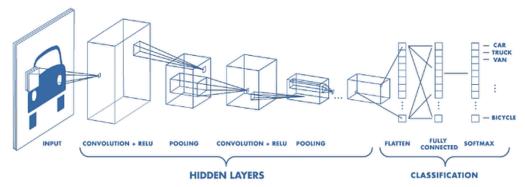
Convolutional Neural Network Architecture

A Convolutional Neural Network, also known as CNN or ConvNet, is a class of <u>neural networks</u> that specializes in processing data that has a grid-like topology, such as an image. A digital image is a binary representation of visual data. It contains a series of pixels arranged in a grid-like fashion that contains pixel values to denote how bright and what color each pixel should be.



The human brain processes a huge amount of information the second we see an image. Each neuron works in its own receptive field and is connected to other neurons in a way that they cover the entire visual field. Just as each neuron responds to stimuli only in the restricted region of the visual field called the receptive field in the biological vision system, each neuron in a CNN processes data only in its receptive field as well. The layers are arranged in such a way so that they detect simpler patterns first (lines, curves, etc.) and more complex patterns (faces, objects, etc.) further along. By using a CNN, one can enable sight to computers.



layer.

Figure 2: Architecture of a CNN (Source)

Convolution Layer

The convolution layer is the core building block of the CNN. It carries the main portion of the network's computational load. This layer performs a dot product between two matrices, where one matrix is the set of learnable parameters otherwise known as a kernel, and the other matrix is the restricted portion of the receptive field. The kernel is spatially smaller than an image but is more in-depth. This means that, if the image is composed of three (RGB) channels, the kernel height and width will be spatially small, but the depth extends up to all three channels.

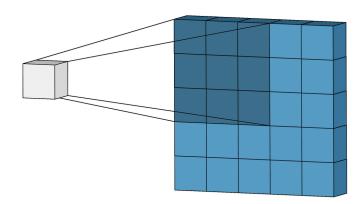


Illustration of Convolution Operation (source)

During the forward pass, the kernel slides across the height and width of the image-producing the image representation of that receptive region. This produces a two-dimensional

representation of the image known as an activation map that gives the response of the kernel at each spatial position of the image. The sliding size of the kernel is called a stride.

If we have an input of size W x W x D and Dout number of kernels with a spatial size of F with stride S and amount of padding P, then the size of output volume can be determined by the following formula:

$$W_{out} = \frac{W - F + 2P}{S} + 1$$

The formula for Convolution Layer

This will yield an output volume of size Wout x Wout x Dout.

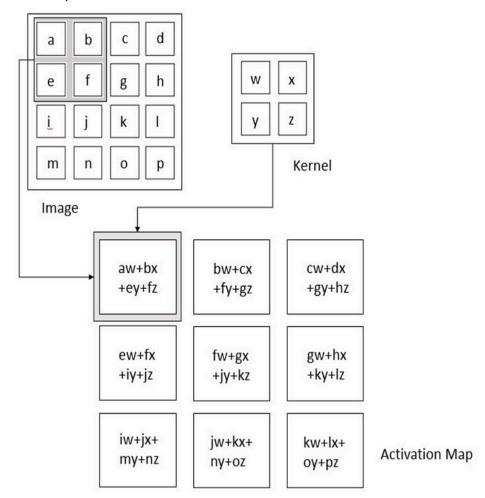


Figure 3: Convolution Operation (Source: Deep Learning by Ian Goodfellow, Yoshua Bengio, and Aaron Courville)

Motivation behind Convolution

Convolution leverages three important ideas that motivated computer vision researchers: sparse interaction, parameter sharing, and equivariant representation. Let's describe each one of them in detail.

Trivial neural network layers use matrix multiplication by a matrix of parameters describing the interaction between the input and output unit. This means that every output unit interacts with every input unit. However, convolution neural networks have sparse interaction. This is achieved by making kernel smaller than the input e.g., an image can have millions or thousands of pixels, but while processing it using kernel we can detect meaningful information that is of tens or hundreds of pixels. This means that we need to store fewer parameters that not only reduces the memory requirement of the model but also improves the statistical efficiency of the model.

If computing one feature at a spatial point (x1, y1) is useful then it should also be useful at some other spatial point say (x2, y2). It means that for a single two-dimensional slice i.e., for creating one activation map, neurons are constrained to use the same set of weights. In a traditional neural network, each element of the weight matrix is used once and then never revisited, while convolution network has shared parameters i.e., for getting output, weights applied to one input are the same as the weight applied elsewhere.

Due to parameter sharing, the layers of convolution neural network will have a property of equivariance to translation. It says that if we changed the input in a way, the output will also get changed in the same way.

Pooling Layer

The pooling layer replaces the output of the network at certain locations by deriving a summary statistic of the nearby outputs. This helps in reducing the spatial size of the representation, which decreases the required amount of computation and weights. The pooling operation is processed on every slice of the representation individually.

There are several pooling functions such as the average of the rectangular neighborhood, L2 norm of the rectangular neighborhood, and a weighted average based on the distance from the central pixel. However, the most popular process is max pooling, which reports the maximum output from the neighborhood.

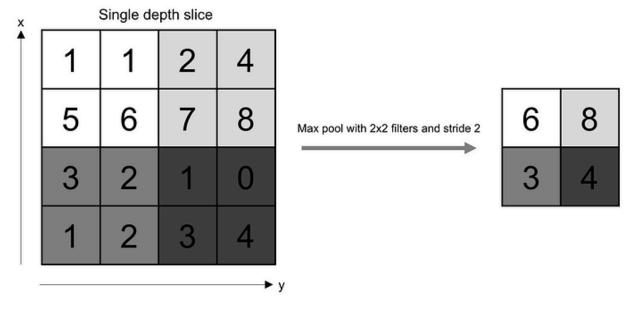


Figure 4: Pooling Operation (Source: O'Reilly Media)

If we have an activation map of size W x W x D, a pooling kernel of spatial size F, and stride S, then the size of output volume can be determined by the following formula:

$$W_{out} = \frac{W - F}{S} + 1$$

Formula for Padding Layer

This will yield an output volume of size Wout x Wout x D.

In all cases, pooling provides some translation invariance which means that an object would be recognizable regardless of where it appears on the frame.

Fully Connected Layer

Neurons in this layer have full connectivity with all neurons in the preceding and succeeding layer as seen in regular FCNN. This is why it can be computed as usual by a matrix multiplication followed by a bias effect.

The FC layer helps to map the representation between the input and the output.

Non-Linearity Layers

Since convolution is a linear operation and images are far from linear, non-linearity layers are often placed directly after the convolutional layer to introduce non-linearity to the activation map.

There are several types of non-linear operations, the popular ones being:

1. Sigmoid

The sigmoid non-linearity has the mathematical form $\sigma(\kappa) = 1/(1+e^{-\kappa})$. It takes a real-valued number and "squashes" it into a range between 0 and 1.

However, a very undesirable property of sigmoid is that when the activation is at either tail, the gradient becomes almost zero. If the local gradient becomes very small, then in backpropagation it will effectively "kill" the gradient. Also, suppose the data coming into the neuron is always positive. In that case, the output of the sigmoid will be either all positives or all negatives, resulting in a zig-zag dynamic of gradient updates for weight.

2. Tanh

Tanh squashes a real-valued number to the range [-1, 1]. Like sigmoid, the activation saturates, but — unlike the sigmoid neurons — its output is zero-centred.

3. ReLU

The Rectified Linear Unit (ReLU) has become very popular in the last few years. It computes the function $f(\kappa)$ =max $(0,\kappa)$. In other words, the activation is simply a threshold at zero. In comparison to sigmoid and tanh, ReLU is more reliable and accelerates the convergence by six times.

Unfortunately, a con is that ReLU can be fragile during training. A large gradient flowing through it can update it in such a way that the neuron will never get further updated. However, we can work with this by setting a proper learning rate.

Designing a Convolutional Neural Network

Now that we understand the various components, we can build a convolutional neural network. We will be using Fashion-MNIST, which is a dataset of Zalando's article images consisting of a training set of 60,000 examples and a test set of 10,000 examples. Each example is a 28x28 grayscale image, associated with a label from 10 classes. The dataset can be downloaded here. Our convolutional neural network has an architecture as follows:

```
[INPUT]

→[CONV 1] → [BATCH NORM] → [ReLU] → [POOL 1]

→ [CONV 2] → [BATCH NORM] → [ReLU] → [POOL 2]

→ [FC LAYER] → [RESULT]
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For both conv layers, we will use kernel of spatial size 5 x 5 with stride size 1 and padding of 2. For both pooling layers, we will use max pool operation with kernel size 2, stride 2, and zero padding.

CONV 1

Input Size $(W_1 x H_1 x D_1) = 28 x 28 x 1$

- Requires four hyperparameter:
 - Number of kernels, k = 16
 - Spatial extend of each one, F = 5
 - Stride Size, S = 1
 - Amount of zero padding, P = 2
- Outputting volume of W₂ x H₂ x D₂

$$OW_2 = (28 - 5 + 2(2)) / 1 + 1 = 28$$

$$OH_2 = (28-5+2(2))/1+1=28$$

O $D_2 = k$

Output of Conv 1 ($W_2 \times H_2 \times D_2$) = 28 x 28 x 16

Calculations for Conv 1 Layer (Image by Author)

POOL 1

Input Size $(W_2 \times H_2 \times D_2) = 28 \times 28 \times 16$

- Requires two hyperparameter:
 - Spatial extend of each one, F = 2
 - Stride Size, S = 2
- Outputting volume of W₃ x H₃ x D₂

$$OW_3 = (28 - 2) / 2 + 1 = 14$$

$$OH_3 = (28-2)/2+1=14$$

Output of Pool 1 ($W_3 \times H_3 \times D_2$) = 14 x 14 x 16

Calculations for Pool1 Layer (Image by Author)

CONV 2

Input Size $(W_3 \times H_3 \times D_2) = 14 \times 14 \times 16$

- Requires four hyperparameter:
 - o Number of kernels, k = 32
 - o Spatial extend of each one, F = 5
 - o Stride Size, S = 1
 - o Amount of zero padding, P = 2
- Outputting volume of W₄ x H₄ x D₃

o
$$W_4 = (14 - 5 + 2(2)) / 1 + 1 = 14$$

o
$$H_4 = (14 - 5 + 2(2)) / 1 + 1 = 14$$

Output of Conv 2 ($W_4 \times H_4 \times D_3$) = 14 x 14 x 32

Calculations for Conv 2 Layer (Image by Author)

POOL 2

Input Size $(W_4 \times H_4 \times D_3) = 14 \times 14 \times 32$

- Requires two hyperparameter:
 - Spatial extend of each one, F = 2
 - Stride Size, S = 2
- Outputting volume of W₅ x H₅ x D₃

$$OW_5 = (14-2)/2+1=7$$

$$\circ$$
 H₅ = (14 - 2) / 2 + 1 = 7

Output of Pool 2 ($W_5 \times H_5 \times D_3$) = 7 x 7 x 32

Calculations for Pool2 Layer (Image by Author)

FC Layer

Input Size $(W_5 \times H_5 \times D_3) = 7 \times 7 \times 32$

Output Size (Number of Classes) = 10

Size of Fully Connected Layer (Image by Author)