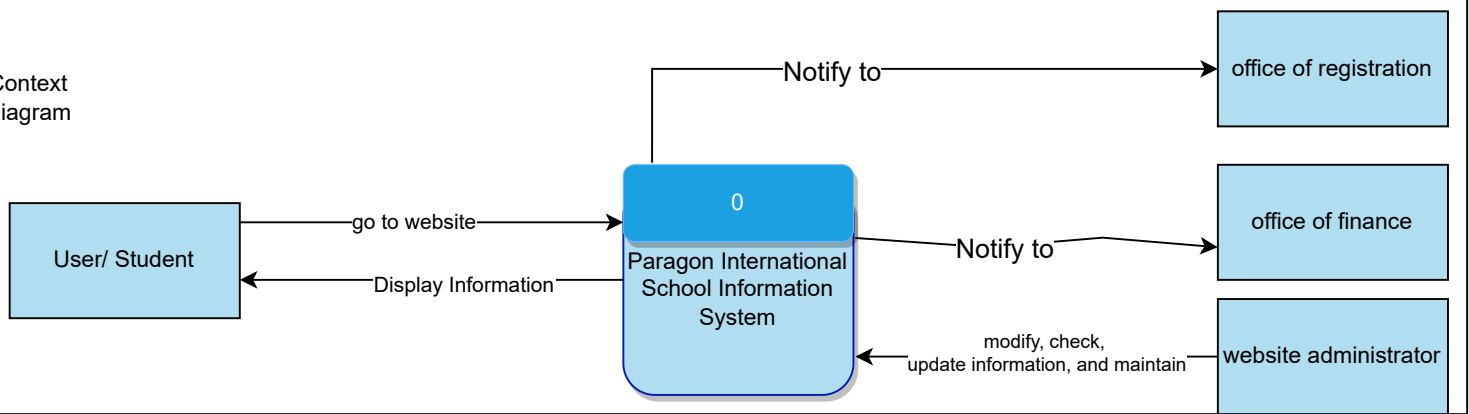
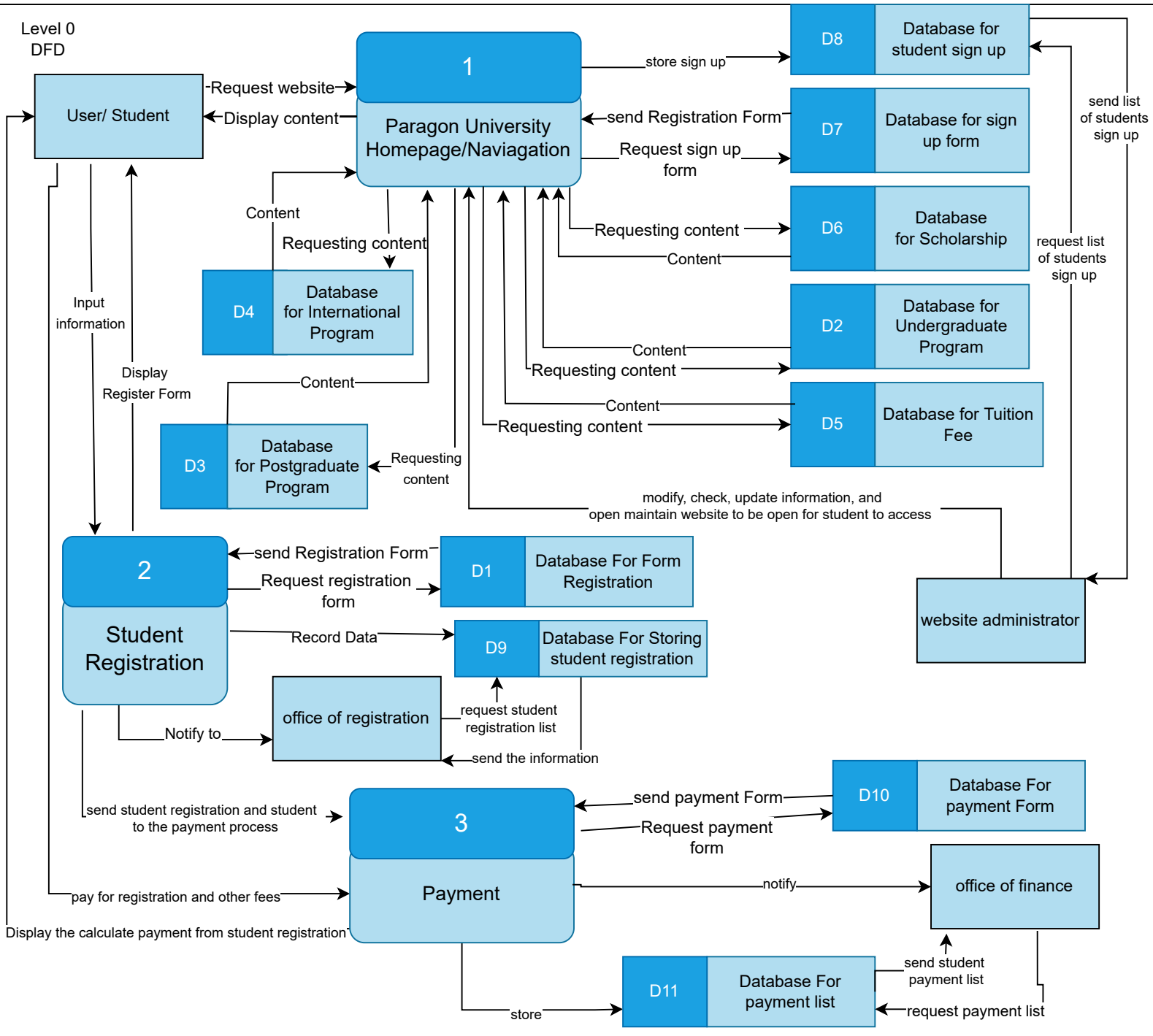


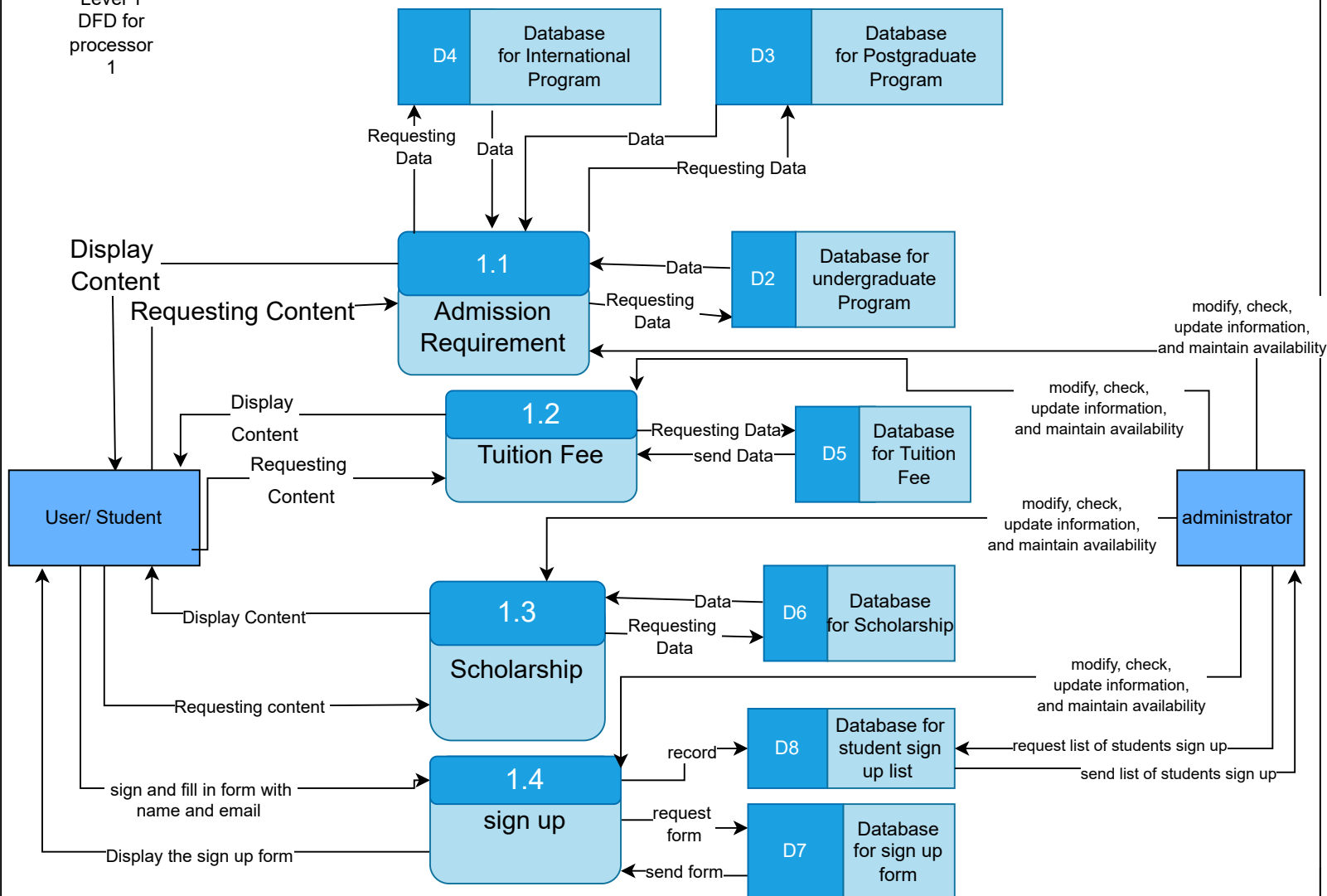
Context Diagram



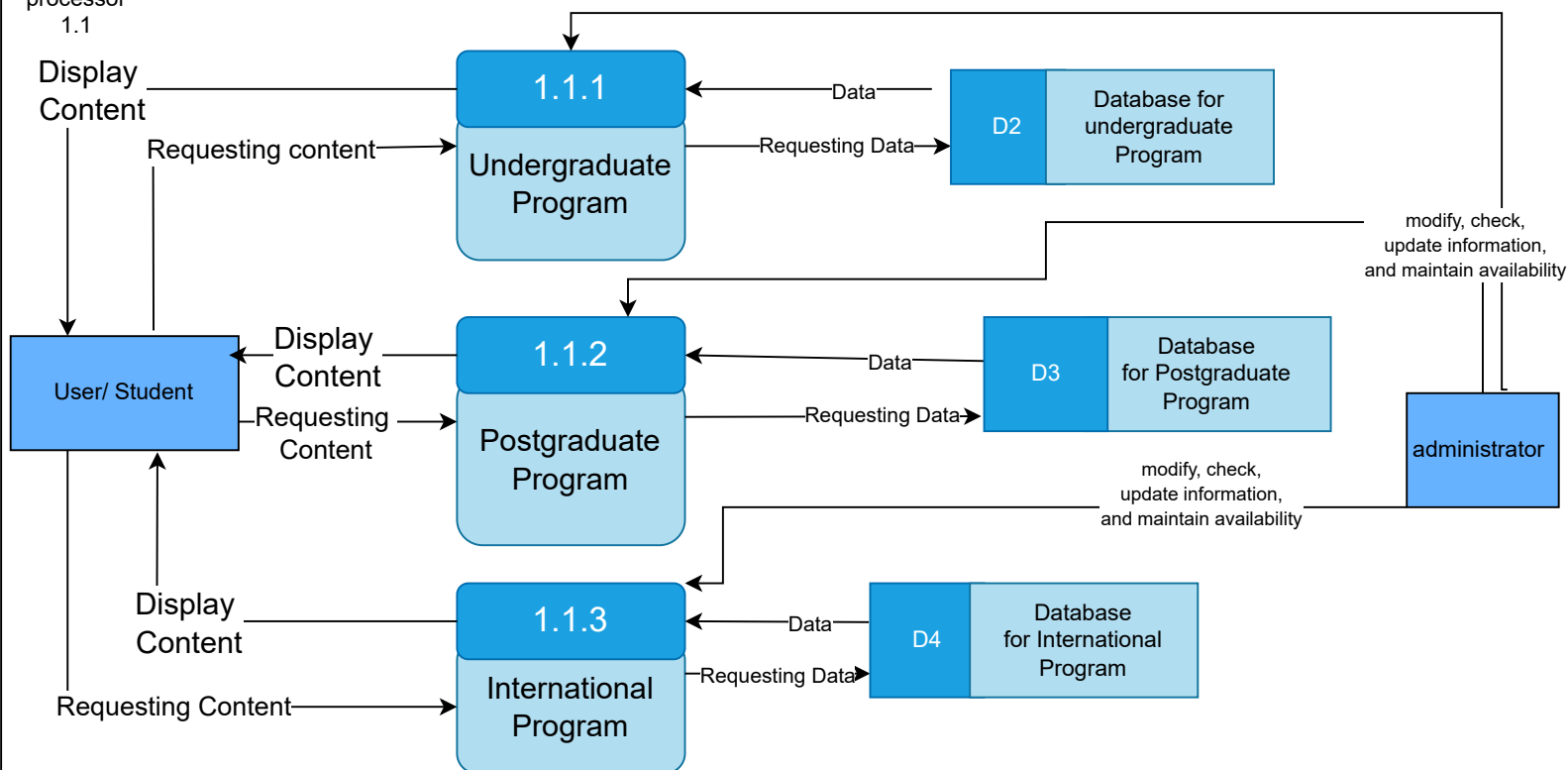
Level 0 DFD



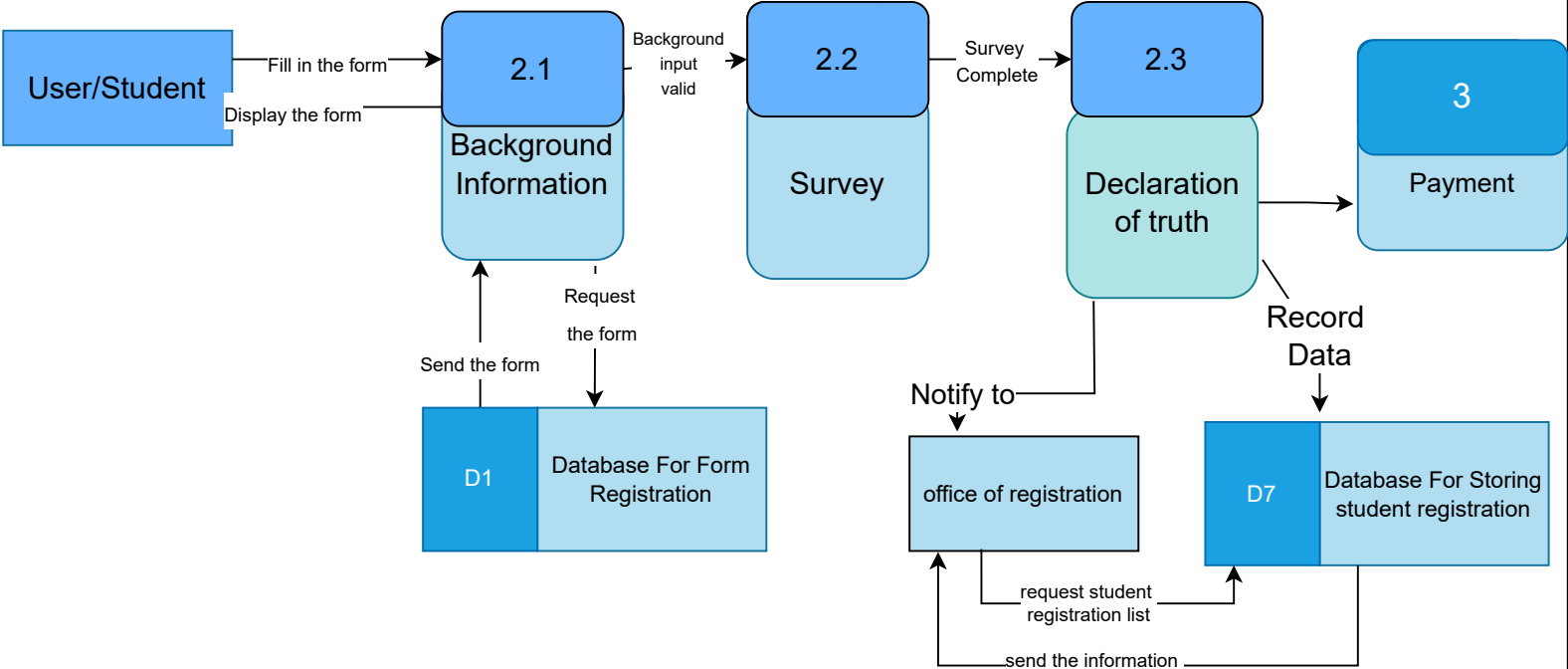
Level 1  
DFD for  
processor  
1



Level 2  
DFD for  
processor  
1.1



Level 1 for  
processor 2



Level 2 for  
processor 2.1

