|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | **-0** | **-1** | **-2** | **-3** | **-4** | **-5** | **-6** | **-7** | **-8** | **-9** | **-A** | **-B** | **-C** | **-D** | **-E** | **-F** |
| **0** | RST |  |  |  |  |  |  |  | Jcc  M0 | Jcc  M1 | Jcc  M2 | Jcc  M3 | Jcc  M4 | Jcc  M5 |  |  |
| **1** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| **2** | RET |  |  |  |  |  |  |  | CALL  M0 | CALL  M1 | CALL  M2 | CALL  M3 | CALL  M4 | CALL  M5 |  |  |
| **3** | RTI |  |  |  |  |  |  |  |  |  |  |  |  |  | INT  imm | INT  reg |
| **4** | ADD  imm | ADD  reg | ADC  imm | ADC  reg |  |  |  | INC  reg | AND  imm | AND  reg | OR  imm | OR  reg | XOR  imm | XOR  reg |  |  |
| **5** | SUB  imm | SUB  reg | SBC  imm | SBC  reg | CMP  imm | CMP  reg |  | DEC  reg | NAND  imm | NAND  reg | NOR  imm | NOR  reg | XNOR  imm | XNOR  reg |  | NOT  reg |
| **6** |  |  |  |  |  |  |  |  |  | LSL  reg |  | ROL  reg |  |  |  | SWP  reg |
| **7** |  |  |  |  |  |  |  |  |  | LSR  reg |  | ROR  reg |  |  |  | SWPL  reg |
| **8** | MOV  M0 | MOV  M1 | MOV  M2 | MOV  M3 | MOV  M4 | MOV  M5 |  | MOV  reg | PHM  M0 | PHM  M1 | PHM  M2 | PHM  M3 | PHM  M4 | PHM  M5 | PHI  imm | PSH  reg |
| **9** | MOVL  M0 | MOVL  M1 | MOVL  M2 | MOVL  M3 | MOVL  M4 | MOVL  M5 |  | MOVL  reg | PHML  M0 | PHML  M1 | PHML  M2 | PHML  M3 | PHML  M4 | PHML  M5 | PHIL  imm | PSHL  reg |
| **A** | MVI  M0 | MVI  M1 | MVI  M2 | MVI  M3 | MVI  M4 | MVI  M5 |  | MVI  reg | PPM  M0 | PPM  M1 | PPM  M2 | PPM  M3 | PPM  M4 | PPM  M5 |  | POP  reg |
| **B** | MVIL  M0 | MVIL  M1 | MVIL  M2 | MVIL  M3 | MVIL  M4 | MVIL  M5 |  | MVIL  reg | PPML  M0 | PPML  M1 | PPML  M2 | PPML  M3 | PPML  M4 | PPML  M5 |  | POPL  reg |
| **C** | MVM  M0 | MVM  M1 | MVM  M2 | MVM  M3 | MVM  M4 | MVM  M5 |  |  |  |  |  |  |  |  |  |  |
| **D** | MVML  M0 | MVML  M1 | MVML  M2 | MVML  M3 | MVML  M4 | MVML  M5 |  |  |  |  |  |  |  |  |  |  |
| **E** | MVM  S0 | MVM  S1 | MVM  S2 | MVM  S3 |  |  |  |  |  |  |  |  |  |  |  | NOP |
| **F** | MVML  S0 | MVML  S1 | MVML  S2 | MVML  S3 |  |  |  |  |  |  |  |  |  |  |  | HLT |

M0 = $(reg16)  
M1 = $(reg32)  
M2 = $(reg16+imm16)  
M3 = $(reg32+imm16)  
M4 = $(reg16+imm32)  
M5 = $(reg32+imm32)

S0 = $(reg16+imm32), $(reg16+imm32)  
S1 = $(reg16+imm32), $(reg32+imm32)  
S2 = $(reg32+imm32), $(reg16+imm32)  
S3 = $(reg32+imm32), $(reg32+imm32)