

Cache Simulator Report

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<https://github.com/auxiliary/CacheSimulator>

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1 Functionalities

Our cache simulator takes a configuration file and a trace file from the command line. It then generates a memory hierarchy based on the configurations and runs the instructions read from the trace file.

1.1 Features

The following features have been implemented in the cache simulator:

- Fully configurable cache hierarchy using YAML
 - Architecture details, level 1 cache and main memory are required
 - Level 2 and level 3 are optional
- Supports write through and write back
- Optionally draws cache layout after simulation
- Writes simulation results to a log file and standard output
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1.2 Assumptions

2 Testing

3 Conclusion and results