Priority	Category	Name	Code	Range	Size	1	Add	fressing	x o	pcode	Instruction M	0 D S	R/M	Reg	Byte 3	Byte 4	Byte 5	Byte 6
	category	Name	Couc	runge	8	Register8	←	Register8			0 0 0 0 0 0 1							
Core				_	٥	Registero	_	Immediate8			0 0 0 0 0 1 0			Dest	Immediate8			
20.0					16	Register16	←	Register16			0 0 0 0 0 0 1			Dest Dest		mediate16		+++++
							+	Immediate16 [Disp8]			0 0 0 0 0 1 1			Dest	Disp8	illiediate10		
					8	Register8	→	[Disp8]			0 0 0 0 0 1 1			Source				
Low				Zero	16	Register16	+	[Disp8]			0 0 0 0 0 1 1			Dest	Disp8			
				Page		_	→	[Disp8]			0 0 0 0 0 1 1			Source	Disp8	Immediate 0		
Low					8 16	Immediate8 Immediate16	\rightarrow	[Disp8] [Disp8]			0 0 0 0 1 1 1 0 0 0 0 1 1 1				Disp8 Disp8	Immediate8	diate16	
						mmediate20	ŕ	[Disp16]			0 0 0 0 0 1 0			Dest		Disp16		
Core					8	Register8	←	[Reg16]			0 0 0 0 0 0 0			Dest				
					_		,				0 0 0 0 0 0			Dest Dest	Disp8	Diam16		
				Near				[Reg16 + Disp16] [Disp16]			0 0 0 0 0 0 1 0 0 0 0 0 1 0			Dest		Disp16 Disp16	 	
					4.0	Dariet and C	,	[Reg16]			0 0 0 0 0 0 0			Dest				
Core		Move	MOV		16	Register16	←		M	0011	0 0 0 0 0 0 0	1 0 1		Dest	Disp8			
								[Reg16 + Disp16]			0 0 0 0 0 0 1			Dest		Disp16	1	
								[Disp16] [Reg16]			0 0 0 0 1 1 0 0 0 0 0 1 0 0				Immediate8	Disp16	Immediate8	
Med						Immediate8	\rightarrow	[Reg16 + Disp8]			0 0 0 0 1 0 0				Disp8	Immediate8		
					8			[Reg16 + Disp16]			0 0 0 0 1 0 1					Disp16	Immediate8	
								[Disp16]			0 0 0 0 0 1 0 0 0 0 0 0 0 0			Source		Disp16		
Core						Register8	\rightarrow	[Reg16] [Reg16 + Disp8]			0 0 0 0 0 0 0			Source	Disp8			
				Near				[Reg16 + Disp16]			0 0 0 0 0 0 1			Source		Disp16		
				14001				[Disp16]			0 0 0 0 1 1 0					Disp16	Immedia	te16
Med						Immediate16	\rightarrow	[Reg16] [Reg16 + Disp8]			0 0 0 0 1 0 0 0 0 0 0 1 0 0				Disp8	mediate16	diate16	
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					16			[Disp16]	M	04 0 3	0 0 0 0 0 1 0	0 1 1		Source		Disp16		
Core						Register16	\rightarrow	[Reg16]			0 0 0 0 0 0 0			Source				
								[Reg16 + Disp8] [Reg16 + Disp16]			0 0 0 0 0 0 0 0 0 0 0 0 0 0 1			Source Source		Disp16	 	
			LDRF	_	8	Flags8	+	Register8	N C	C10	0 0 0 0 1 1 0	1 0 0		Source				
Low		Move Flags	LUKE		16	Flags16	+	Register16	N C	C 1 1	0 0 0 0 1 1 0	1 0 1		Source				
	Data Transfer		STRF	-	8 16	Flags8 Flags16	→ →	Register8			0 0 0 0 1 1 0 0 0 0 0 1 1 0			Dest Dest		++++++		++++++
	Sata Hulisjer	P l.	ve		8	Register8	\rightarrow \leftrightarrow	Register16 Register8			0 0 0 0 1 1 0					++++++	 	
Med		Exchange	XCHG	-	16	Register16	\leftrightarrow	Register16	N C	0033	0 0 0 0 0 0 1	1 1 1	Source	Dest				
					8			Register8	H 2	2032	0 0 1 0 0 0 1			Source				
				-		-		Immediate8			0 0 0 1 0 1 0 0 0 1 0 0 0 1		Immedia	ste8 Source		++++++		++++++
					16			Register16 Immediate16			0 0 1 0 0 0 1				i diate16			
				Zero	8			[Disp8]			0 0 0 1 1 0 0		Disp8					
				Page	16			[Disp8]			0 0 0 1 1 0 0		Disp8					
Core		Push	PUSH			Stack16	←	[Disp16]			0 0 0 1 1 0 1 0 0 1 0 0 0 0		Base	Source	p16			
Core		rusii			8			[Reg16] [Reg16 + Disp8]			0 0 1 0 0 0 0			Source				
				Near							0 0 1 0 0 0 1			Source		Disp16		
				iveai				[Disp16]			0 0 0 1 1 0 1				p16			
					16			[Reg16]			0 0 1 0 0 0 0 0 0 1 0 0 0 0			Source Source			 	
											0 0 1 0 0 0 0			Source		Disp16		
			PUSHF	-	16	Stack16	+	Flags16			0 0 0 1 0 1 1							
				_	8			Register8			0 0 1 0 0 0 1			Dest				
				Zero	16 8	-		Register16 [Disp8]			0 0 1 0 0 0 1 0 0 0 1 1 0		Disp8	Dest				
				Page	16			[Disp8]			0 0 0 1 1 1 0		Disp8					
								[Disp16]			0 0 0 1 1 1 1				p16			
6			POP		8	Stack16	\rightarrow	[Reg16]			0 0 1 0 0 0 0			Source				
Core		Pop						[Reg16 + Disp8] [Reg16 + Disp16]			0 0 1 0 0 0 0 0 0 1 0 0 0 1			Source Source		Disp16		
				Near				[Disp16]			0 0 0 1 1 1 1		Dusc		: p16	DISP10		
					16			[Reg16]	M 2	2001	0 0 1 0 0 0 0			Source				
					10			[Reg16 + Disp8]			0 0 1 0 0 0 0			Source		Bi16		
			POPF	_	16	Stack16	→	[Reg16 + Disp16] Flags16			0 0 1 0 0 0 1 0 0 0 1 0 1 1			Source		Disp16	 	
High	-	In	IN	Near	8	Register8	É	Port(Disp8)			1 1 0 1 1 1 0		Disp8	3				
riigii			IIV	iveai	16	Register16	+	Port(Disp8)			1 1 0 1 1 1 1		Disp8					
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		Land Effective		Short	8	Register8	←	Reg8 + Disp8			0 0 0 0 1 0 1			Dest	Disp8			
Low		Load Effective Address	LEA	Near	16	Register16	<u>+</u>	Reg16 + Disp8	L C	0810	0 0 0 0 1 0 0	1 0 0	Base	Dest	Disp8			
		Address		IVCai	10	Register10	`	Reg16 + Disp16	LC	0821	0 0 0 0 1 0 1	0 0 1	Base	Dest		Disp16		
Low		Sign Extend	CBW	-	8		Re	gister8	N 2	24 3 0	0 0 1 0 0 1 1	1 0 0		Reg				
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riigii				_	•	Register8	+>	[Disp8]			1 0 0 0 0 1 1			Source				
Med				Zero Page	16	Register16	← +	[Disp8] [Disp8]			1 0 0 0 0 1 1 1 0 0 0 0 1 1			Dest Source	Disp8 Disp8			++++++
				. age	8	Immediate8	+→	[Disp8]			1 0 0 0 0 1 1			Source	Disp8	Immediate8		
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			1					Immediate8			1 0 0 0 0 1 0			Dest Dest	Immediate8			+++++
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Core						Register8	(+	[Disp16] [Reg16]			1 0 0 0 0 0 0	0 0 0	Base	Dest				
Core						Register8	-+	[Reg16] [Reg16 + Disp8]	M 8	3010	1 0 0 0 0 0 0 1 0 0 0 0 0 0	0 0 0 1 0 0	Base Base	Dest	Disp8	Direct 6		
Core						Register8	← +	[Reg16] [Reg16 + Disp8] [Reg16 + Disp16]	M 8	30 1 0 30 2 0	1 0 0 0 0 0 0 0 1 0 0 0 0 0 0 0 1 0 0 0 0	0 0 0 1 0 0 0 0 0	Base Base Base	Dest Dest	Disp8	Disp16		
Core					8			[Reg16] [Reg16 + Disp8]	M 8 M 8	30 1 0 30 2 0 34 0 2	1 0 0 0 0 0 0 1 0 0 0 0 0 0	0 0 0 1 0 0 0 0 0	Base Base Base	Dest		Disp16 Disp16		
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					8			[Reg16] [Reg16 + Disp8] [Reg16 + Disp16] [Disp16] [Reg16] [Reg16] [Reg16 + Disp8] [Reg16 + Disp16]	M 8 M 8 M 8 M 8 M 8	30 1 0 30 2 0 34 0 2 30 0 2 30 1 2	1 0 0 0 0 0 0 0 1 1 0 0 0 0 1 1 0 0 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Base Base Base Base Base Base Base Base	Dest Dest Source Source	Disp8	Disp16 Disp16	Immediate ²	
		Add	ADD		8	Register8	+>	[Reg16] [Reg16 + Disp8] [Reg16 + Disp16] [Disp16] [Reg16] [Reg16]	M 8 M 8 M 8 M 8 L 8	30 1 0 30 2 0 34 0 2 30 0 2 30 1 2 30 2 2 3C 0 2	1 0 0 0 0 0 0 0 1 1 0 0 0 0 1 0 1 0 1 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Base Base Base Base Base Base Base Base	Dest Dest Source Source Source	Disp8	Disp16	Immediate8	
		Add	ADD		8			[Reg16] [Reg16 + Disp18] [Reg16 + Disp16] [Disp16] [Reg16] [Reg16 + Disp8] [Reg16 + Disp16] [Disp16] [Reg16] [Reg16] [Reg16] [Reg16]	M 8 M 8 M 8 M 8 L 8 L 8 L 8	80 1 0 80 2 0 84 0 2 80 0 2 80 1 2 80 2 2 8C 0 2 88 0 2	1 0 0 0 0 0 0 1 1 0 0 0 0 0 0 1 0 0 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Base Base Base Base Base Base Base Base	Dest Dest Source Source Source	Disp8	Disp16 Disp16 Disp16 Immediate8		
		Add	ADD	Near	8	Register8	+>	[Reg16] [Reg16 + Disp18] [Reg16 + Disp16] [Disp16] [Reg16] [Reg16] [Reg16 + Disp18] [Reg16 + Disp16] [Reg16] [Reg16] [Reg16] [Reg16] [Reg16] [Reg16 + Disp16]	M 8 8 M 8 8 M 8 M 8 L 8 8 L 8 8 L 8 8 L 8 8 L 8 8 L 8 8 L 8 8 L 8 8 M 8 M	30 1 0 30 2 0 34 0 2 30 0 2 30 1 2 30 2 2 3C 0 2 38 0 2 38 1 2	1 0 0 0 0 0 0 0 1 1 0 0 0 0 0 0 1 0 0 0 0 0 1 1 0 0 0 0 1 0 1 0 0 0 1 0	0 0 0 0 0 0 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0	Base Base Base Base Base Base Base Base	Dest Dest Source Source Source	Disp8 Immediate8 Disp8	Disp16 Disp16 Disp16 Disp16 Disp16 Immediate8	Immediate8	
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Med High		Rotate Left Rotate Right	ROL	-	16 8	Reg Regi Reg	[Reg16 + Disp8] L [Reg16 + Disp16] L ister8, Immediate3 N ster16, Immediate3 N ister8, Immediate3 N	B8 2 3 24 0 0 24 0 1 24 0 2	0 1 1 1 0 1 0 1 1 Base	Disp16	Immediate16
Med High Med		Rotate Right	ROR		16 8 16	Reg Regi Reg	[Reg16 + Disp16] L [Reg16 + Disp16] L ister8, Immediate3 N ster16, Immediate3 N ster16, Immediate3 N	B8 2 3 24 0 0 24 0 1 24 0 1 24 0 2 1 24 0 3	0 1 0 0 1 0 1 0 1 0 8ase	Disp16	Immediate16
Med High Med Core		Rotate Right Rotate Left			16 8 16 8	Reg Regi Reg Regi Reg	[Reg16 + Disp18] L [Reg16 + Disp16] L ister8, Immediate3 N ster16, Immediate3 N ster16, Immediate3 N ster16, Immediate3 N ster16, Immediate3 N	B8 2 3 24 0 0 24 0 1 24 0 2 24 0 3 1 24 1 0	0 1 1 1 0 1 0 1 0 1 1 1 Base	Disp16	Immediate16
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Med High Med Core Med High		Rotate Right Rotate Left through Carry Rotate Right through Carry	ROR RCL RCR	-	16 8 16 8 16	Reg Regi Reg Regi Regi Regi Reg	[Reg16 + Disp8] L [Reg16 + Disp16] L ister8, Immediate3 N ster16, Immediate3 N ster16, Immediate3 N ster16, Immediate3 N ster16, Immediate3 N ster8, Immediate3 N ster8, Immediate3 N ster8, Immediate3 N	B8 2 3 24 0 0 24 0 1 24 0 2 24 0 3 24 1 0 24 1 1 24 1 1 24 1 2 24 1 3	0 1 0 0 1 0 1 0 1 0 1 1 888	Disp16	Immediate16
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