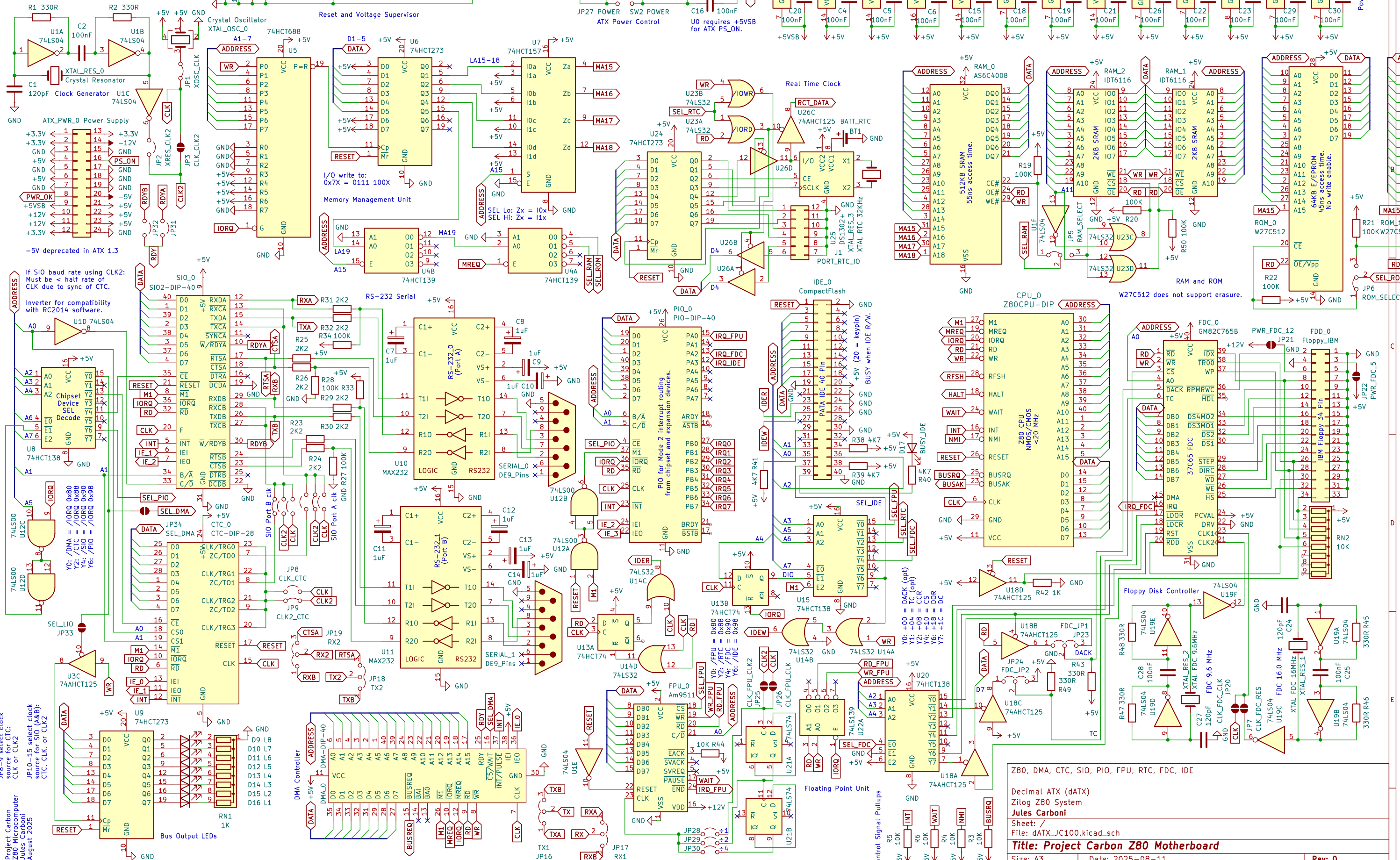


Recommended CLK1:
1.00 – 8.00 MHz (safe).
100 kHz (min), 4.00 MHz (best*), 20.0 MHz (max).

Recommended CLK2:
1.8432 MHz (safe), 7.3728 MHz (best**).

*For speeds >4 MHz, Am9511 must divide clock.
**Only use strictly if CLK is CLK2 (JP3 closed).



Z80, DMA, CTC, SIO, PIO, FPU, RTC, FDC, IDE

Decimal ATX (dATX)
Zilog Z80 System
Jules Carboni
Sheet: /
File: dATX_JC100.kicad_sch

Title: Project Carbon Z80 Motherboard

Size: A3 Date: 2025-08-11
KICad E.D.A. 9.0.3

Rev: 0
Id: 1/1