

Recommended CLK:
1.00 – 8.00 MHz (safe).
100 kHz (min), 4.00 MHz (best*), 20.0 MHz (max).

Recommended CLK2:
1.8432 MHz (safe), 7.3728 MHz (best**).

*For speeds >4 MHz, Am9511 must divide clock.
**Only use strictly if CLK is CLK2 (JP3 closed).

