

cuNumeric.jl : Automating Distributed Numerical Computing

David Krasowska¹, Ethan Meitz², Wonchan Lee³

¹Northwestern University

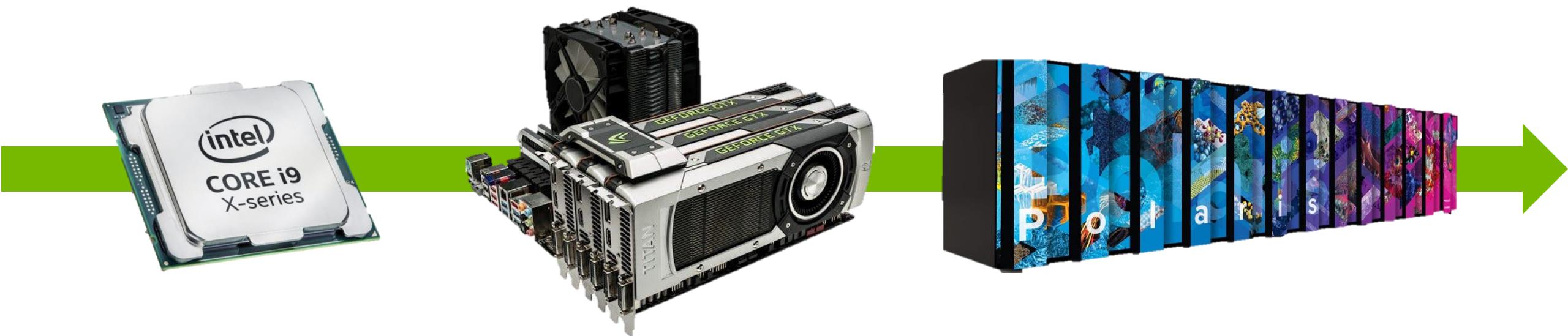
²Carnegie Mellon University

³NVIDIA



The Goal: Scale with Zero Code Changes

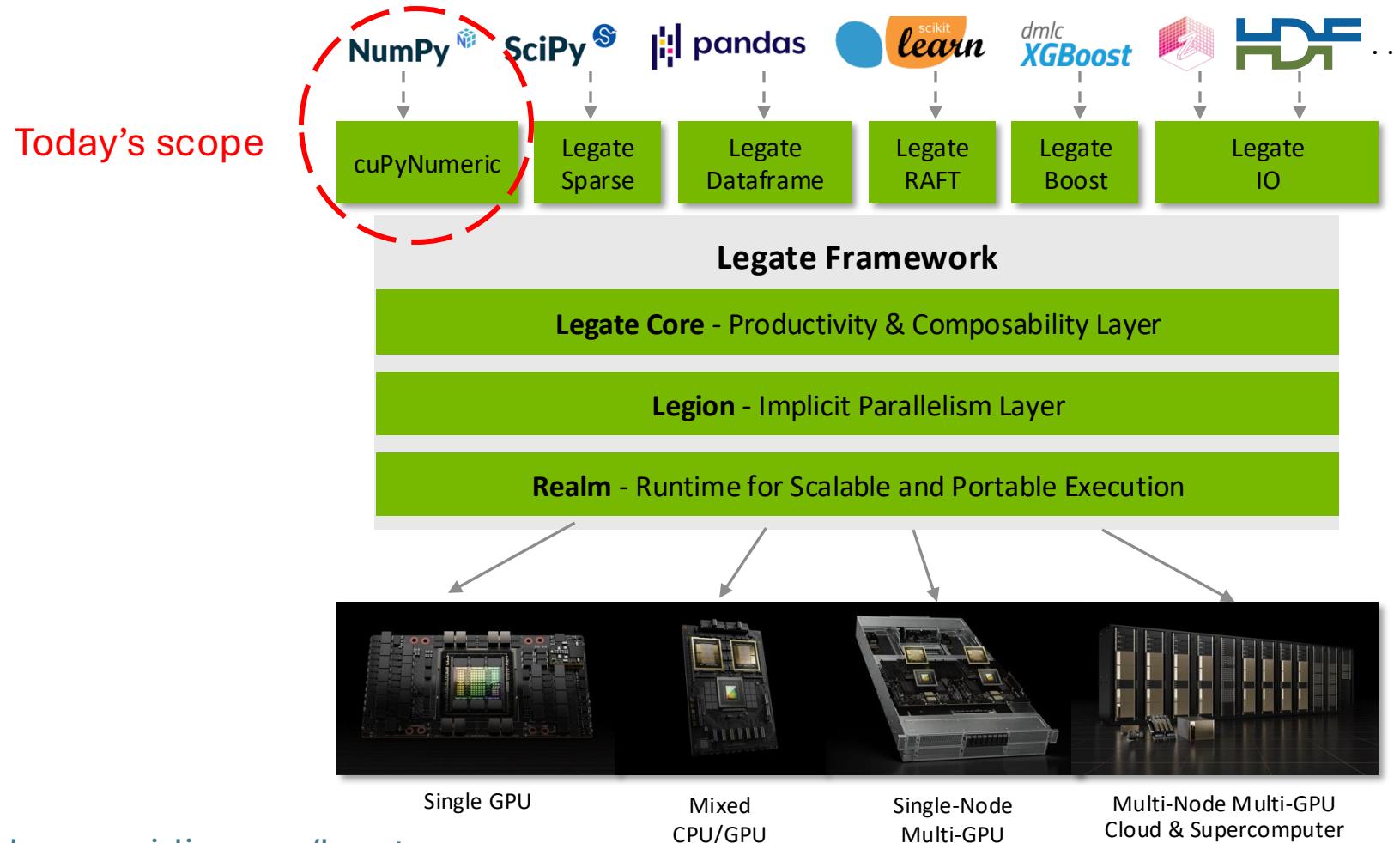
- “Easy” to implement the correct physics in a high-level language like Python, Julia, or MATLAB
- Time consuming to modify code to scale across multiple CPUs/GPUs
 - Need to learn and debug new technologies like MPI, CUDA etc.



Code that runs on a single CPU core should also be able to run on multiple cores, multiple GPUs and across multiple nodes

Legate Enables Composable and Distributed Libraries

By providing data and task management abstractions, this enables the efficient implementation of complex library APIs.

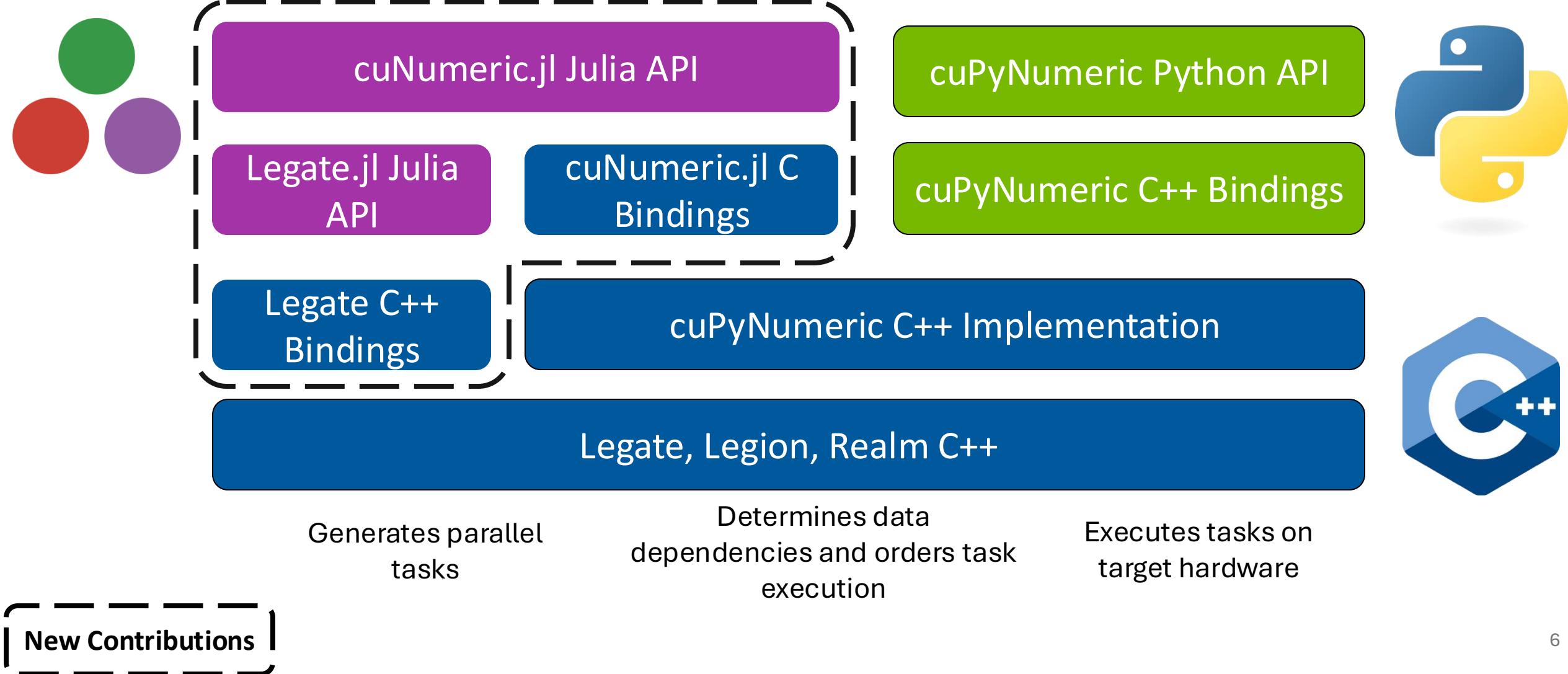


Wouldn't this be great in Julia?

<https://developer.nvidia.com/legate>

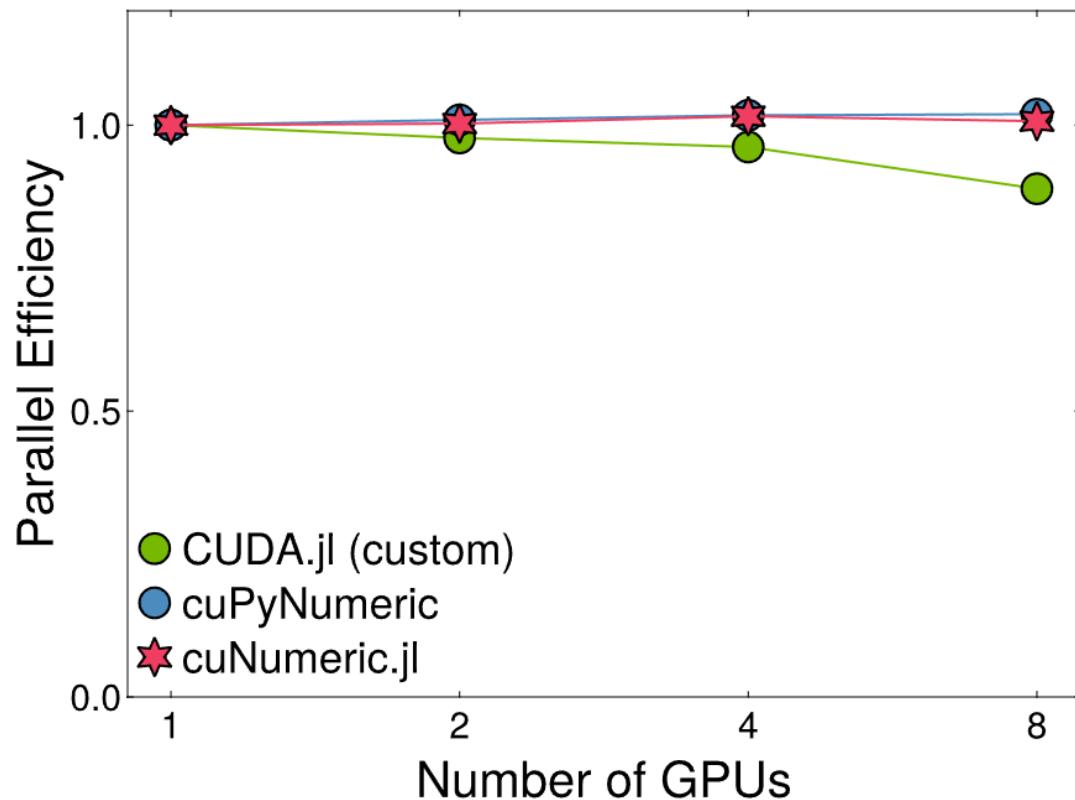
<https://legion.stanford.edu/>

Software Stack



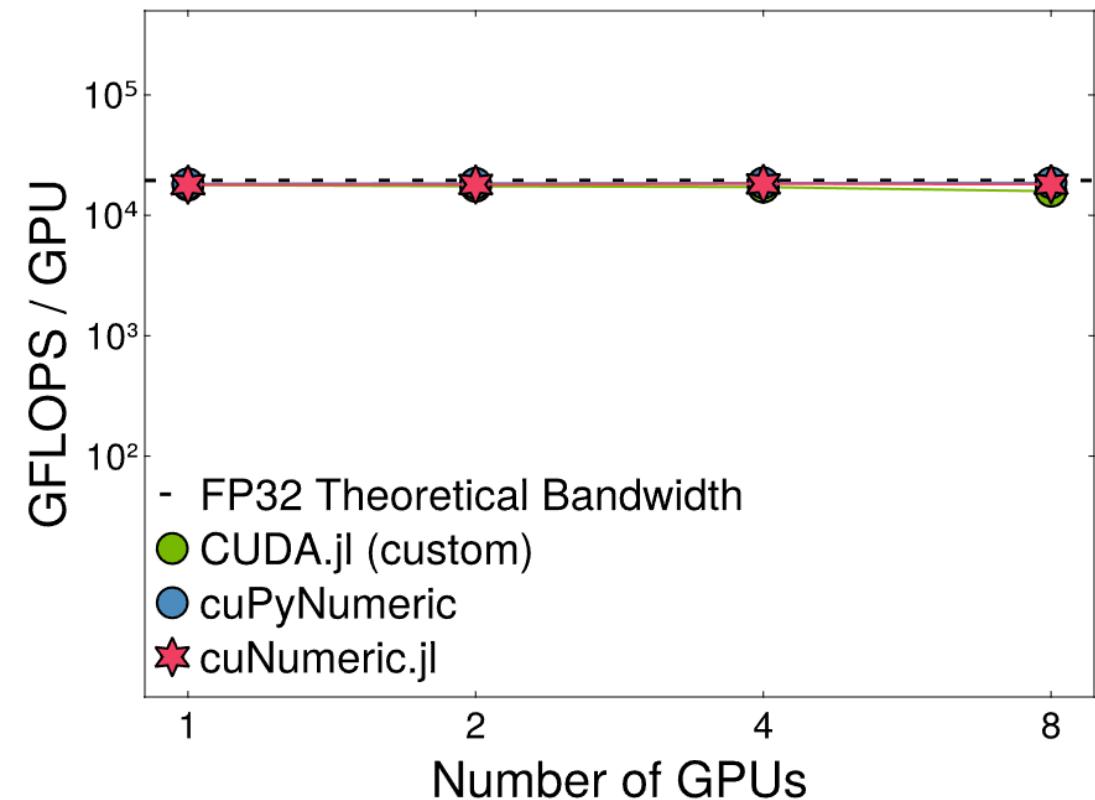
Matrix Multiplication

Benchmark (8x A100):



Syntax:

```
1 N = 10
2 A = cuNumeric.rand(Float32, N, N)
3 B = cuNumeric.rand(Float32, N, N)
4 C = cuNumeric.zeros(Float32, N, N)
5 mul!(C, A, B)
```



Running CUDA Kernels with cuNumeric.jl

```
1  using cuNumeric
2  using CUDA
3
4  function kernel_add(a, b, c, N)
5      i = (blockIdx().x - 1i32) * blockDim().x + threadIdx().x
6      if i <= N
7          @inbounds c[i] = a[i] + b[i]
8      end
9      return nothing
10 end
11
12
13 N = 1024
14 threads = 256
15 blocks = cld(N, threads)
16
17 a = cuNumeric.full(N, 1.0f0)
18 b = cuNumeric.full(N, 2.0f0)
19 c = cuNumeric.ones(Float32, N)
20
21 task = cuNumeric.@cuda_task kernel_add(a, b, c, UInt32(1))
22
23 cuNumeric.@launch task=task threads=threads blocks=blocks \
24 | | | | inputs=(a, b) outputs=c scalars=UInt32(N)
25
```

The code is annotated with four green curly braces on the right side, each grouping a set of code lines and aligned with text labels:

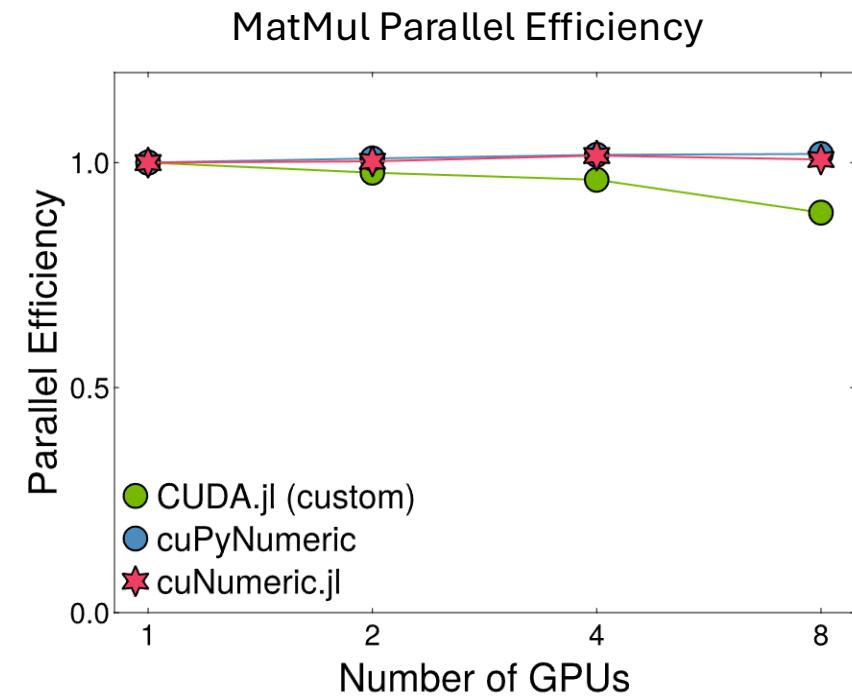
- A brace groups lines 4-10, labeled "CUDA.jl kernel".
- A brace groups lines 13-19, labeled "Initialize NDArrays".
- A brace groups lines 21-22, labeled "Compile".
- A brace groups lines 23-24, labeled "Launch".

Conclusions and Future Work

- Minimal code changes for scaling across large heterogeneous distributed systems
- Good weak scaling efficiency
- Ability to register custom CUDA kernels

Disclaimers:

1. Still in development, we will be releasing a public beta within the coming months
2. cuPyNumeric and Legate are currently in beta



David Krasowska krasow@u.northwestern.edu

Ethan Meitz emeitz@andrew.cmu.edu

Wonchan Lee wonchanl@nvidia.com

Check out our repo

<https://github.com/JuliaLegate/cuNumeric.jl/>

