

BBB
Headers.SchDoc

USB_D+
USB_D-
I2C2_SCL
I2C2_SDA
SYS_RESETN
ARB
GPIO_INT
KEYS_INT
Wiegand
CLK_OUT

UART_INTA
UART_INTB

I2S

SPI0

Power
Power.SchDoc

12V_EN

IO
IOExpanders.SchDoc

USB_D+
USB_D-
SCL
SDA
RESET_N
ARB
GPIO_INT
KEYS_INT

Serial buses
Serial.SchDoc

SCL
SDA
RESET_N
EXT_CLK
Wiegand
UART_INTA
UART_INTB

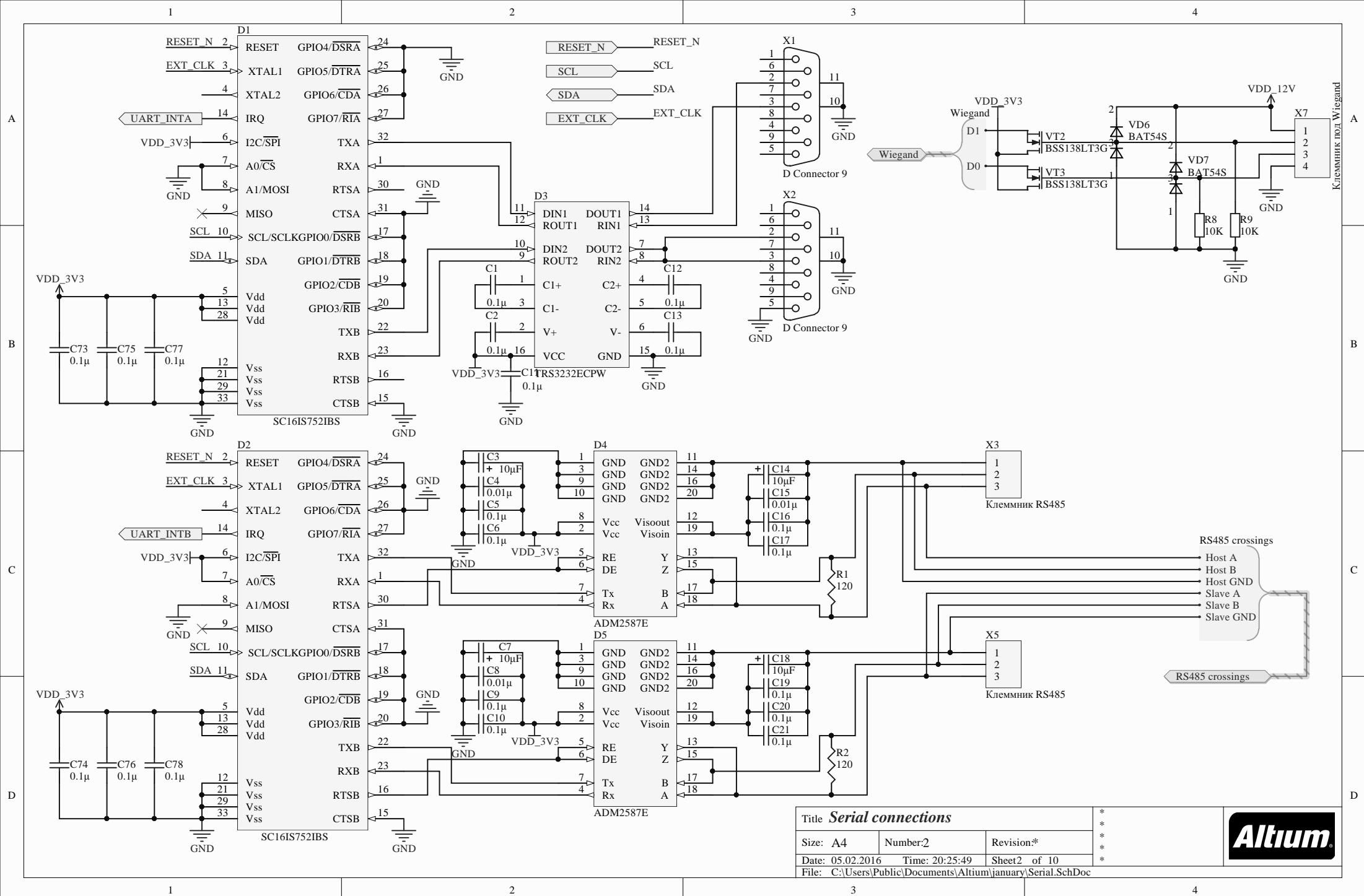
Audio
Audio.SchDoc

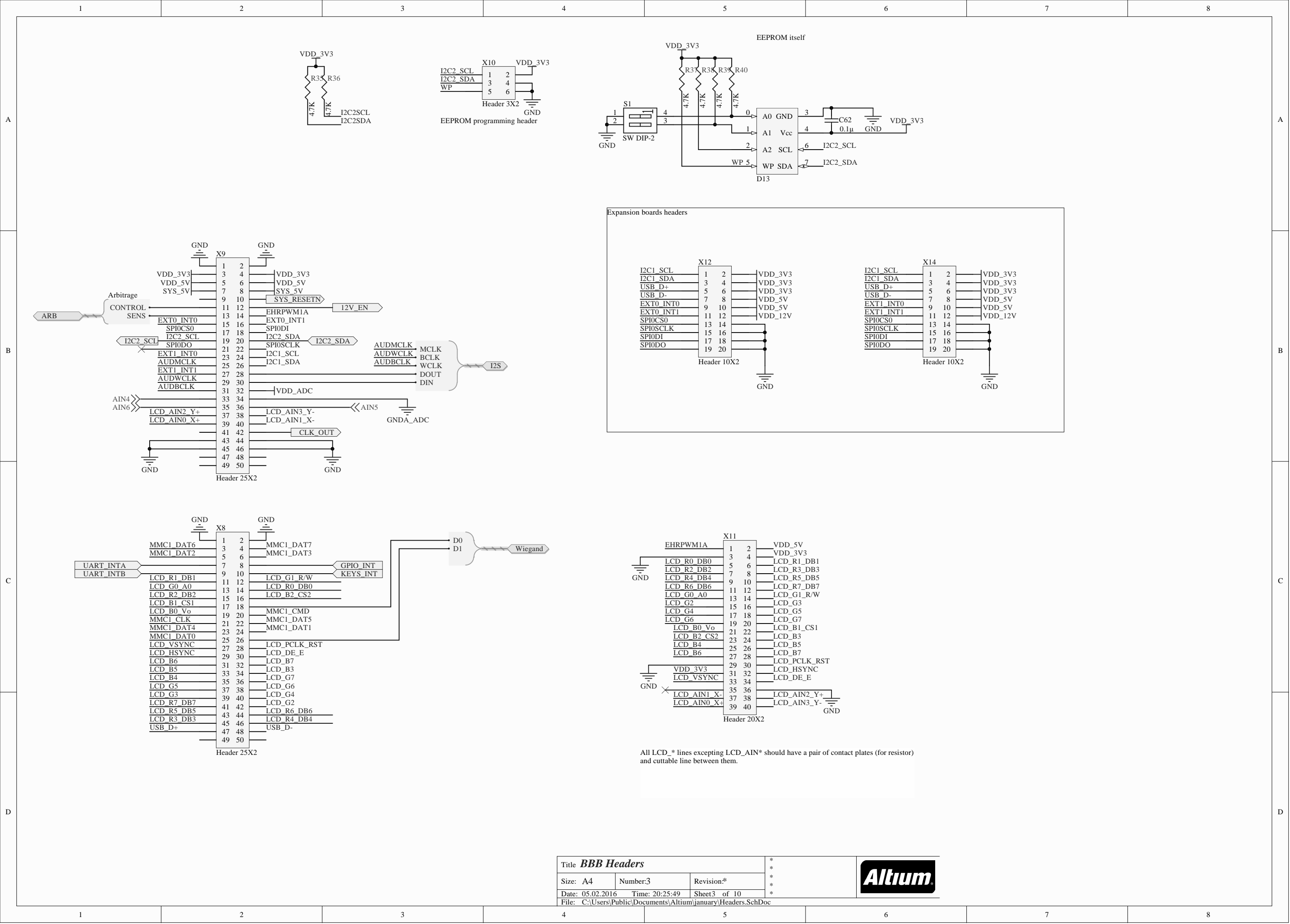
SCL
SDA
RESET_N
I2S

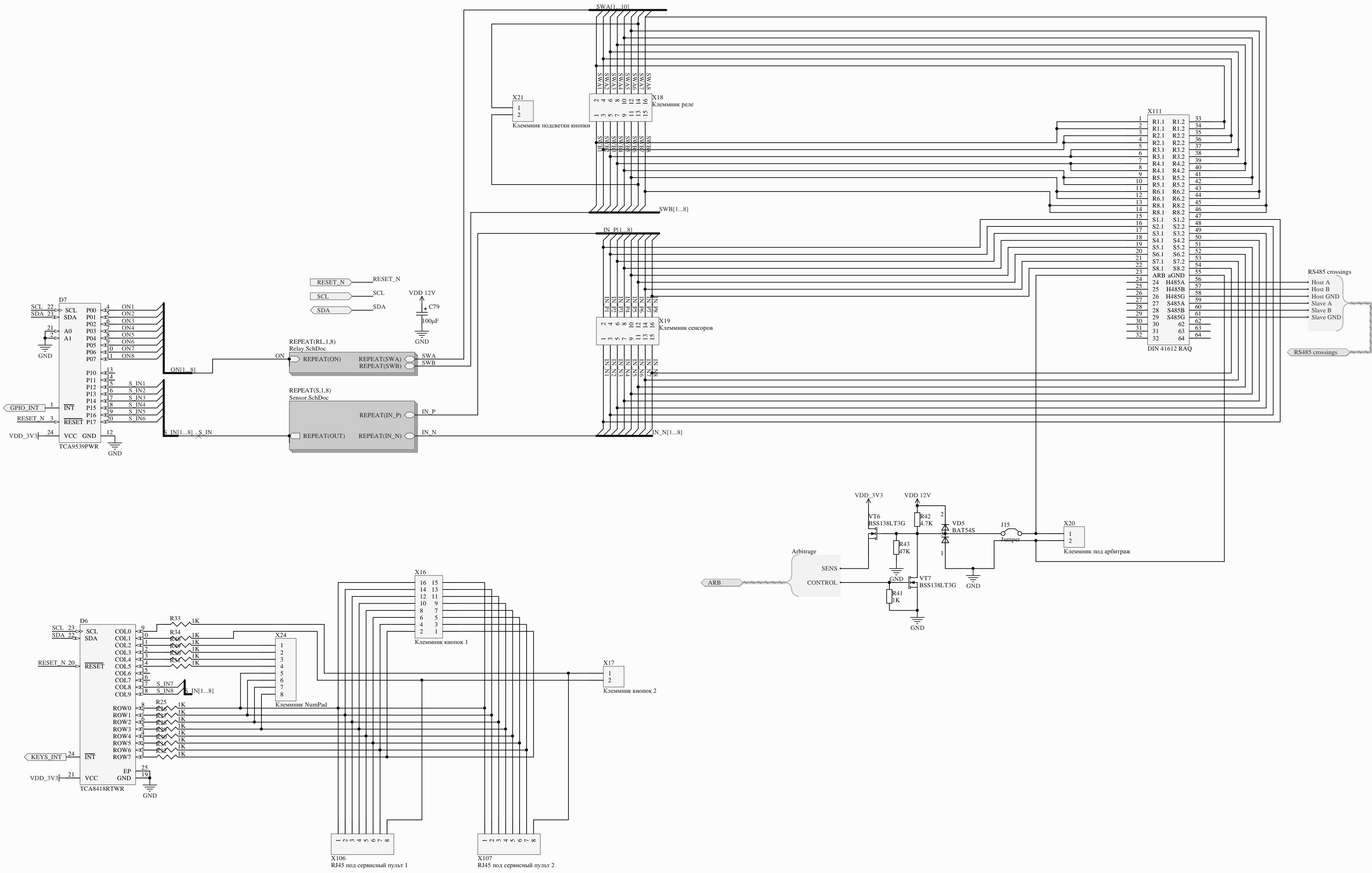
RTC
RTC.SchDoc

SCL
SDA

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File:	C:\Users\...\Toplevel.SchDoc	Drawn By:







A

B

C

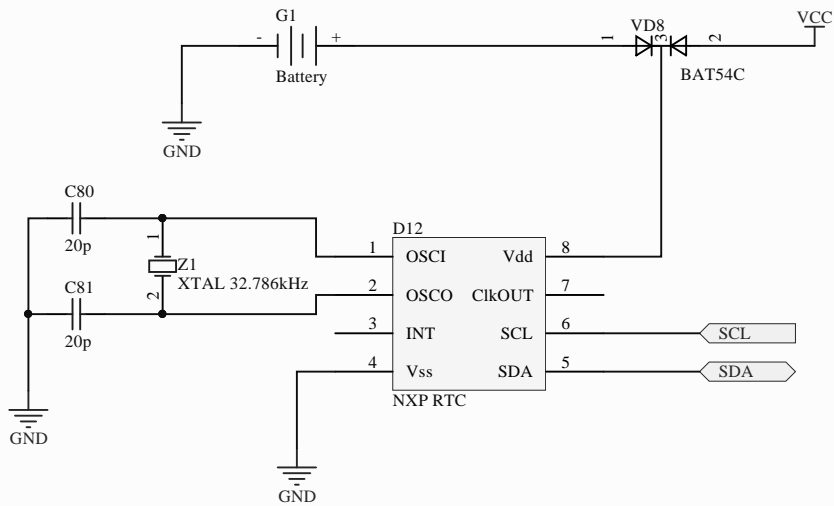
D

A

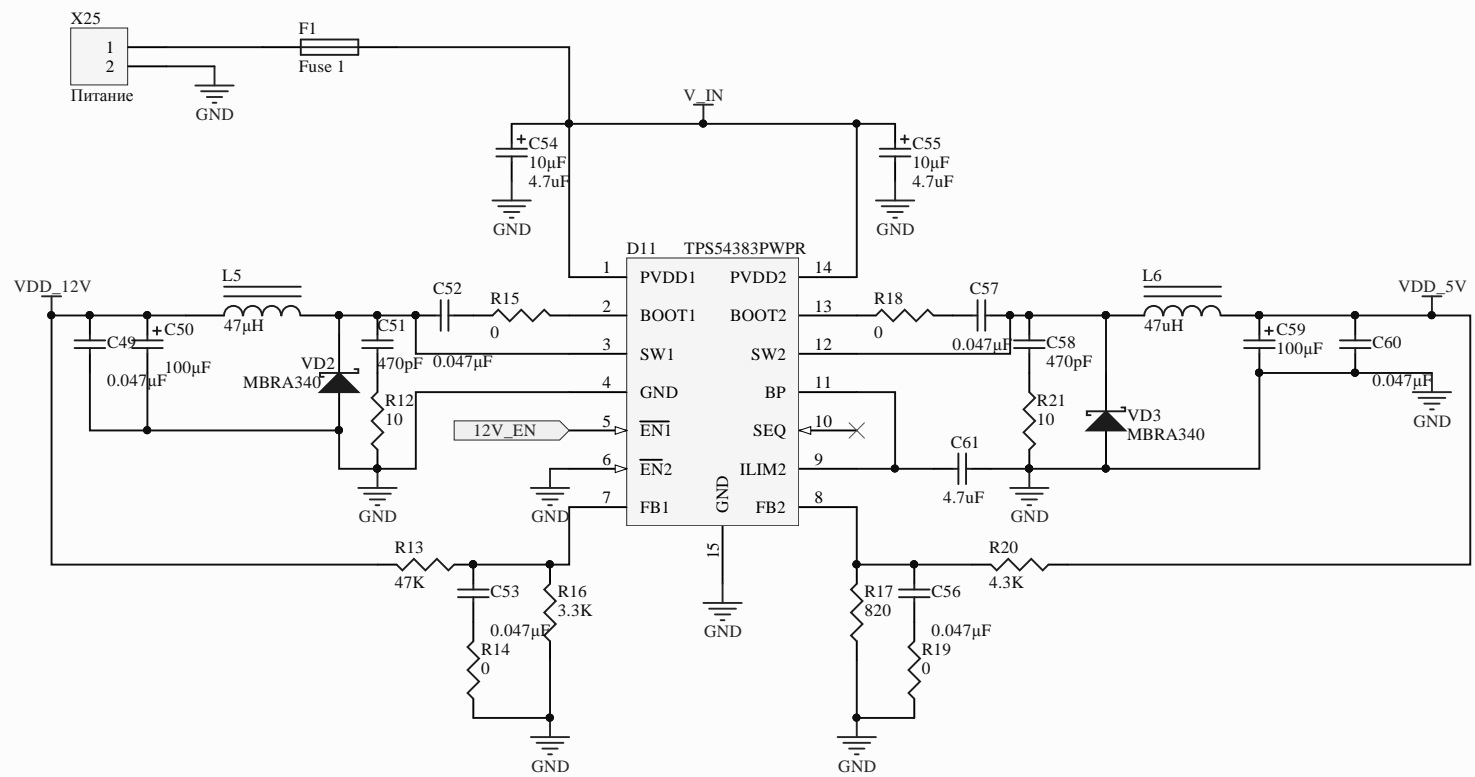
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C

D



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Title		
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A

B

C

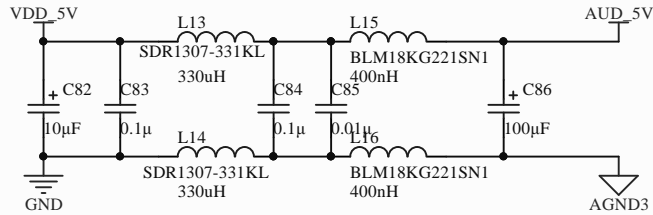
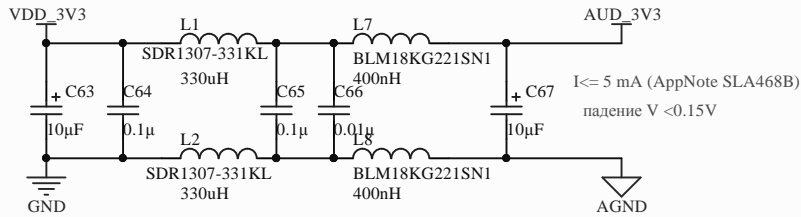
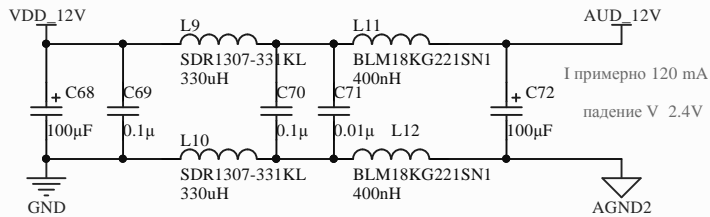
D

A

B

C

D



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1

2

3

4

A

A

B

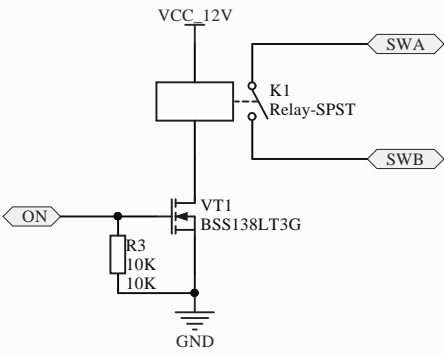
B

C

C

D

D



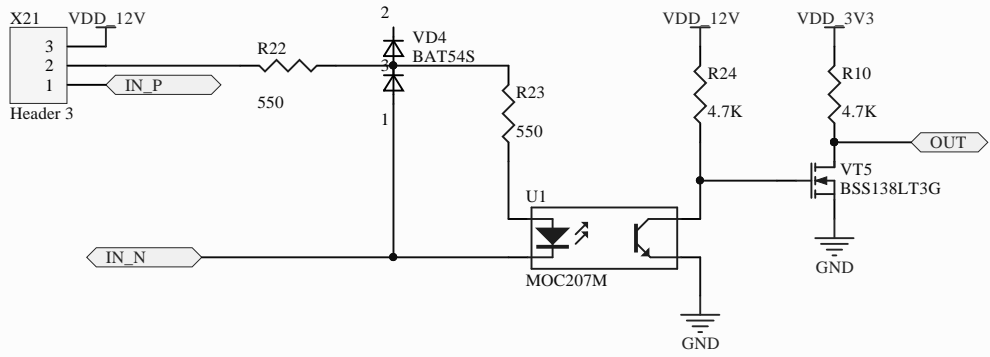
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1

2

3

4



Title		
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File:	C:\Users\...\Sensor.SchDoc	Drawn By: