

BBB
Headers.SchDoc

USB_D+
USB_D-
I2C2_SCL
I2C2_SDA
SYS_RESETN
ARB
GPIO_INT
KEYS_INT

Wiegand
CLK_OUT

UART_INTA
UART_INTB

I2S

SPI0

Power
Power.SchDoc

12V_EN

IO
IOExpanders.SchDoc

USB_D+
USB_D-
SCL
SDA
RESET_N
ARB
GPIO_INT
KEYS_INT

Serial buses
Serial.SchDoc

SCL
SDA
RESET_N
EXT_CLK
Wiegand

UART_INTA
UART_INTB

Audio
Audio.SchDoc

SCL
SDA
RESET_N
I2S

RTC
RTC.SchDoc

SCL
SDA

Title

Size
A

Number

Revision

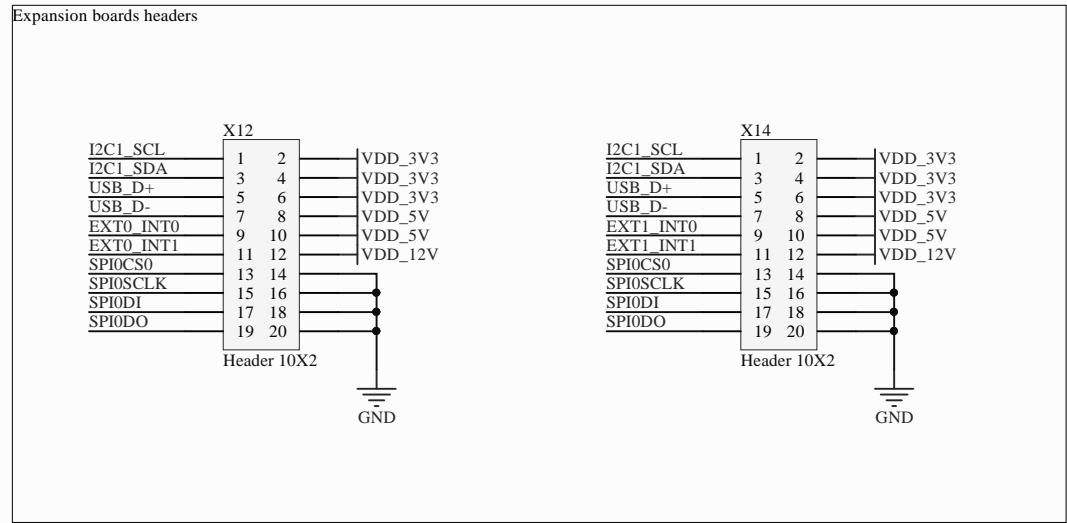
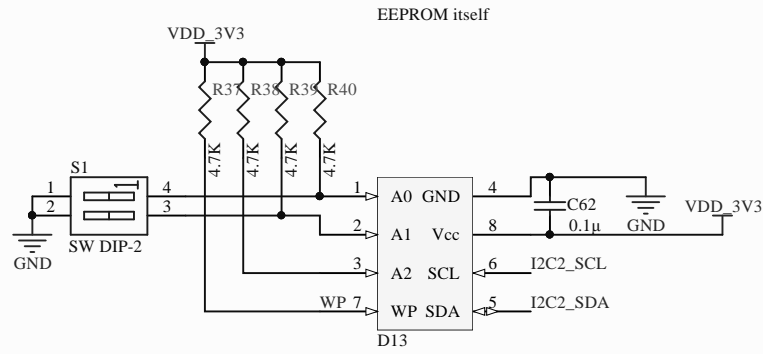
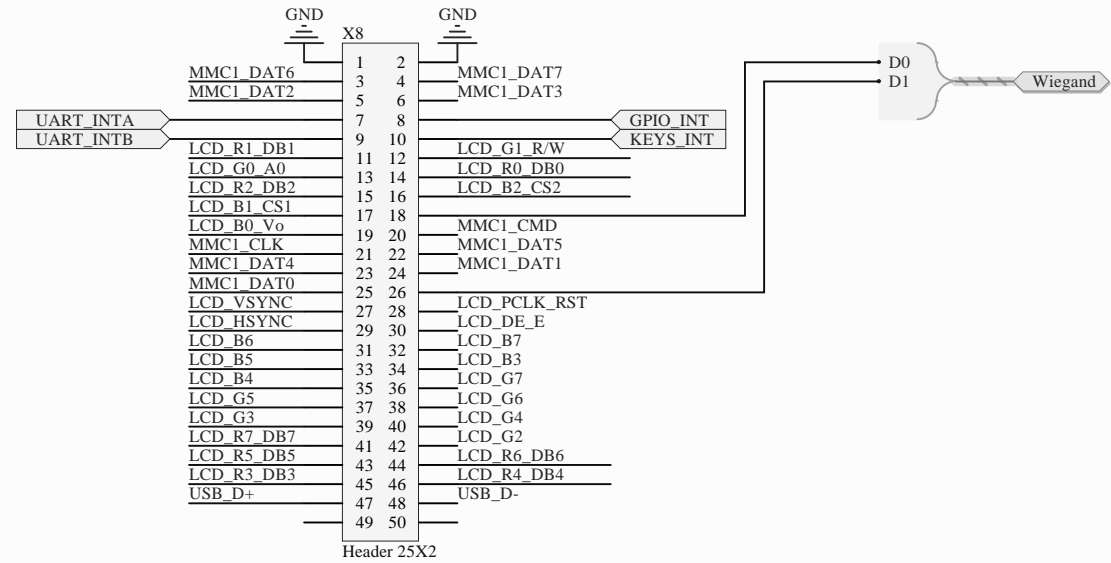
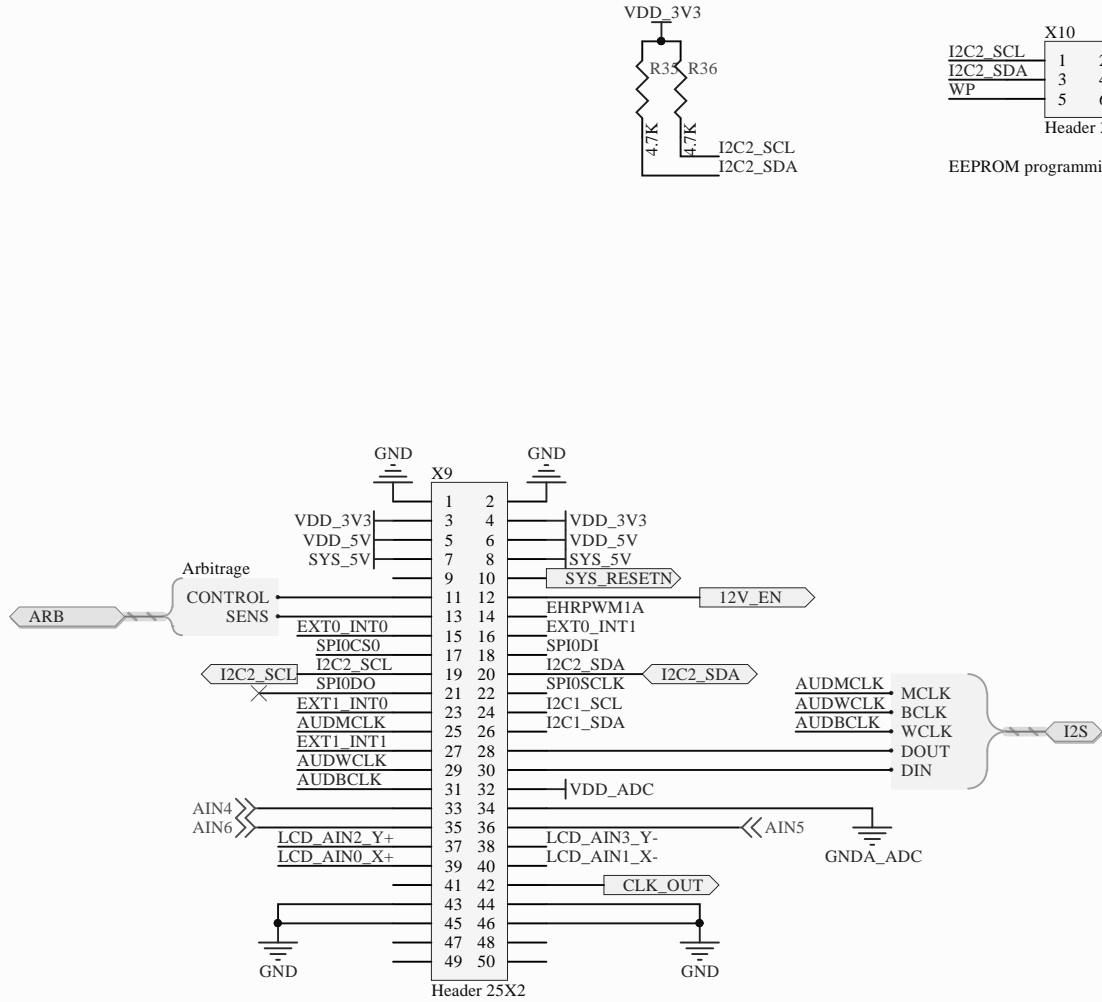
Date: 07.02.2016

Sheet of

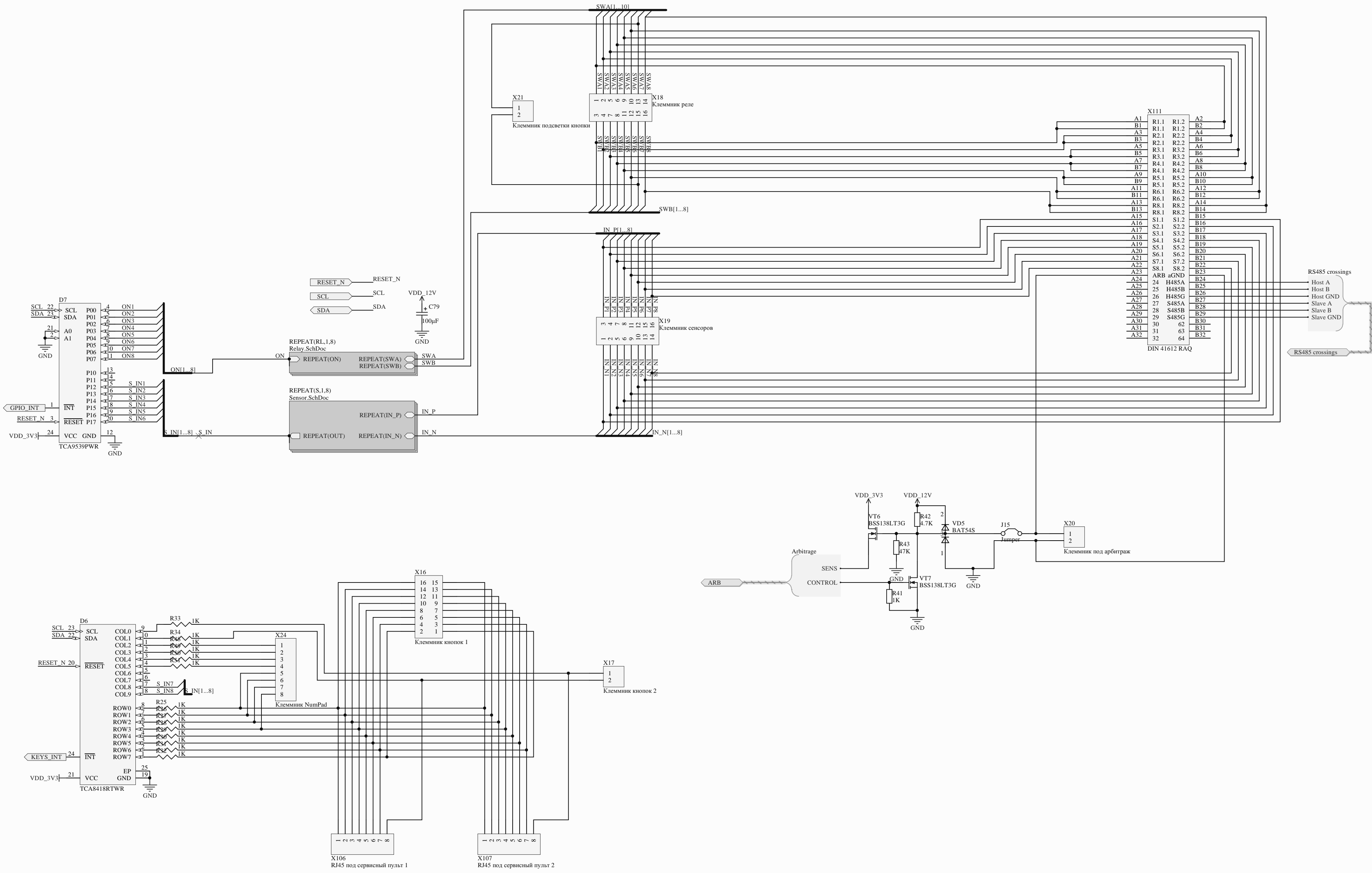
File: C:\Users\...\Toplevel.SchDoc

Drawn By:





All LCD_* lines excepting LCD_AIN* should have a pair of contact plates (for resistor) and cuttable line between them.



A

A

B

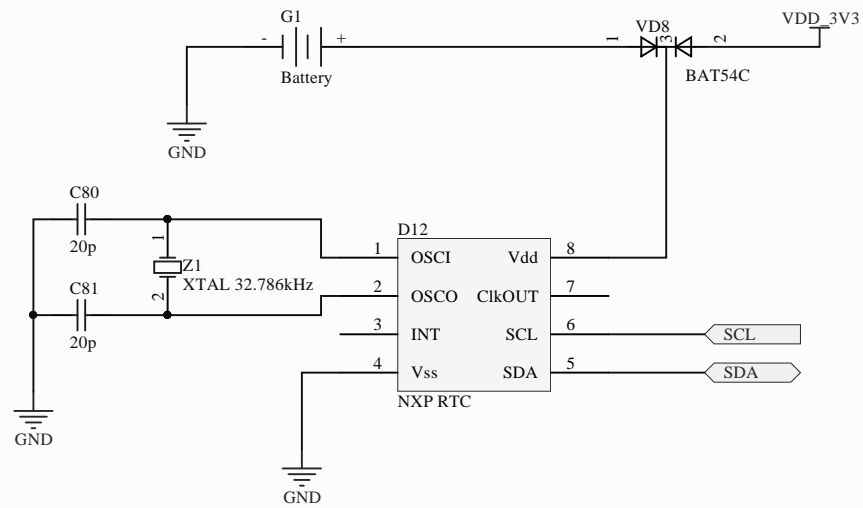
B

C

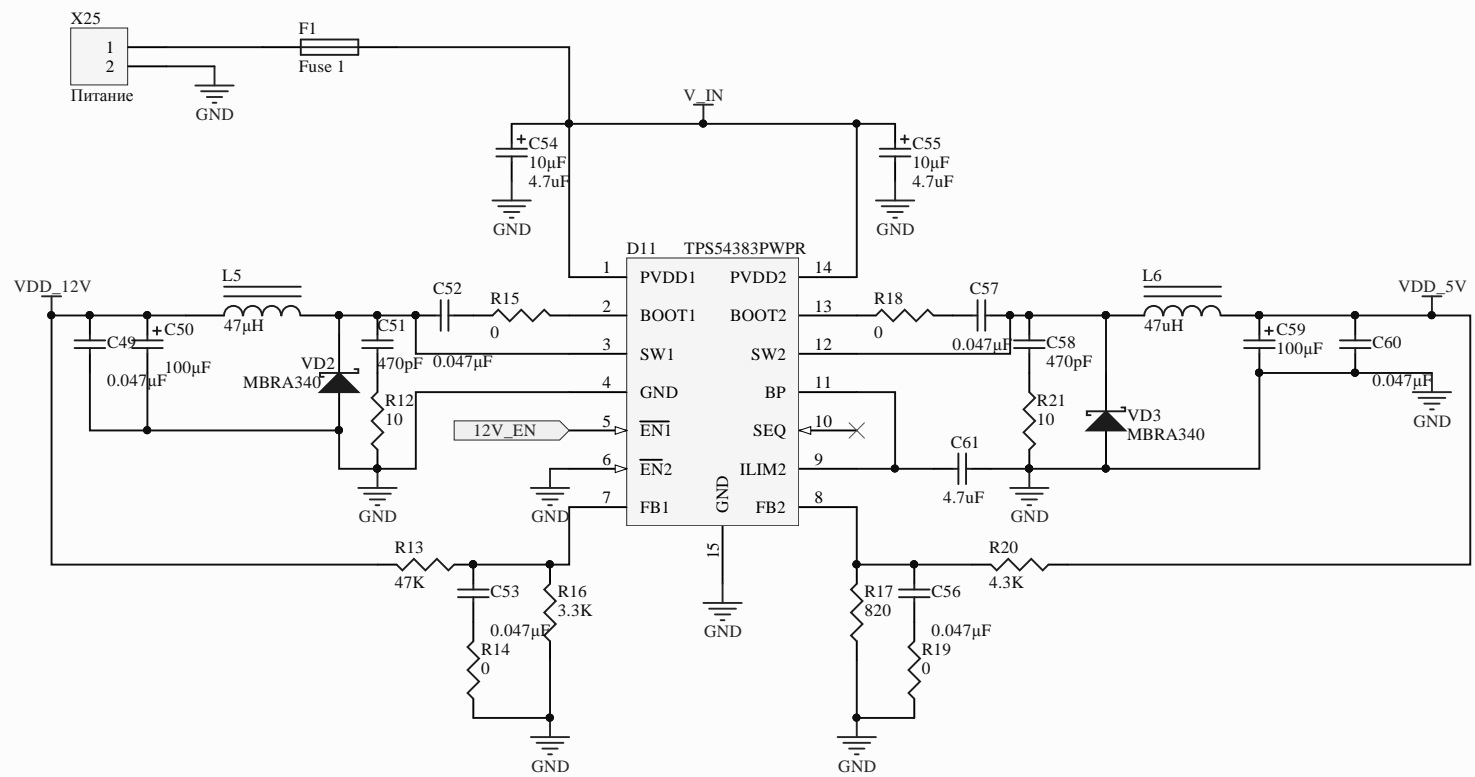
C

D

D



Title		
Size	Number	Revision
A4		
Date:	07.02.2016	Sheet of
File:	C:\Users\...\RTC.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	07.02.2016	Sheet of
File:	C:\Users\...\Power.SchDoc	Drawn By:

A

A

B

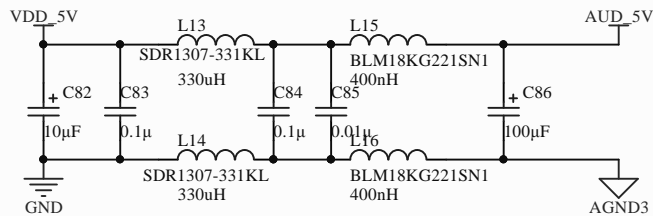
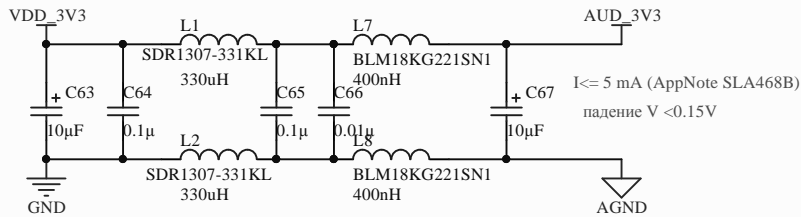
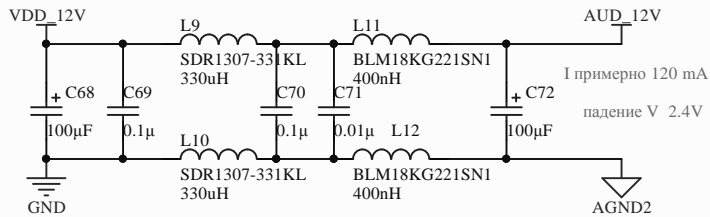
B

C

C

D

D



Title		
Size	Number	Revision
A		
Date: 07.02.2016		Sheet of
File: C:\Users\...\Power_decoupling.SchDoc		Drawn By:

1

2

3

4

A

A

B

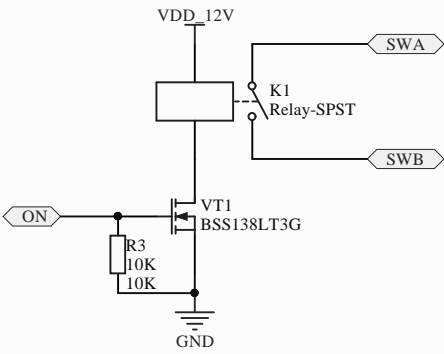
B

C

C

D

D



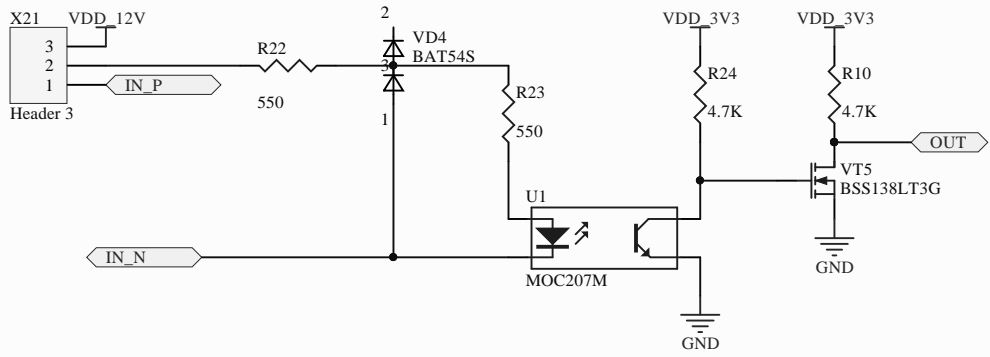
Title		
Size	Number	Revision
A		
Date:	07.02.2016	Sheet of
File:	C:\Users\...\Relay.SchDoc	Drawn By:

1

2

3

4



Title		
Size	Number	Revision
A		
Date:	07.02.2016	Sheet of
File:	C:\Users\...\Sensor.SchDoc	Drawn By: