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Quiz5

W40-30-2,x R40-32-10,x W10-10-0,x R10-10-2,x R40-30-4,x R40-35-16,x W40-30-2,
W40-30-4.

How many bytes of data go back and forth between the main memory and the
cache/CPU when

Write policy: **write-through**

Write miss policy: **no-write-allocate**

Replacement policy: **FIFO**

W40-30-2: Cache miss. Since its a write, don't load bytes into cache

R40-32-10: Cache miss. **Load 64** bytes from memory

W10-10-0: Cache miss. Since its a write, don't load bytes into cache

R10-10-2: Cache miss. **Load 64** bytes from memory

R40-30-4: Cache miss. **Load 64** bytes from memory, put in second block frame

R40-35-16: Cache miss. **Load 64** bytes from memory, replace first block frame

W40-30-2: Cache hit. Write 1 byte to the block

W40-30-4: Cache hit. Write 1 byte to the block

TOTAL = 4 * 64 = 256bytes