

Instruction on hardware part selection

Ref: [Digilent Reference manuals](#), [Zedboard User Guide](#)

Buy	
Reference Manual	Technical Support
ZedBoard Zynq-7000 Development Board	
Features <ul style="list-style-type: none"> • USB-JTAG Programming using Digilent SMT1-equivalent circuit 	
Key Specifications	
FPGA Part #	XC7Z020-CLG484
Oscillators	33.333MHz (PS), 100MHz (PL)
Ethernet	10/100/1G
Connectivity and On-board I/O	
Pmod Connectors	5 2x6
HDMI	HDMI output
VGA	12-bit VGA
Display	128x32 OLED
Audio	Line-in, line-out, headphone, mic
USB	USB OTG 2.0
SD	4GB SD Card
FMC	One LPC FMC
AMS	One AMS Header
Switches	8 DIP/slide switches
Push-buttons	7
Reset Buttons	2
User LEDs	9
DONE LED	1

Part #: **xc7z020clg484-1**

Oscillator clk frequency (PL): **100MHz**

Available inputs for PL:

- 8 DIP switches (**SW0-7**) – I/O bank 35
- 5 push buttons (**BTNU/R/D/C/L**) – I/O bank 34
- 2 reset buttons:
 - **BTN6/PROG** – initiates reconfigure PL-subsection
 - **BTN7/PS_RST** – erases all debug configurations, allow reset all functional logic within the device, without disturbing the debug environment. (For example, the previous break points set by the user remains valid after system reset)

Available outputs for PL:

- 8 user LEDs (**LD0-7**) – I/O bank 33

