ACTIVITY

Answer questions

Descripción breve

Throughout this document some question will be answered according to the activity requested

JULIO EMANUEL GONZALEZ GONZALEZ
Internship

STM32H7B0VBT6

Memory map questions.

1. **What's the base address of AHB1 BUS Peripherals?**
   0x40040000 – 0x400203FF
2. **What's the base address of RCC engine registers of the MCU?**
   0x58024400 - 0x580247FF
3. **What's the base address of APB1 Peripherals?**
   0x4000AC00 – 0x400003FF
4. **What's the base address of Flash memory?**
   0x0810 0000 - 0x081F FFFF Flash memory bank 2
   0x0800 0000 - 0x080F FFFF Flash memory bank 1
5. **What's the base address of SRAM2?**
   0x3001 0000 - 0x3001 FFFF AHB SRAM2
   0x2404 0000 - 0x2409 FFFF AXI SRAM2
6. **What's the base address of ADC registers?**
   0x40022000 - 0x400223FF

Block diagram questions.

1. **Can the system bus operate at the speed up to 180MHz?**
   Yes, it can, its maximum operating speed is 280MHz.
2. **Are SRAMS connected to System Bus T/F?**
   True, they are connected to the System Bus
3. **Can APB1 bus operate at the speed up to 180 MHz?**
   No, it can´t, its maximum operating speed is 140MHz.
4. **Let´s say I have a Peripheral whose datasheet says that its operating frequency or speed must be above 96 MHz, Can I connect that Peripheral via APB2 Bus?**
   Yes, because of the maximum operating speed of APB2 is 140MHz what means the peripheral will work so well.

5. **What's is the MAX. HCLK value of your MCU?**

   It´s 280MHz.

6. **What is the MAX, P1CLK (APB1) value of your MCU?**

   It´s 140MHz.

7. **What is the MAX, P2CLK (APB2) value of your MCU?**

   It´s 140 MHz.

8. **Are GPIOs and Processor communicated over AHB1 bus T/F?**

   False, GPIOs are communicated over AHB4.

9. **USB OTG and processor communicate over AHB2 bus T/F?**

   No, OTG is comminating by AHB BUS-MATRIX and is receiving information of AHB1.