

2.

Throughput - number of instructions done per cycle or per unit time

Latency - amount of time it takes to finish an instruction

3.

- to keep track the instruction executed and the input data needed by each stage of the pipeline

- to store the output data correctly to its proper memory address/register

4.

		EX			MEM			WB	
	Inst. Type	ALU Src	Reg Dst	ALU Op	Branch	Mem Write	Mem Read	Mem toReg	RegWrite
add	R	0 RF	1 rd	ADD	0	0	0	1 ALU	1
load	I	1 imm	0 rt	ADD	0	0	1	0 Mem	1
bne	I	0 RF	X	SUB	1	0	0	X	0