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General

Configuration: Debug [Active]

Manage Configurations...

Project Products MPU

☐ Enable Memory Protection Unit (MPU)

☒ Let compiler handle memory partitioning, associated access rights, and linker output section placement

☐ Manually specify memory segment boundaries and access rights

0xFF7F	Segment 3	R W X	<input checked="" type="checkbox"/> <input type="checkbox"/> <input checked="" type="checkbox"/>	<input checked="" type="checkbox"/> Assert PUC on access rights violation
0xC600	Segment 2	R W X	<input checked="" type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>	<input checked="" type="checkbox"/> Assert PUC on access rights violation
0xC400	Segment 1	R W X	<input checked="" type="checkbox"/> <input checked="" type="checkbox"/> <input type="checkbox"/>	<input checked="" type="checkbox"/> Assert PUC on access rights violation
0xC200	Info Memory	R W X	<input checked="" type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>	<input checked="" type="checkbox"/> Assert PUC on access rights violation

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Processor Options

Configuration: Debug [Active]

Manage Configurations...

Silicon version (--silicon_version, -v)

mbsp

Specify the code memory model (--code_model)

large

Specify the data memory model (--data_model)

small

Indicates what data must be near (--near_data)

globals

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Optimization

Configuration: Debug [Active]

Manage Configurations...

Optimization level (--opt_level, -O)

4 - Whole Program Optimizations

Speed vs. size trade-offs (--opt_for_speed, -mf)

4

0 (size)

5 (speed)

☐ Align functions and loops for power (--align_for_power)

Inline hardware multiply version of RTS mpy routine (--use_hw_mpy)

F5

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Language Options

Configuration: Debug [Active]

Manage Configurations...

C Dialect

Compile program in C99 mode. (--c99)

C++ Dialect

Compile program in C++14 mode. (--c++14)

Language mode

Relaxed parsing (non-strict ANSI) (--relaxed_ansi)

☐ Enable C++ exception handling (--exceptions)

☐ Support C++ run-time type information (--rtti, -rtti)

☐ Treat C files as C++ files (--cpp_default, -fg)

☐ Allow extern C functions to propagate exceptions (--extern_c_can_throw)

Floating point precision accepted by compiler (--float_operations_allowed)

none

Specify how to treat plain chars (signed/unsigned) (--plain_char)

unsigned

Limit pending template instantiations (--pending_instantiations)

Level of printf/scanf support required (--printf_support)

minimal

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Runtime Model Options

Configuration: Debug [Active] Manage Configurations...

Place structs and arrays in separate subsections (--gen_data_subsections) on

Designate enum type (--enum_type) packed

Use ELF common symbols (--common) on

☐ Reserve a register for use by the user. (--global_register) [r4]

☐ Reserve a register for use by the user. (--global_register) [r5]

☐ Use large-data memory model (--large_memory_model, -ml)

Run functions from RAM. (--ramfunc)

☐ Workaround specified silicon errata (--silicon_errata) [CPU12]

☐ Workaround specified silicon errata (--silicon_errata) [CPU13]

☐ Workaround specified silicon errata (--silicon_errata) [CPU15]

☐ Workaround specified silicon errata (--silicon_errata) [CPU18]

☐ Workaround specified silicon errata (--silicon_errata) [CPU19]

☒ Workaround specified silicon errata (--silicon_errata) [CPU21]

☒ Workaround specified silicon errata (--silicon_errata) [CPU22]

☐ Workaround specified silicon errata (--silicon_errata) [CPU23]

☒ Workaround specified silicon errata (--silicon_errata) [CPU40]

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Advanced Optimizations

Configuration: Debug [Active]

Manage Configurations...

Allow reassociation of FP arithmetic (--fp_reassoc)

off

Allow reassociation of sat arithmetic (--sat_reassoc)

off

☐ Program mode compilation (--program_level_compile, -pm)

Specify call assumptions when optimizing (--call_assumptions, -op)

3

Specify threshold for automatic inlining (--auto_inline, -oi)

100

☐ Disable inlining (--disable_inlining)

Inline hardware multiply version of RTS mpy routine (--disable_interrupts_around_hw_mpy)

☐ Assume called funcs create hidden aliases (rare) (--aliased_variables, -ma)

Properties for Beispiel1 LEDwithLOOP

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Basic Options

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Manage Configurations...

Specify the output path for section dependence information (--emit_references:file)

☐ Specify the format for section reference information (--emit_references:format) [TEXT]

Link information (map) listed into <file> (--map_file, -m)

\$(ProjName).map

Specify output file name (--output_file, -o)

\$(ProjName).out

Heap size for C/C++ dynamic memory allocation (--heap_size, -heap)

8

Set C system stack size (--stack_size, -stack)

32

Hold watchdog timer during cinit auto-initialization (--cinit_hold_wdt)

off

?

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Symbol Management

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Manage Configurations...

Specify program entry point for the output module (--entry_point, -e)

Do not localize symbols matching pattern (--globalize)

Hide symbols matching pattern (--hide)

Make the symbols matching pattern local (--localize)

Don't make global symbol static if -h is specified (--make_global, -g)

☐ Make all global symbols static (--make_static, -h)

☐ No type merging in symbolic debugging information (--no_sym_merge, -b)

☐ Strip symbol table and line number entries (--no_symtable, -s)

Specify symbols/sections to be retained by linker (--retain)

☐ Scan libraries for duplicate symbol definitions (--scan_libraries, -scanlibs)

Specify a symbol map, mapping refname to defname (--symbol_map)

Add <sym> to symbol table as an unresolved symbol (--undef_sym, -u)

Exclude symbols matching pattern from hiding (--unhide)

Deprecated: Now a compiler option instead of linker option (--use_hw_mpy)

F5

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