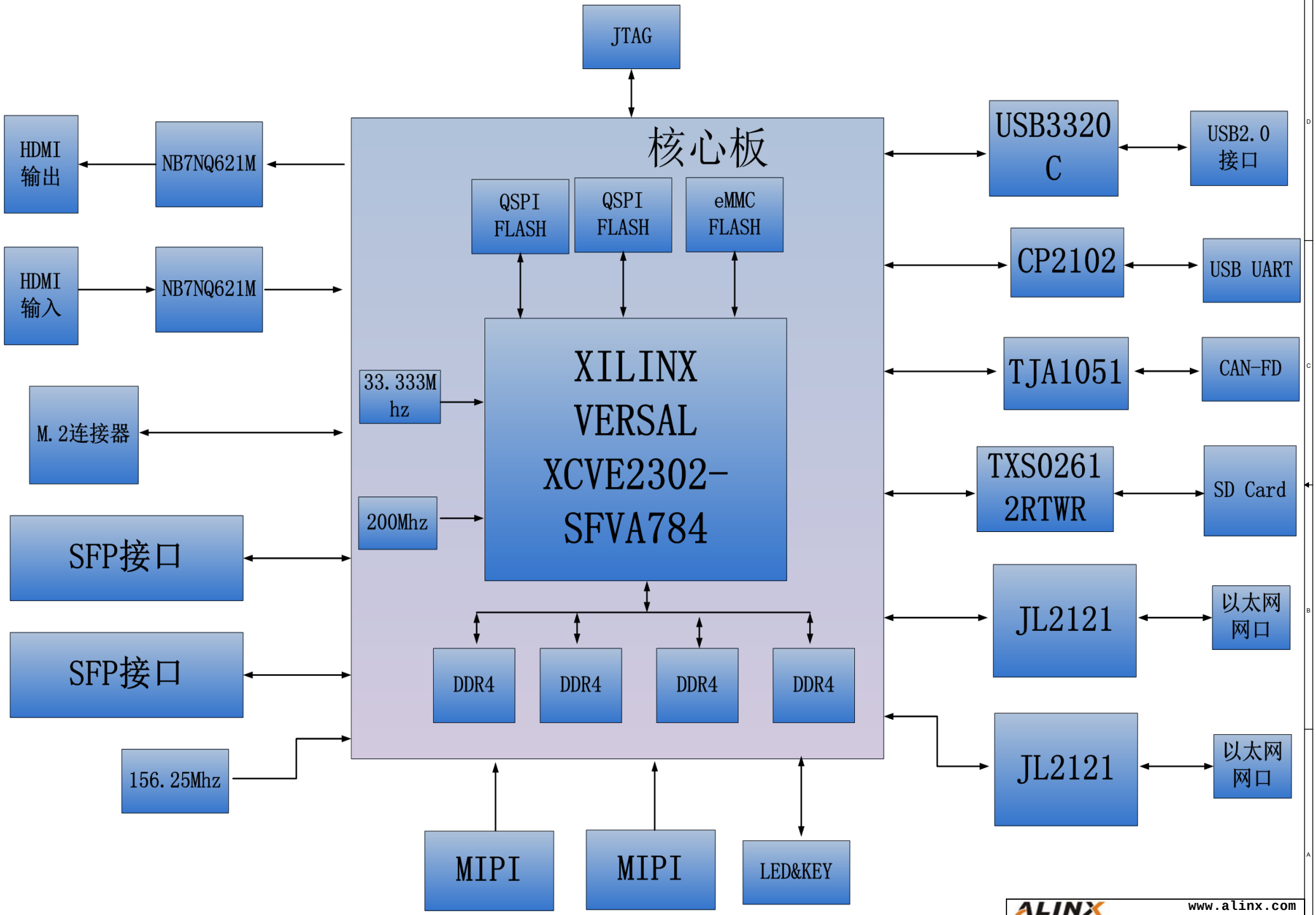

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Title: PAGE01 Block diagram

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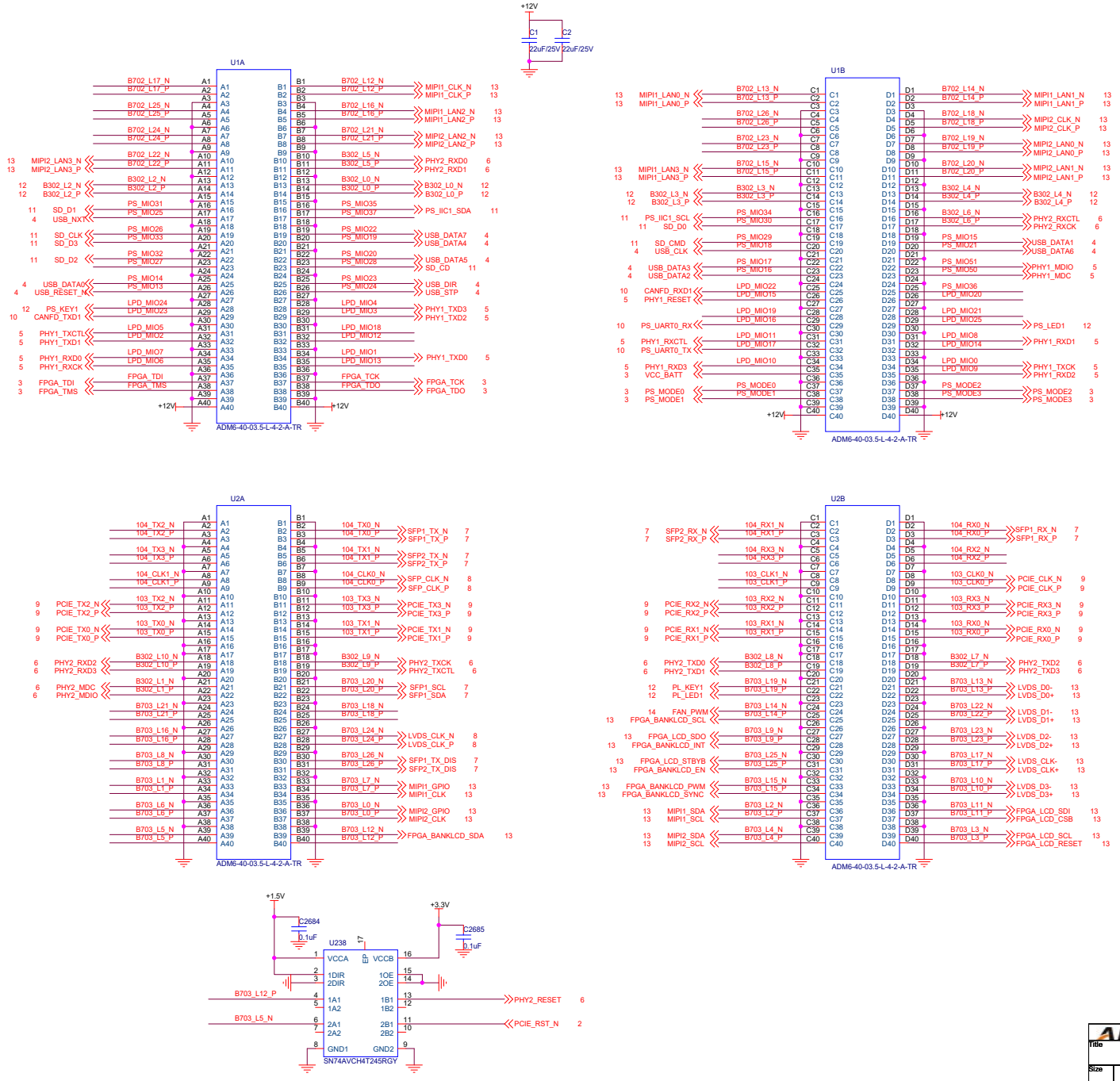

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Title	PAGE02 Connector_1
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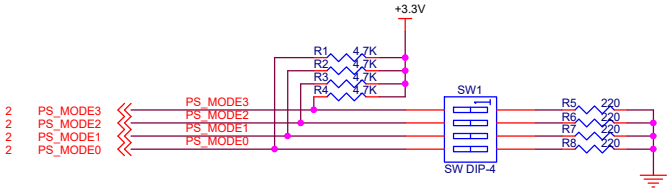
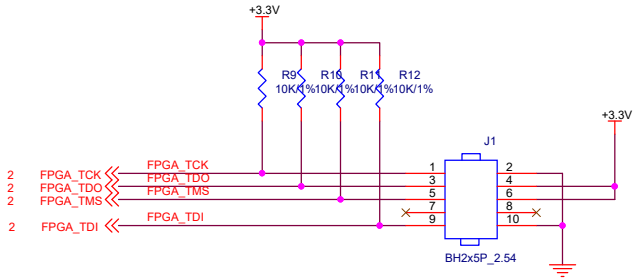
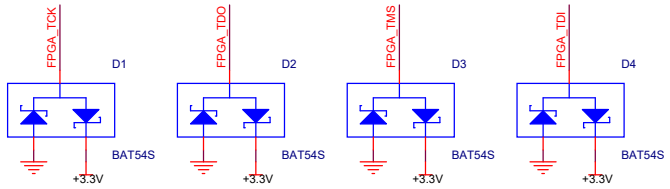
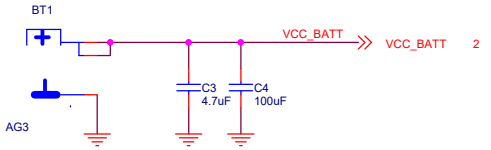


MODE[3:0]	BOOT MODE	Descritpion
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0100	NAND	Requires 8-bit data bus width
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1000	PJTAG(MIO #0)	PJTAG connection 0 option
1001	PJTAG(MIO #1)	PJTAG connection 1 option
1110	SD1 LS(3.0)	SD 3.0

JTAG Connector







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Title: Page03 JTAG

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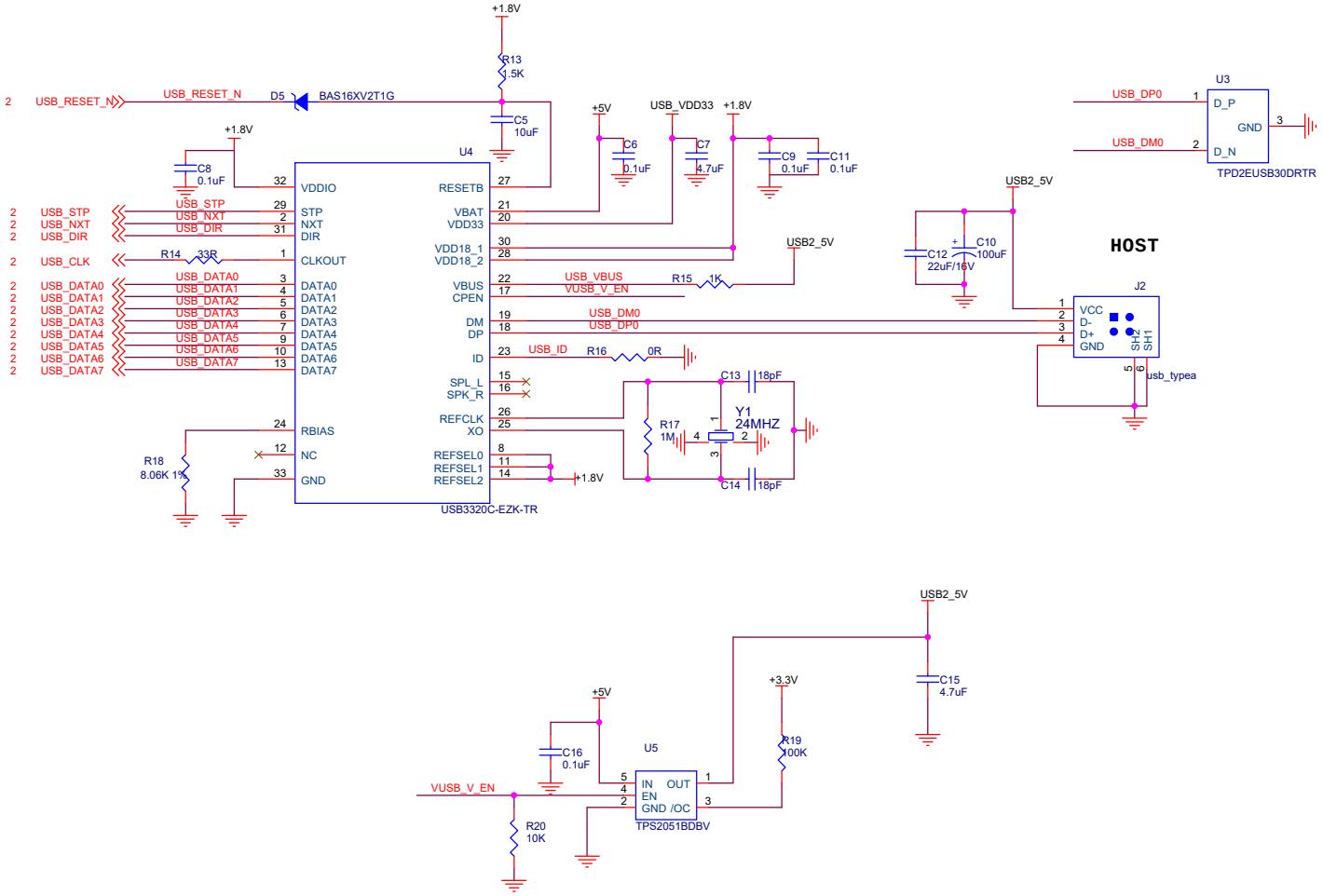

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Title	PAGE04 USB2.0
Size	
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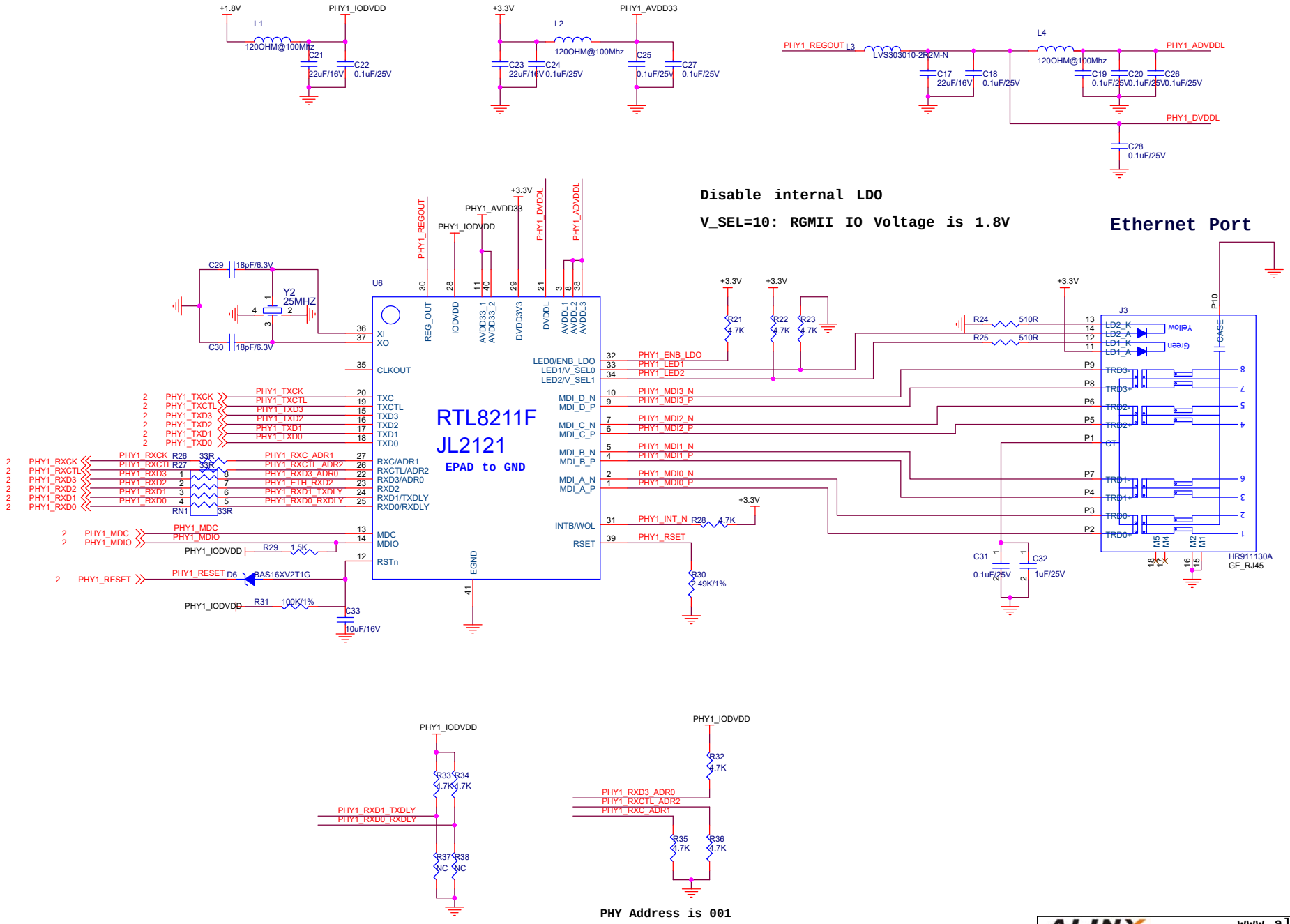

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Title: PAGE05 PS Ethernet1

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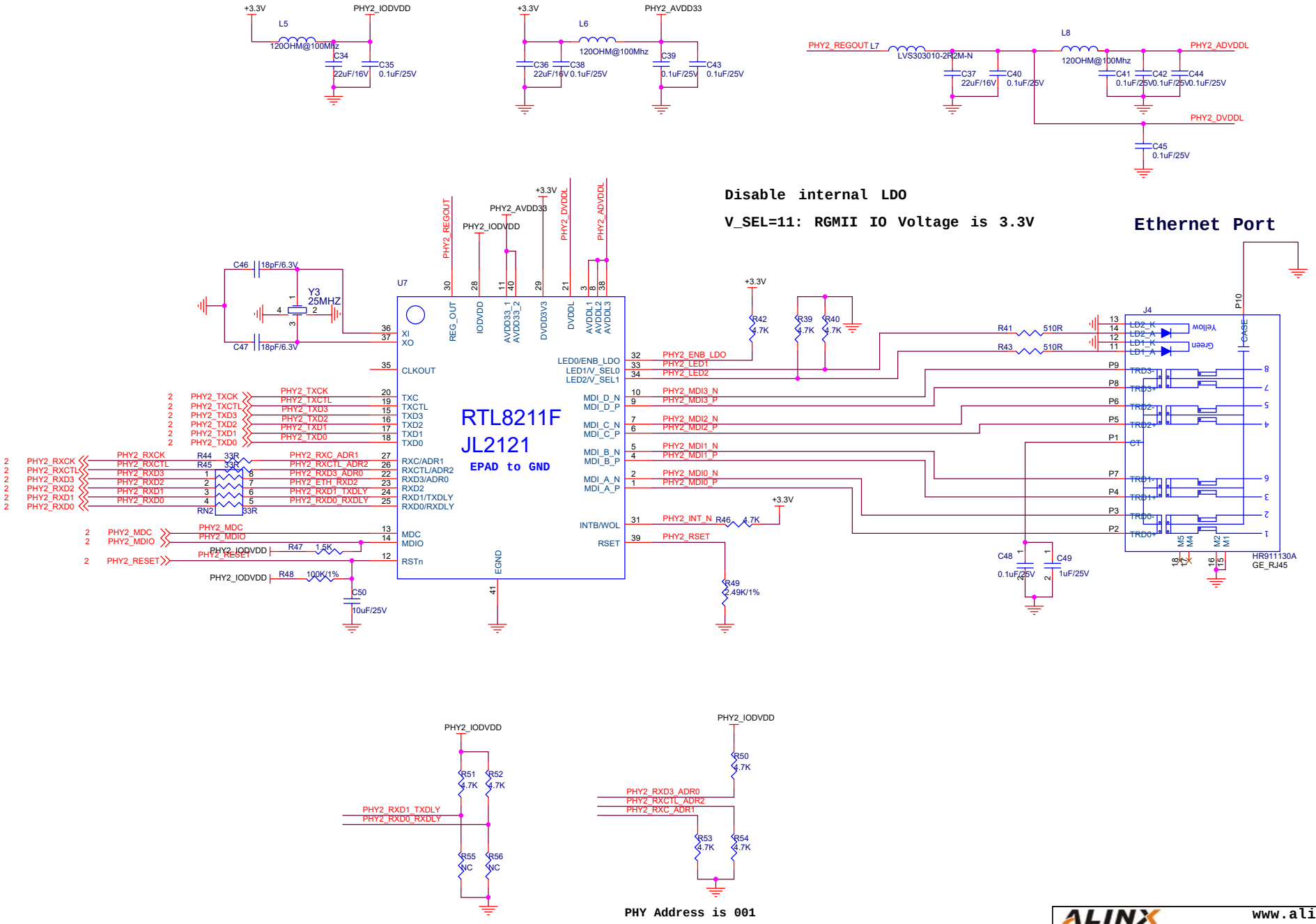

ALINX	www.alinx.com
Title	PAGE06 PL Ethernet2
Size	Document Number: VERSAL开发板 Schematics
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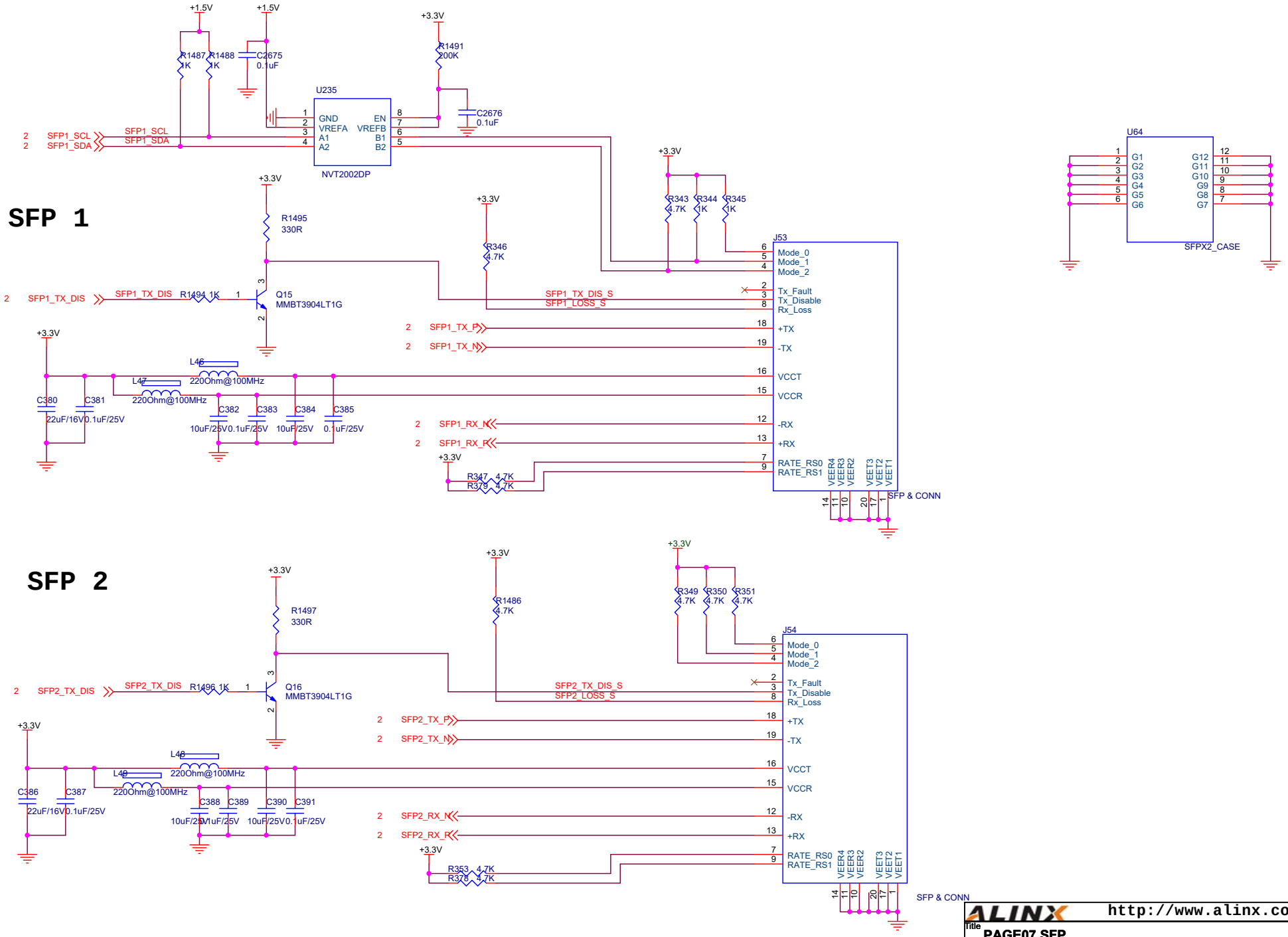

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Title	PAGE07 SFP	
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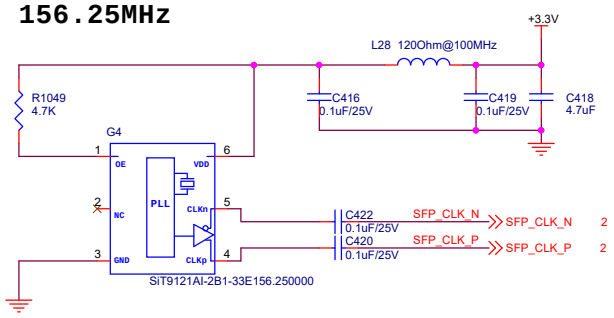
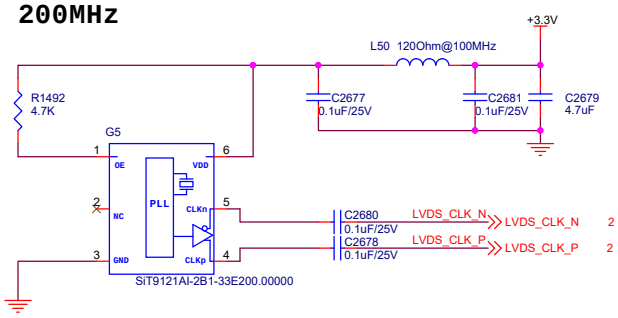
156.25Mhz for SFP



200Mhz for LVDS



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Title: PAGE08 CLOCK

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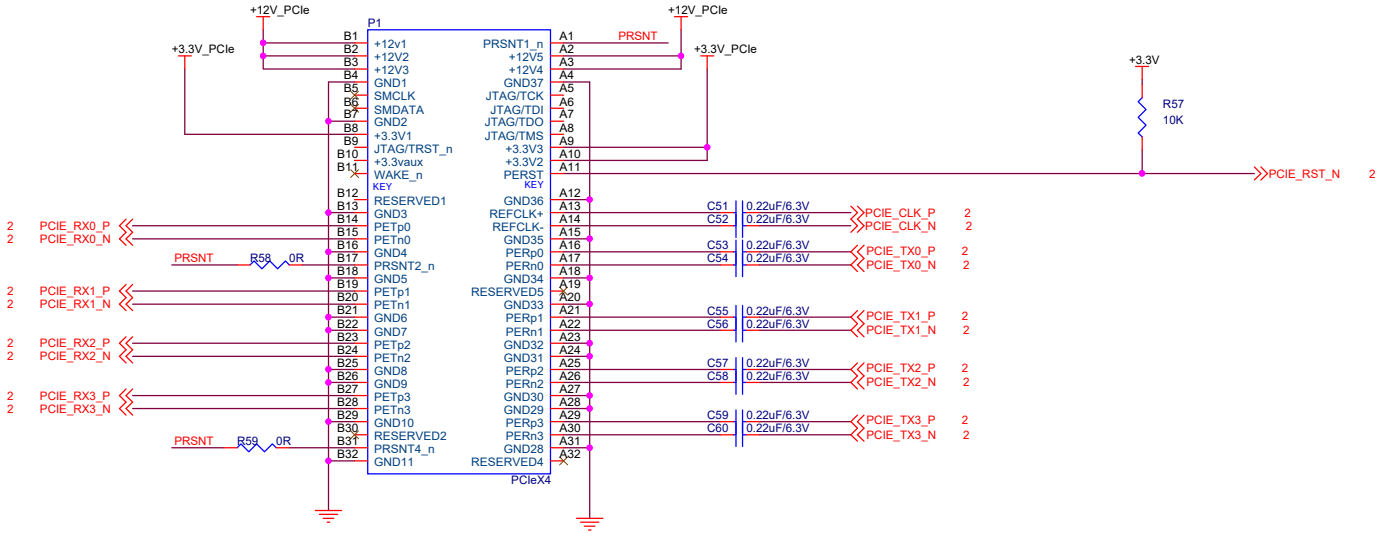
PCIE X4 SLOT



Title	PAGE09 PCIE	
Size	Document Number: VERSAL开发板 Schematics	Rev: 1.1
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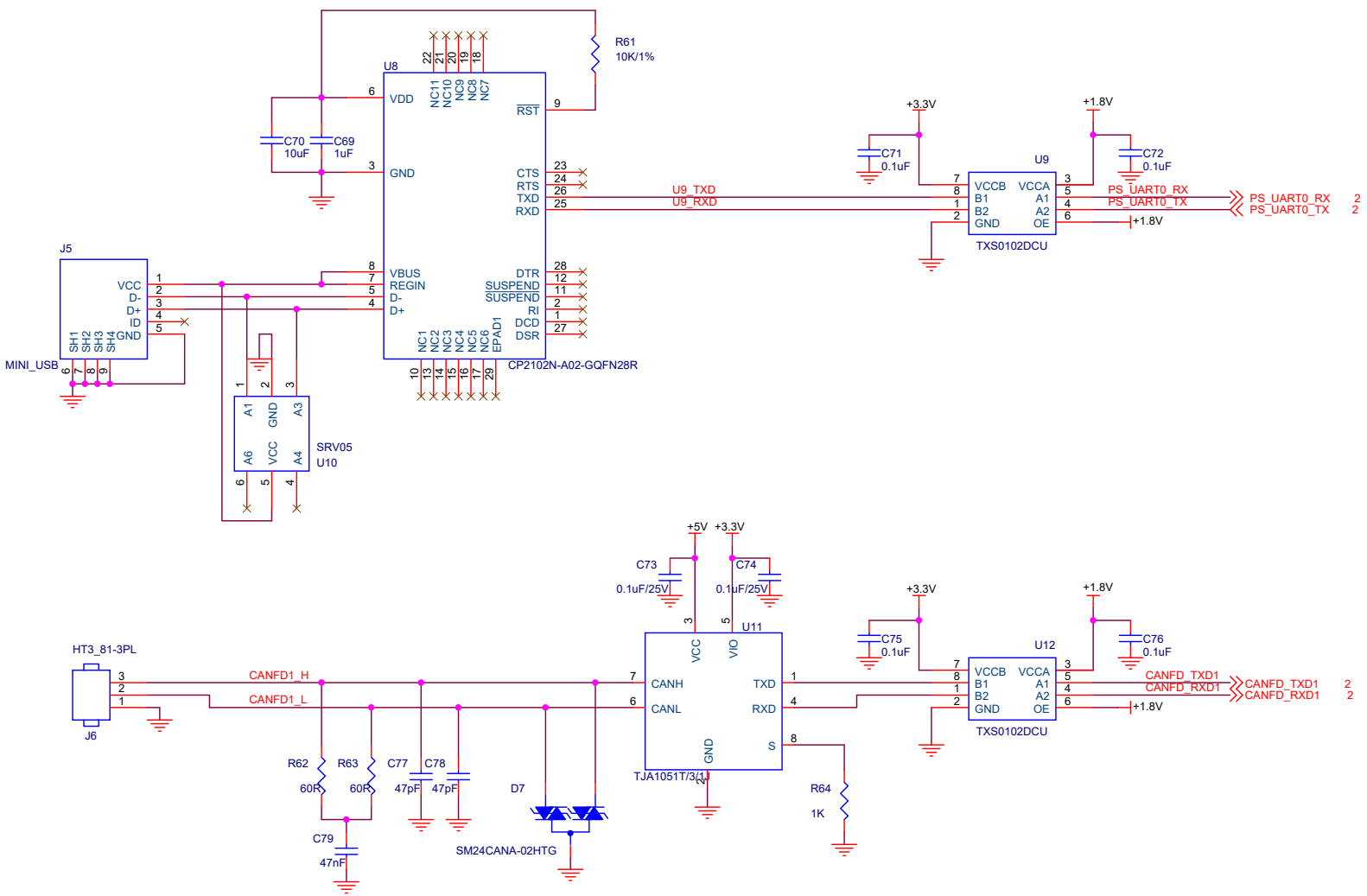
PS UART PORT



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Title: PAGE10 UART		
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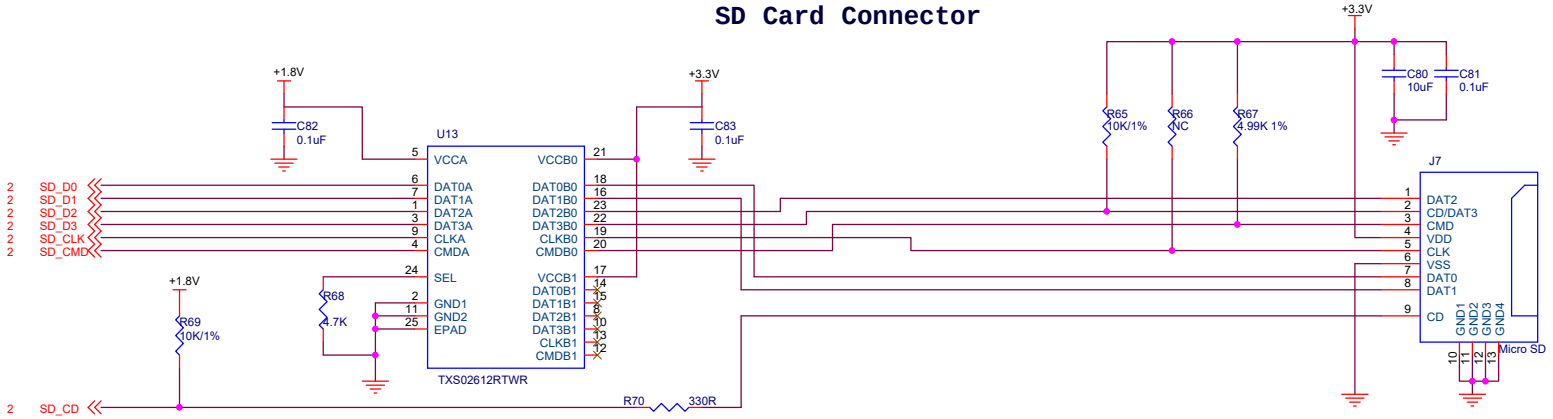
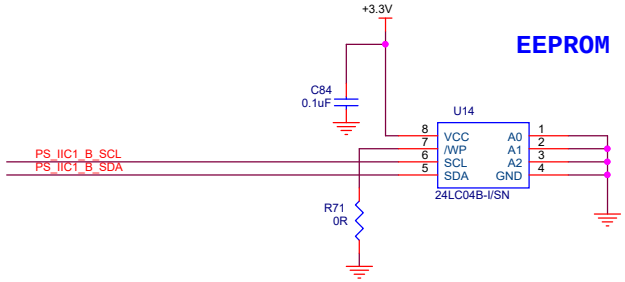
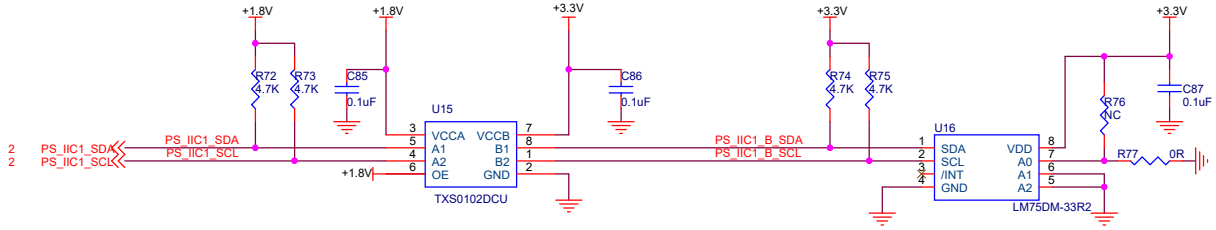
SD Card Connector



EEPROM



SENSOR



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Title	PAGE11 SD & EEPROM & Sensor	
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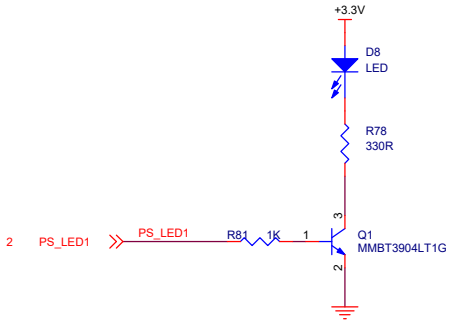

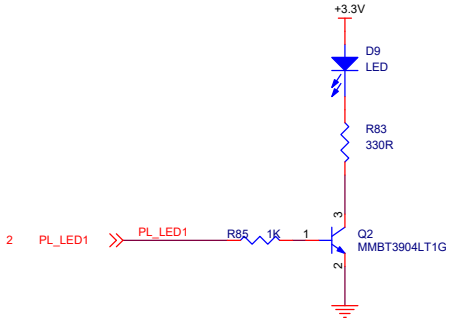

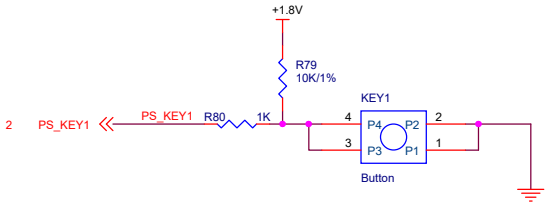

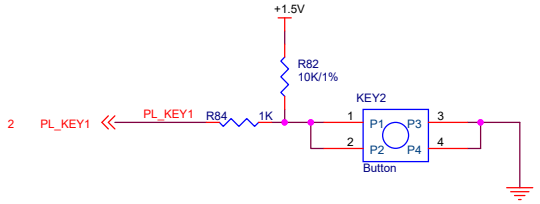

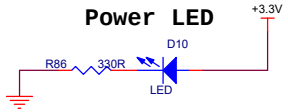

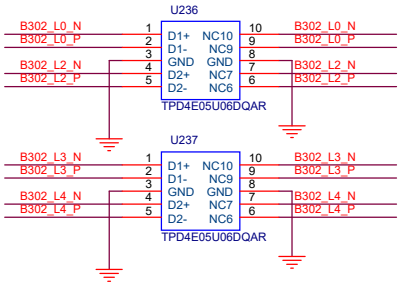

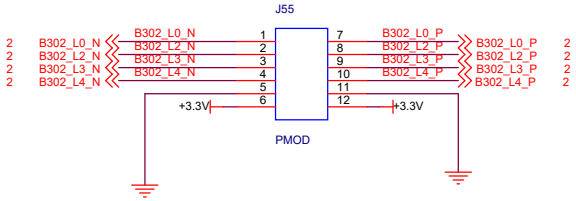

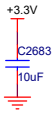

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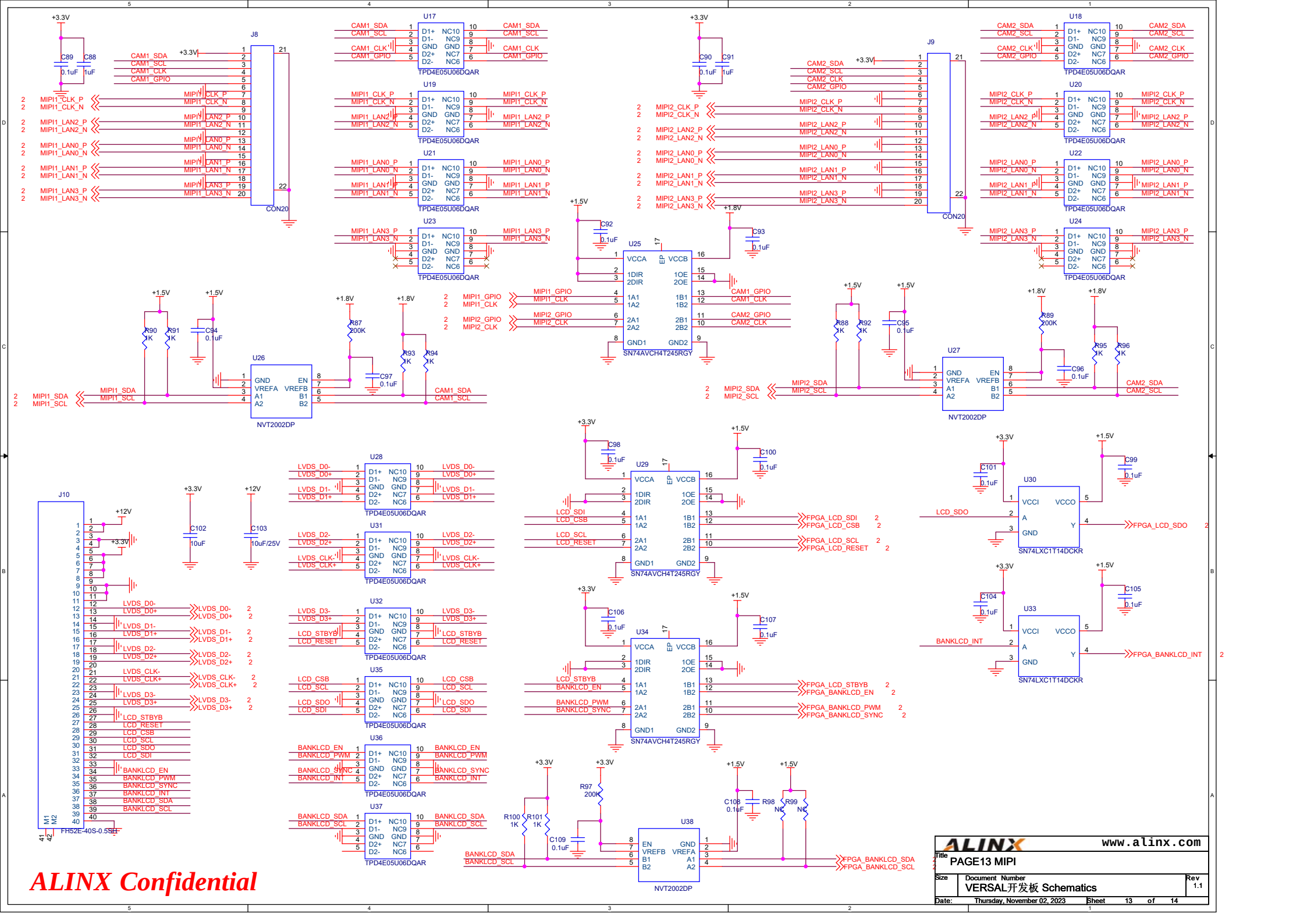
J8
CON20
U17
U18
U19
U20
U21
U22
U23
U24
TPD4E05U06DQAR
J9
U25
U26
U27
SN74AVCH4T245RGY
NVT2002DP
U28
U29
U30
U31
U32
U33
U34
U35
U36
U37
U38
SN74LXC1T14DCKR
J10
FH52E-40S-0.5SH
CAM1_SDA
CAM1_SCL
CAM1_CLK
CAM1_GPIO
CAM2_SDA
CAM2_SCL
CAM2_CLK
CAM2_GPIO
MIPI1_CLK_P
MIPI1_CLK_N
MIPI1_LAN2_P
MIPI1_LAN2_N
MIPI1_LAN0_P
MIPI1_LAN0_N
MIPI1_LAN1_P
MIPI1_LAN1_N
MIPI1_LAN3_P
MIPI1_LAN3_N
MIPI2_CLK_P
MIPI2_CLK_N
MIPI2_LAN2_P
MIPI2_LAN2_N
MIPI2_LAN0_P
MIPI2_LAN0_N
MIPI2_LAN1_P
MIPI2_LAN1_N
MIPI2_LAN3_P
MIPI2_LAN3_N
MIPI1_GPIO
MIPI1_CLK
MIPI2_GPIO
MIPI2_CLK
MIPI1_SDA
MIPI1_SCL
MIPI2_SDA
MIPI2_SCL
LVDS_D0-
LVDS_D0+
LVDS_D1-
LVDS_D1+
LVDS_D2-
LVDS_D2+
LVDS_CLK-
LVDS_CLK+
LVDS_D3-
LVDS_D3+
LCD_STBYB
LCD_RESET
LCD_CSB
LCD_SCL
LCD_SDO
LCD_SDI
BANKLCD_EN
BANKLCD_PWM
BANKLCD_SYNC
BANKLCD_INT
BANKLCD_SDA
BANKLCD_SCL
FPGA_LCD_SDI
FPGA_LCD_CSB
FPGA_LCD_SCL
FPGA_LCD_RESET
FPGA_LCD_SDO
FPGA_LCD_STBYB
FPGA_BANKLCD_EN
FPGA_BANKLCD_PWM
FPGA_BANKLCD_SYNC
FPGA_BANKLCD_INT
FPGA_BANKLCD_SDA
FPGA_BANKLCD_SCL
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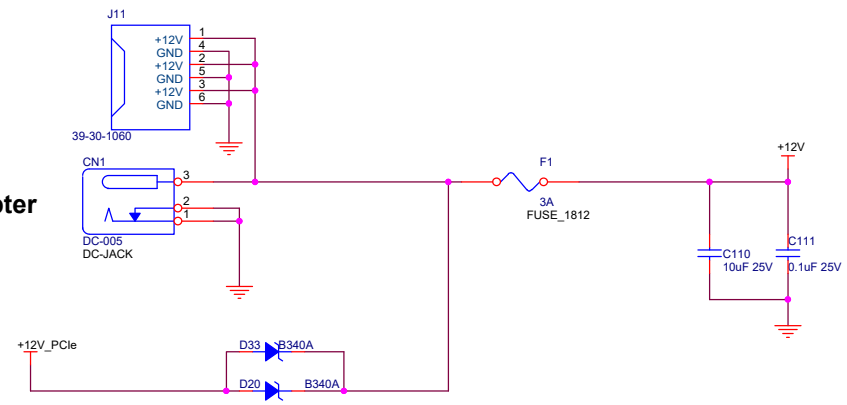
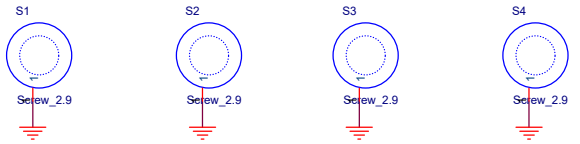
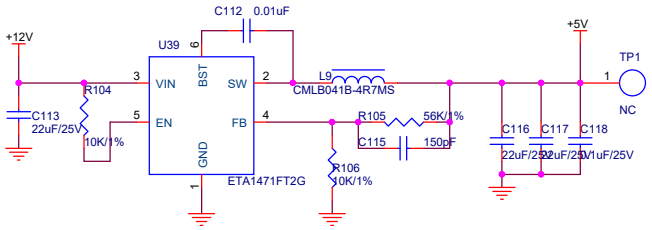
12V/3A AC Adapter



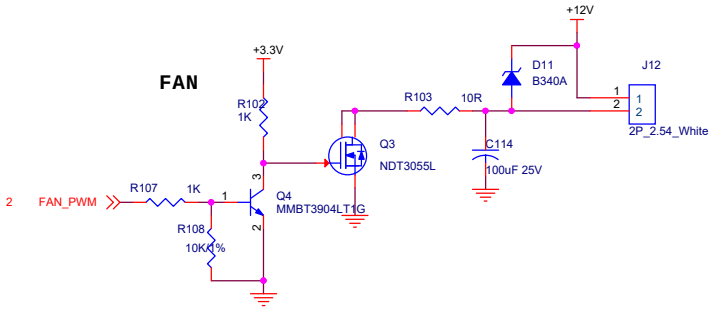


+5V POWER

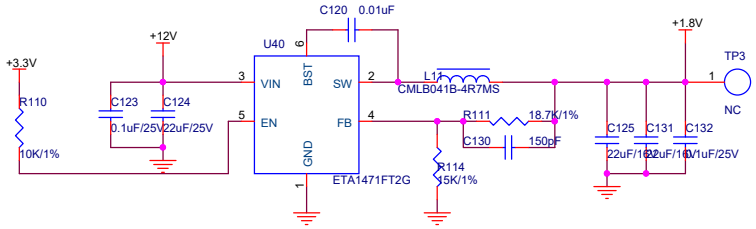


Vout=0.8 x (1 + R1/R2)

FAN

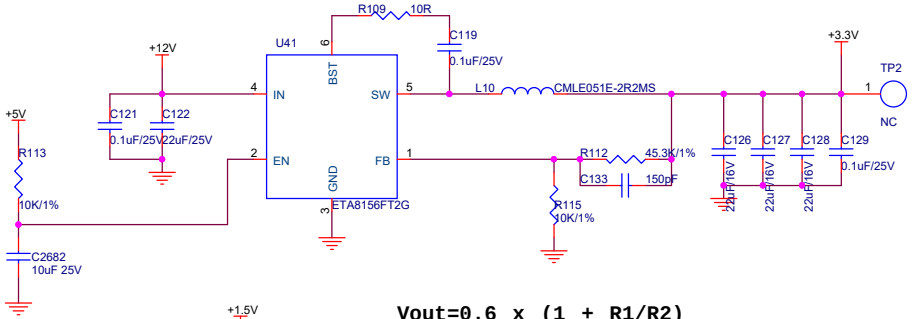


+1.8V POWER

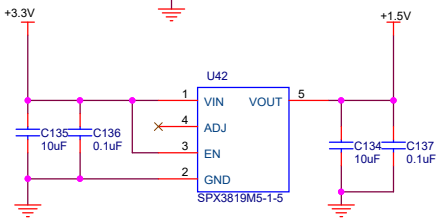


Vout=0.8 x (1 + R1/R2)

+3.3V POWER 6A



Vout=0.6 x (1 + R1/R2)



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