

Formal Verification of Removing Unnecessary Synchronization Optimizations in Compilation

JUNPENG ZHA, Nanjing University, China

ACM Reference Format:

Junpeng Zha. 2018. Formal Verification of Removing Unnecessary Synchronization Optimizations in Compilation. 1, 1 (August 2018), 48 pages. <https://doi.org/10.1145/1122445.1122456>

There are some works, e.g. Jeff et al. [2] and Aldrich et al. [1], about removing unnecessary synchronizations, which is usually called lock elision and means that accessing thread local memory locations doesn't need to be protected by synchronizations in concurrent program. These works show that this optimization can reduce the synchronization overhead and improve the performance of concurrent program in practice. In this document, we consider how to verifying the correctness of removing unnecessary synchronization optimization formally.

As for verifying the concurrent program compilation correctness, Jiang et al. [3] propose a framework for verifying the compilation correctness for DRF concurrent programs and develop CASCompCert as an instantiation of their verification framework. However, CASCompCert work fails to support verifying this optimizations. The verification framework views ExtAtom as a switch pointer in non-preemptive semantics and requires that, when the source program does context switch, the target program needs to switch at the same time and to the same thread. This requirement provides some convenience for their verification work, because the source and target program can always execute the same threads. However, this requirement may not hold after applying lock elision optimization. As the following example shown in Fig. 1:

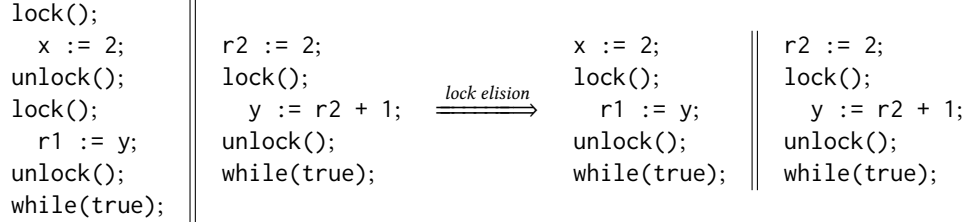


Fig. 1. Example of lock elision

In Fig. 1, we can find that the variable x is not a shared variable and doesn't need to be protected by synchronization. So, there is no problem that the compiler eliminates such synchronization. Verifying compiler with lock elision is not a trivial task, we need to solve the following problems:

- (1) *How to say that a synchronization can be eliminated?* As the example shown in Fig. 1, we find that the synchronization protecting accessing variable x can be eliminated. However, in a language-independent

Author's address: Junpeng Zha, Nanjing University, Nanjin, China.

framework for verifying compiler correctness, there is no specific code and we need to consider how to describe that such synchronization can be eliminated.

- (2) *How to establish the simulation relation between source and target program?* As we have explained, the CASCompCert fails to support verifying lock elision because it restricts that the switch points of source and target programs should match.
- (3) *How to verify lock elision modularly?* The lock elision is just a compilation pass in a compiler. It needs to be composed with other compiler passes. And we also hope to achieve separate compilation.

In this document, we solve the problems shown above. We show our method to solve problem (1) in Sect. 1; we present our method to establish simulation relation between source and target programs in Sect. 2; and we show how to achieve modular verification in Sect. 3.

Definition 1 (Module-Local Downward Simulation).

$(sl, ge, \gamma) \preceq_{\varphi, E} (tl, ge', \pi)$ iff

- (1) $\lfloor \varphi \rfloor(ge) = ge'$;
- (2) for all $f, \mathbb{k}, \Sigma, \sigma, \mathbb{F}, F$, and $\mu = (\text{dom}(\Sigma), \text{dom}(\sigma), \varphi|_{\text{dom}(\Sigma)})$, if $sl.\text{InitCore}(\gamma, f) = \mathbb{k}$, $\mathbb{F} \cap \text{dom}(\Sigma) = F \cap \text{dom}(\sigma) = \emptyset$, and $\text{initM}(\varphi, ge, \Sigma, \sigma)$, then there exists $i \in \text{index}$, κ such that:
 $tl.\text{InitCore}(\pi, f) = \kappa$, and $(\mathbb{F}, (\mathbb{k}, \Sigma), \text{emp}) \preceq_{\mu}^{i, E} (F, (\kappa, \sigma), \text{emp})$,
 where $(\mathbb{F}, (\mathbb{k}, \Sigma), \Delta) \preceq_{\mu}^{i, E} (F, (\kappa, \sigma), \delta)$ is defined in Def. 2.

Definition 2. $(\mathbb{F}, (\mathbb{k}, \Sigma), \Delta_0) \preceq_{\mu}^{i, E} (F, (\kappa, \sigma), \delta_0)$ is the largest relation such that, whenever $(\mathbb{F}, (\mathbb{k}, \Sigma), \Delta_0) \preceq_{\mu}^{i, E} (F, (\kappa, \sigma), \delta_0)$, then the following are true:

- (1) for all \mathbb{k}', Σ' and Δ , if $\mathbb{F} \vdash (\mathbb{k}, \Sigma) \xrightarrow{\tau}_{\Delta} (\mathbb{k}', \Sigma')$, and $(\Delta_0 \cup \Delta) \subseteq (\mathbb{F} \cup \mu.\mathbb{S})$, then one of the following holds:
 - (a) $\exists j < i. (\mathbb{F}, (\mathbb{k}', \Sigma'), \Delta_0 \cup \Delta) \preceq_{\mu}^{j, E} (F, (\kappa, \sigma), \delta_0)$, or
 - (b) there exists κ', σ', δ and j such that:
 - (i) $F \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^+ (\kappa', \sigma')$;
 - (ii) $(\delta_0 \cup \delta) \subseteq (F \cup \mu.\mathbb{S})$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$;
 - (iii) $(\mathbb{F}, (\mathbb{k}', \Sigma'), \Delta_0 \cup \Delta) \preceq_{\mu}^{j, E} (F, (\kappa', \sigma'), \delta_0 \cup \delta)$.
- (2) for all \mathbb{k}' and ι , if $\mathbb{F} \vdash (\mathbb{k}, \Sigma) \xrightarrow{\iota}_{\text{emp}} (\mathbb{k}', \Sigma)$, $\iota \neq \tau$, and $\text{HG}(\Delta_0, \Sigma, \mathbb{F}, \mu.\mathbb{S})$, one of the following holds:
 - (a) there exists $\kappa', \kappa'', \sigma', \delta$, such that (* switch point match *)
 - (i) $F \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^* (\kappa', \sigma')$, $F \vdash (\kappa', \sigma') \xrightarrow{\iota}_{\text{emp}} (\kappa'', \sigma')$, and $\neg(\iota \in E)$;
 - (ii) $\text{LG}(\mu, (\delta_0 \cup \delta, \sigma', F), (\Delta_0, \Sigma))$;
 - (iii) for all σ'' and Σ' , if $\text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma', \sigma'', F))$, then there exists j such that
 $(\mathbb{F}, (\mathbb{k}', \Sigma'), \text{emp}) \preceq_{\mu}^{j, E} (F, (\kappa'', \sigma''), \text{emp})$; or
 - (b) there exists $\kappa', \sigma', \delta, n$ and j , such that (* switch point not match *)
 - (i) $F \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^n (\kappa', \sigma')$, $\iota \in E$, and $(n = 0 \implies j < i)$;
 - (ii) $\text{LG}(\mu, (\delta_0 \cup \delta, \sigma', F), (\Delta_0, \Sigma))$;
 - (iii) for all σ'' and Σ' , if $\text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma', \sigma'', F))$, then
 $(\mathbb{F}, (\mathbb{k}', \Sigma'), \text{emp}) \preceq_{\mu}^{j, E} (F, (\kappa', \sigma''), \text{emp})$.

Definition 3 (Whole-Program Downward Simulation). Let $\hat{P} = \mathbf{let} \Gamma \mathbf{in} f_1 \mid \dots \mid f_n$, $\hat{P} = \mathbf{let} \Pi \mathbf{in} f_1 \mid \dots \mid f_n$, where $\Gamma = \{(sl_1, ge_1, \gamma_1), \dots, (sl_m, ge_m, \gamma_m)\}$, $\Pi = \{(tl_1, ge'_1, \pi_1), \dots, (tl_m, ge'_m, \pi_m)\}$. We say $\hat{P} \preceq_E \hat{P}$ iff

- $\exists \varphi. (\forall 1 \leq i \leq m. \lfloor \varphi \rfloor ge_i = ge'_i)$, and
- for any \widehat{W} , if $\hat{P} \xrightarrow{\text{load}} \widehat{W}$, then there exists $\widehat{W}, i \in \text{index}, \mu$, such that:

$$\hat{P} \xrightarrow{\text{load}} \widehat{W} \wedge (\widehat{W}, \text{emp}) \preceq_{(\widehat{W}, \emptyset), 0}^{i, \mu, E} (\widehat{W}, \text{emp}, \text{emp}).$$

Here we define $(\widehat{W}, \Delta_0) \preceq_{p, n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$ as the largest relation such that whenever $(\widehat{W}, \Delta_0) \preceq_{p, n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$ holds, then there exists t , where $\widehat{W}.t = \widehat{W}.t = t$, such that the following are true:

- (1) $t \notin \text{dom}(p.B)$, $\text{dom}(p.B) \subseteq \text{dom}(\mathbb{T})$, $\widehat{W}.d(t) \leq \widehat{W}.d(t)$ and $p.\widehat{W} \xrightarrow[\delta_r \cup \delta_0]{\tau}^{n_0} \widehat{W}$;
- (2) $\forall \widehat{W}', \Delta$. if $\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}'$, then one of the following holds:
 - (a) $\exists j < i. (\widehat{W}', \Delta_0 \cup \Delta) \preceq_{p, n_0}^{j, \mu, E} (\widehat{W}, \delta_0, \delta_r)$; or
 - (b) $\exists \widehat{W}', \delta, j, n > 0$ such that:
 - (i) $\widehat{W} \xrightarrow[\delta]{\tau}^n \widehat{W}'$, and
 - (ii) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$, and
 - (iii) $(\widehat{W}', \Delta_0 \cup \Delta) \preceq_{p, n_0+n}^{j, \mu, E} (\widehat{W}', \delta_0 \cup \delta, \delta_r)$;
- (3) $\forall T', t', \Sigma', o, d'$, if $o \neq \tau$, and $\widehat{W} \xrightarrow[\text{emp}]{o} (T', t', d', \Sigma')$, then one of the following holds:
 - (a) (* switch point match *) (* update the last switch point, and remove t'' from reorder buffer *)
 $\exists \widehat{W}', \delta, T', \sigma', d'_1, j$, for any $t'' \in \text{dom}(T')$, let $p' = ((T', t'', d'_1, \sigma'), B \setminus \{t''\})$, such that
 - (i) $\widehat{W} \xrightarrow[\delta]{\tau}^* \widehat{W}'$, and $\widehat{W}' \xrightarrow[\text{emp}]{o} (T', t'', d'_1, \sigma')$;
 - (ii) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta)$;
 - (iii) $\exists T'', \sigma''. (T', \sigma') \Rightarrow_{p.B}^\mu (T'', \sigma'') \wedge \text{closed}(\mu.S, \sigma'')$;
 - (iv) if $p.B(t'') = (n'', \delta'', \widehat{W}'')$, then:
 - $\exists \widehat{W}_1''. (T', t'', d'_1, \sigma') \xrightarrow[\delta'']{\tau}^{n''} \widehat{W}_1'' \wedge \widehat{W}'' \xrightarrow[\delta'']{\delta''} \widehat{W}_1''$; (* redo executions of t'' in buffer *)
 - For any \widehat{W}_1'' , if $(T', t'', d'_1, \sigma') \xrightarrow[\delta'']{\tau}^{n''} \widehat{W}_1''$ and $\widehat{W}'' \xrightarrow[\delta'']{\delta''} \widehat{W}_1''$, then

$$((T', t'', d', \Sigma'), \text{emp}) \preceq_{p', n''}^{j, \mu, E} (\widehat{W}_1'', \text{emp}, \delta'')$$
;
 - else (* no execution of t'' in buffer *)

$$((T', t'', d', \Sigma'), \text{emp}) \preceq_{p', 0}^{j, \mu, E} ((T', t'', d'_1, \sigma'), \text{emp}, \text{emp});$$
 or

(b) (* switch point not match *)

$\exists \widehat{W}', \delta, n, j$, for any $t'' \in \text{dom}(\mathbb{T}')$, such that

(i) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}'$, ($n = 0 \implies j < i$), $o \in E$; (* synchronization eliminated must in E *)

(ii) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta)$;

(iii) (* undo the executions of current thread, and rollback to the last switch point *)

there exists $B = p.B\{t \leadsto (n_0 + n, \delta_r \cup \delta_0 \cup \delta, \widehat{W}')\}$, and $p' = (p.\widehat{W}^{t''}, B \setminus \{t''\})$ such that:

if $B(t'') = (n'', \delta'', \widehat{W}'')$, then

• $\exists \widehat{W}_1'' . p.\widehat{W}^{t''} \xrightarrow[\delta'']{\tau} \widehat{W}_1'' \wedge \widehat{W}'' \xrightarrow{\delta''} \widehat{W}_1''$; (* redo executions of t'' in buffer *)

• For any \widehat{W}_1'' , if $p.\widehat{W}^{t''} \xrightarrow[\delta'']{\tau} \widehat{W}_1''$ and $\widehat{W}'' \xrightarrow{\delta''} \widehat{W}_1''$, then

$((\mathbb{T}', t'', \mathbb{d}', \Sigma'), \text{emp}) \preceq_{p', n''}^{j, \mu, E} (\widehat{W}_1'', \text{emp}, \delta'')$;

else (* no execution of t'' in buffer *)

$((\mathbb{T}', t'', \mathbb{d}', \Sigma'), \text{emp}) \preceq_{p', 0}^{j, \mu, E} (p.\widehat{W}^{t''}, \text{emp}, \text{emp})$;

(4) if $\widehat{W} \xrightarrow[\text{emp}]{\tau} \text{done}$, then $\exists \widehat{W}', \delta$.

(a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}'$, $\widehat{W}' \xrightarrow[\text{emp}]{\tau} \text{done}$;

(b) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta)$;

Definition 4 (Sequential Compiler Correctness). $\text{Correct}(\text{SeqComp}, sl, tl, \mathcal{R})$ holds iff

for any γ, π, ge, ge' and E , if $\text{SeqComp.CodeT}(\gamma, E) = \pi$, $\lfloor \text{SeqComp}.\varphi \rfloor(ge) = ge'$, then there exists M_0, \dots, M_n such that:

- (1) $M_0 = \{(sl, ge, \gamma)\}$ and $M_n = \{(tl, ge', \pi)\}$; and
- (2) $\forall i \in [0, n)$, $\exists R \in \mathcal{R}$, $(M_i, M_{i+1}) \in R(E)$.

Theorem 5 (Final Theorem). For any $\text{SeqComp}_1, \dots, \text{SeqComp}_m, sl_1, \dots, sl_m, tl_1, \dots, tl_m$ such that $\forall i \in \{1, \dots, m\}$, we have $\text{Correct}(\text{SeqComp}_i, sl_i, tl_i, \mathcal{R})$, and for any $f_1, \dots, f_n, \varphi, E = E_1 \cup \dots \cup E_m$, $\Gamma = \{(sl_1, ge_1, \gamma_1), \dots, (sl_m, ge_m, \gamma_m)\}$ and $\Pi = \{(tl_1, ge'_1, \pi_1), \dots, (tl_m, ge'_m, \pi_m)\}$, if

- (1) $\forall i \in \{1, \dots, m\}$. $(\text{SeqComp}_i.\text{CodeT}(\gamma_i, E_i) = \pi_i) \wedge \text{injective}(\varphi)$
 $\wedge (\text{SeqComp}_i.\varphi = \varphi) \wedge \lfloor \varphi \rfloor(ge_i) = ge'_i \wedge \{(sl_i, ge_i, \gamma_i), (tl_i, ge'_i, \pi_i)\} \in \text{Self}(\mathcal{R})$;
- (2) $\text{Safe}(\text{let } \Gamma \text{ in } f_1 \parallel \dots \parallel f_n)$, $\text{DRF}(\text{let } \Gamma \text{ in } f_1 \parallel \dots \parallel f_n)$;
- (3) $\text{AC}(\text{let } \Gamma \text{ in } f_1 \parallel \dots \parallel f_n, E)$; (* the analysis result is correct *)
- (4) $\forall i \in \{1, \dots, m\}$. $\text{ReachClose}(sl_i, ge_i, \gamma_i)$;

then $\text{let } \Gamma \text{ in } f_1 \parallel \dots \parallel f_n \sqsupseteq \text{let } \Pi \text{ in } f_1 \parallel \dots \parallel f_n$.

1 ABSTRACT PROGRAMMING LANGUAGE WITH IDENTIFIED SYNCHRONIZATION

In order to support verifying lock elision, we need to specify which synchronizations can be eliminated. we give a general framework for verifying lock elision optimization. Here, the compiler takes two arguments as the input: the first argument is the source code, and the second is the result of analysis declaring which synchronization operations can be eliminated. We use Figure. 2 to show the compilation process.

Here, we give each `lock()` and `unlock()` operation an unique identifier. The result of the analysis is a set of pairs of the identifiers of `lock()` and `unlock()` operations. The compiler does lock elision according to the result of the analysis. In this example, the synchronization operations identified `id1`, `id2`, `id5`, `id6` will be eliminated by compiler. In the target code, we can find the synchronization operations identified `id1`, `id2`, `id5` and `id6` are eliminated. The code transformation about lock elision in compiler is trivial. It only need to eliminate

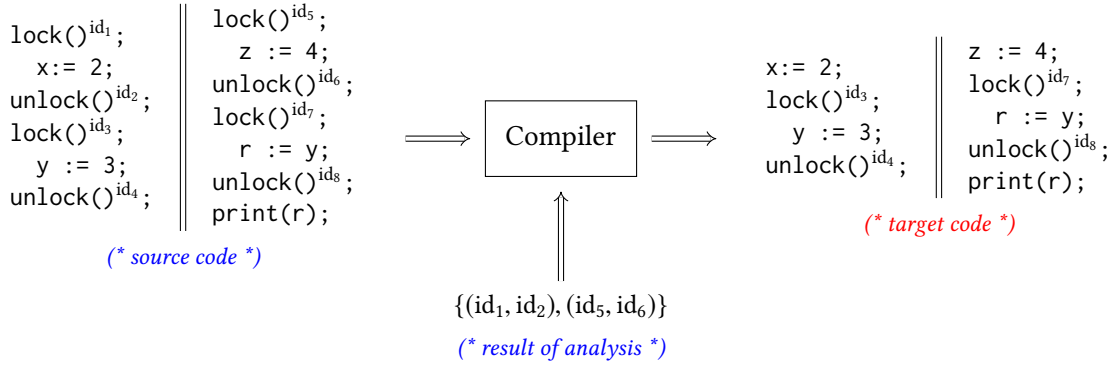


Fig. 2. The compilation process with lock elision

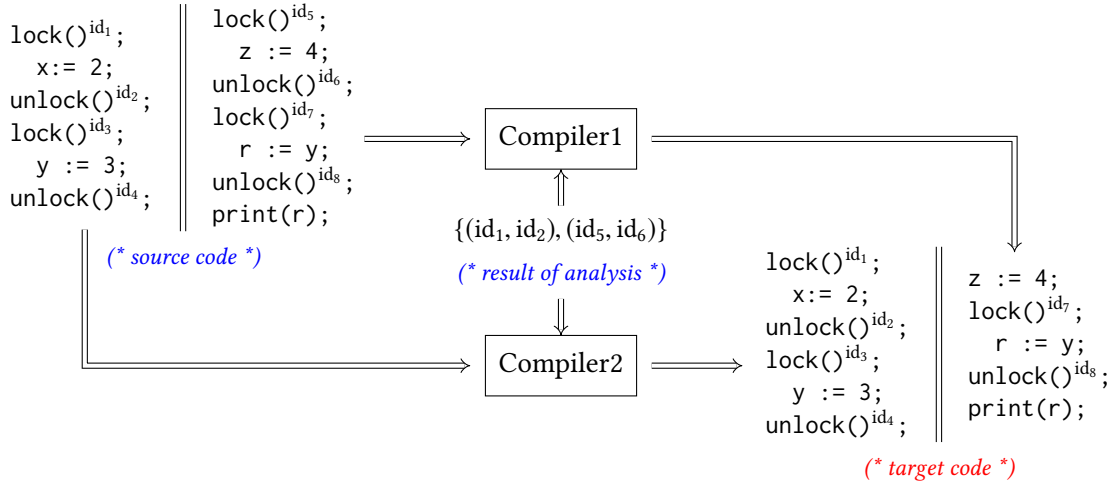


Fig. 3. The separate compilation process with lock elision

the synchronization operations according to the analysis result. Figure 3 shows the lock elision in separate compilation. Here, only Compiler1 does lock elision.

1.1 Operational Semantics

We can find that the key to support the compilation process is given each synchronization operation an unique identifier. However, the abstract language defined in CASCompCert doesn't distinguish each synchronization operation in the program. So, in this subsection, we will make some modification to the language defined in CASCompCert. The first is the definition of *Msg*. Entering and exiting the atomic block will not only produce *EntAtom* and *ExtAtom* event, as well as their identifiers. The definitions of abstract concurrent language and its state are presented in Figure 4 and 5.

(Entry)	$f \in \text{String}$
(Prog)	$P, \mathbb{P} ::= \mathbf{let} \Pi \mathbf{in} f_1 \parallel \dots \parallel f_n$
(GEnv)	$ge \in \text{Addr} \rightarrow_{\text{fin}} \text{Val}$
(MdSet)	$\Pi, \Gamma ::= \{(tl_1, ge_1, \pi_1), \dots, (tl_m, ge_m, \pi_m)\}$
(Lang)	$tl, sl ::= (\text{Module}, \text{Core}, \text{InitCore}, \mapsto)$
(Module)	$\pi, \gamma ::= \dots$
(Core)	$\kappa, \mathbb{K} ::= \dots$
InitCore	$\in \text{Module} \rightarrow \text{Entry} \rightarrow \text{Core}$
\mapsto	$\in \text{FList} \times (\text{Core} \times \text{State}) \rightarrow$ $\mathcal{P}((\text{Msg} \times \text{FtPrt}) \times ((\text{Core} \times \text{State}) \cup \mathbf{abort}))$
(ThrdID)	$t \in \mathbb{N}$
(Addr)	$l ::= \dots$
(Val)	$v ::= l \mid \dots$
(FList)	$F, \mathbb{F} \in \mathcal{P}^\omega(\text{Addr})$
(State)	$\sigma, \Sigma \in \text{Addr} \rightarrow_{\text{fin}} \text{Val}$
(FtPrt)	$\delta, \Delta ::= (rs, ws) \quad \text{where } rs, ws \in \mathcal{P}(\text{Addr})$
(Msg)	$\iota ::= \tau \mid e \mid \text{ret} \mid \text{id.EntA} \mid \text{id.ExtA} \quad (\text{where } \text{id} \in \mathbb{N} \setminus \{0\})$
(Event)	$e ::= \dots$
(Config)	$\phi, \Phi ::= (\kappa, \sigma) \mid \mathbf{abort}$

Fig. 4. The abstract concurrent language

(World)	$W, \mathbb{W} ::= (T, t, d, \sigma)$	(AtomBit)	$d ::= \text{id} \mid 0$
(NPWorld)	$\widehat{W}, \widehat{\mathbb{W}} ::= (T, t, \mathbb{d}, \sigma)$	(AtomBits)	$\mathbb{d} ::= \{t_1 \rightsquigarrow d_1, \dots, t_n \rightsquigarrow d_n\}$
(GMsg)	$o ::= \tau \mid e \mid \text{sw} \mid \text{id.EntA} \mid \text{id.ExtA}$		
(ThrdPool)	$T, \mathbb{T} ::= \{t_1 \rightsquigarrow (tl_1, F_1, \kappa_1), \dots, t_n \rightsquigarrow (tl_n, F_n, \kappa_n)\}$		

Fig. 5. The state of abstract concurrent language

We can introduce *identified synchronization* that is in the form of $\text{lock}()^{\text{id}}$ and $\text{unlock}()^{\text{id}}$. And their semantics can be defined as:

$$\frac{}{F \vdash (\text{lock}()^{\text{id}}, \sigma) \xrightarrow[\text{emp}]{\text{id.EntA}} (\mathbf{skip}, \sigma)} \qquad \frac{}{F \vdash (\text{unlock}()^{\text{id}}, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (\mathbf{skip}, \sigma)}$$

$$\begin{array}{c}
 \frac{T(t) = (tl, F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau} (\kappa', \sigma')}{(T, t, d, \sigma) \xrightarrow[\delta]{\tau} (T\{t \rightsquigarrow (tl, F, \kappa')\}, t, d, \sigma')} \quad \tau\text{-step} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (\kappa', \sigma)}{(T, t, 0, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (T\{t \rightsquigarrow (tl, F, \kappa')\}, t, 0, \sigma)} \quad \text{EntAt} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (\kappa', \sigma)}{(T, t, \text{id}_0, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (T\{t \rightsquigarrow (tl, F, \kappa')\}, t, 0, \sigma)} \quad \text{ExtAt} \\
 \\
 \frac{t' \in \text{dom}(T)}{(T, t, 0, \sigma) \xrightarrow[\text{emp}]{\text{sw}} (T, t', 0, \sigma)} \quad \text{Switch} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\text{abort}} \mathbf{abort}}{(T, t, 0, \sigma) \xrightarrow[\delta]{\tau} \mathbf{abort}} \quad \text{Abort} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad t' \in \text{dom}(T \setminus t) \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{ret}} (\kappa', \sigma)}{(T, t, 0, \sigma) \xrightarrow[\text{emp}]{\text{sw}} (T \setminus t, t', 0, \sigma)} \quad \text{Term} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad \text{dom}(T) = \{t\} \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{ret}} (\kappa', \sigma)}{(T, t, 0, \sigma) \xrightarrow[\text{emp}]{\tau} \mathbf{done}} \quad \text{Done}
 \end{array}$$

Fig. 6. Modified preemptive global semantics

$$\begin{array}{c}
 \frac{T(t) = (tl, F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau} (\kappa', \sigma') \quad T' = T\{t \rightsquigarrow (tl, F, \kappa')\}}{(T, t, \text{dl}, \sigma) \xrightarrow[\delta]{\tau} (T', t, \text{dl}, \sigma')} \quad \tau\text{-step}_{\text{np}} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad \text{dl}(t) = 0 \quad t' \in \text{dom}(T) \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (\kappa', \sigma) \quad T' = T\{t \rightsquigarrow (tl, F, \kappa')\}}{(T, t, \text{dl}, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (T', t', \text{dl}\{t \rightsquigarrow \text{id}\}, \sigma)} \quad \text{EntAt}_{\text{np}} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad \text{dl}(t) = \text{id}_0 \quad t' \in \text{dom}(T) \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (\kappa', \sigma) \quad T' = T\{t \rightsquigarrow (tl, F, \kappa')\}}{(T, t, \text{dl}, \sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (T', t', \text{dl}\{t \rightsquigarrow 0\}, \sigma)} \quad \text{ExtAt}_{\text{np}} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\text{abort}} \mathbf{abort}}{(T, t, \text{dl}, \sigma) \xrightarrow[\delta]{\tau} \mathbf{abort}} \quad \text{Abort}_{\text{np}} \\
 \\
 \frac{T(t) = (tl, F, \kappa) \quad \text{dl}(t) = 0 \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{ret}} (\kappa', \sigma) \quad t' \in \text{dom}(T \setminus t)}{(T, t, \text{dl}, \sigma) \xrightarrow[\text{emp}]{\text{sw}} (T \setminus t, t', \text{dl} \setminus t, \sigma)} \quad \text{Term}_{\text{np}} \\
 \\
 \frac{T(t) = \{t \rightsquigarrow (tl, F, \kappa)\} \quad \text{dl} = \{t \rightsquigarrow 0\} \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{ret}} (\kappa', \sigma)}{(T, t, \text{dl}, \sigma) \xrightarrow[\text{emp}]{\tau} \mathbf{done}} \quad \text{Done}_{\text{np}}
 \end{array}$$

Fig. 7. Modified non-preemptive global semantics

We present the operation semantics in Figure. 6 and 7. Here, we only modify the rules for entering and exiting atomic block. We mark the rule modified in blue.

$$\begin{array}{c}
\frac{P \xRightarrow{\text{load}} W \quad W \Rightarrow^* W' \quad (W', E) \Longrightarrow \text{WA}}{(P, E) \Longrightarrow \text{WA}} \quad \frac{(W, E) \Longrightarrow \text{WA}_0 \vee (W, E) \Longrightarrow \text{WA}_1}{(W, E) \Longrightarrow \text{WA}} \\
\\
\frac{\begin{array}{c} t_1 \neq t_2 \quad \delta_1 \frown \delta_2 \quad (\text{id}_1, _) \in E \\ \text{predict}(W, t_1, (\delta_1, \text{id}_1)) \quad \text{predict}(W, t_2, (\delta_2, d_2)) \end{array}}{(W, E) \Longrightarrow \text{WA}_0} \\
\\
\frac{\begin{array}{c} W \Rightarrow^* W_1 \quad W_1 \xRightarrow[\text{emp}]{\text{id}'.\text{ExtA}} W' \quad W_1.d = \text{id} \\ ((\text{id}, \text{id}_1) \in E \wedge \text{id}_1 \neq \text{id}') \vee ((\text{id}_2, \text{id}') \in E \wedge \text{id}_2 \neq \text{id}) \end{array}}{(W, E) \Longrightarrow \text{WA}_1}
\end{array}$$

$$\begin{array}{c}
\frac{\hat{P} \xRightarrow{\text{load}} \hat{W} \quad \hat{W} \Rightarrow^* \hat{W}' \quad (\hat{W}', E) \Longrightarrow \text{WA}}{(\hat{P}, E) \Longrightarrow \text{WA}} \quad \frac{(\hat{W}, E) \Longrightarrow \text{WA}_0 \vee (\hat{W}, E) \Longrightarrow \text{WA}_1}{(\hat{W}, E) \Longrightarrow \text{WA}} \\
\\
\frac{\begin{array}{c} \hat{W} \xRightarrow[\text{emp}]{o} \hat{W}' \quad o \neq \tau \quad t_1 \neq t_2 \quad \delta_1 \frown \delta_2 \quad (\text{id}_1, _) \in E \\ \text{NPpredict}(\hat{W}', t_1, (\delta_1, \text{id}_1)) \quad \text{NPpredict}(\hat{W}', t_2, (\delta_2, d_2)) \end{array}}{(\hat{W}, E) \Longrightarrow \text{WA}_0} \\
\\
\frac{\begin{array}{c} \hat{W} \Rightarrow^* \hat{W}_1^t \quad \hat{W}_1^t \xRightarrow[\text{emp}]{\text{id}'.\text{ExtA}} \hat{W}' \quad \hat{W}_1.d(t) = \text{id} \\ ((\text{id}, \text{id}_1) \in E \wedge \text{id}_1 \neq \text{id}') \vee ((\text{id}_2, \text{id}') \in E \wedge \text{id}_2 \neq \text{id}) \end{array}}{(\hat{W}, E) \Longrightarrow \text{WA}_1}
\end{array}$$

Fig. 8. Wrong Analysis Result

1.2 Properties of Program Ensured by Analysis Result

In order to describe the properties of program ensured by analysis result. We first need to define the result of analysis below formally. *SyncEA* means synchronization elision analysis.

$$(\text{SyncEA}) \quad E ::= \{(\text{id}_1, \text{id}'_1), \dots, (\text{id}_n, \text{id}'_n)\}$$

Then, we define the properties of program ensured by analysis result. The analysis result ensures two properties:

- (1) The code execution in synchronization that can be eliminated will not conflict with other threads execution;
- (2) If an `lock()` operation is eliminated, the corresponding `unlock()` operation will also be eliminated.

Similar with the data race free property. We define " $(P, E) \Longrightarrow \text{WA}$ " to represent the analysis result is wrong. In assumption, we require the analysis result is correct. Full definitions can be found in Figure. 8.

$$(\text{Analysis Correct}) \quad \text{AC}(P, E) ::= \neg((P, E) \Longrightarrow \text{WA})$$

2 IDEA TO ESTABLISH SIMULATION RELATION BETWEEN SOURCE AND TARGET WHEN SWITCH POINT DOES NOT MATCH

We use the example shown in Fig. 1 to illustrate our idea to establish simulation relation between source and target programs. We can construct the target program's execution from the given source execution as below:

- (1) The source program first executes the code $x := 2$ in T1, and the target program also execute the same code in step (1).

Source: T1: lock(); $x := 2$; unlock();

Target: T1: $x := 2$;

Buffer:

- (2) In step (2), the source switches to thread T2 to execute, but the target can't switch to T2, because there is no switch point in target at this moment. So, we **undo** the execution of T1, and let the T2 executes.

Source: T1: lock(); $x := 2$; unlock(); T2: $r2 := 2$; lock(); $y := r2 + 1$; unlock();

Target: T2: $r2 := 2$; lock(); $y := r2 + 1$; unlock();

Buffer: T1: $x := 2$;

- (3) In step (3), the source switches to thread T1 again and executes $r1 := y$. The target can also switch to T1 and **redoes** $x := 2$ in buffer again.

Source: T1: lock(); $x := 2$; unlock(); T2: $r2 := 2$; lock(); $y := r2 + 1$; unlock();
T1: lock(); $r1 := y$; unlock();

Target: T2: $r2 := 2$; lock(); $y := r2 + 1$; unlock(); T1: $x := 2$; lock(); $r1 := y$; unlock();

Buffer:

- (4) Finally, the source and target both switch to T2 and diverge silently.

Source: T1: lock(); $x := 2$; unlock(); T2: $r2 := 2$; lock(); $y := r2 + 1$; unlock();
T1: lock(); $r1 := y$; unlock(); T2: silent diverge;

Target: T2: $r2 := 2$; lock(); $y := r2 + 1$; unlock(); T1: $x := 2$; lock(); $r1 := y$; unlock();
T2: T2: silent diverge;

Buffer:

Note that our **undo** and **redo** mechanism to construct target program executions is to simulate the reordering of thread executions. So, our method requires that the source program should be data-race-free, otherwise the execution of program before and after reordering will not be equal. We give the following counterexample:

$x := 3$; lock(); $y := 2$; unlock(); while(true);	\parallel	$r2 := x$; print(r2);	$\xRightarrow{\text{lock elision}}$	$x := 3$; $y := 2$; while(true);	\parallel	$r2 := x$; print(r2);
--	-------------	---------------------------	-------------------------------------	--	-------------	---------------------------

We give a source program's execution below:

T1: $x := 3$; T1: $y := 2$; T2: $r2 := x$; T2: print(r2) (* output 3 *); T1: silent diverge

By using our method, we construct a target program's execution.

T2: $r2 := x$; T2: print(r2) (* output 0 *); T1: $x := 3$; T1: $y := 2$; T1: silent diverge

Supposing the variables x and y are 0 in the initial state. We can find that the source program will output 3 and the target program will output 0. Although our method is correct under the assumption that the source program is data-race-free, we can find that the source and target program's behaviors will not be equivalent, such as the example shown above. The target program can't output 3, but source program can.

$$S_1 \parallel S_2 \sqsupseteq T_1 \parallel T_2$$

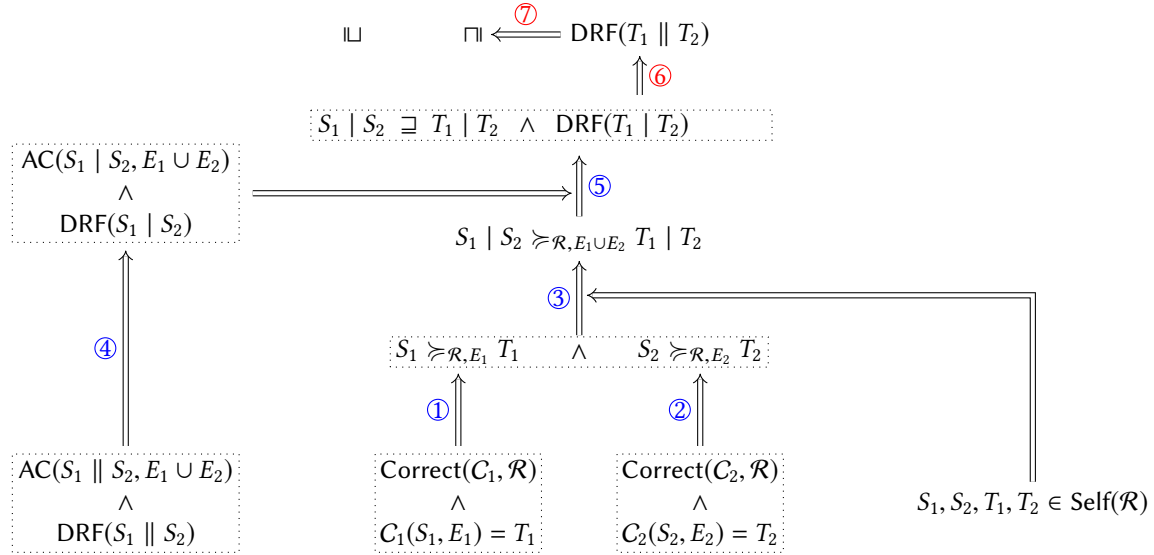


Fig. 9. Proof Sketch

3 ESTABLISHING COMPILER CORRECTNESS USING RUSC

We use the theory of RUSC proposed in CompCertM [4] to establish the proof of the compiler correctness.

Proof Sketch. We use Figure. 9 to show the structure of establishing compiler correctness using RUSC. The compilation and the compiler correctness ensure the refinement under self-related contexts between source and target, shown as ① and ②. Here, the compiler takes the source code and analysis result as input. The RUSC relation is compositional, so we get the whole source and target program have refinement under self-related contexts, shown as ③. The target code produced by compiler not only preserves the behaviors of the source code, but also the data-race-free and analysis correctness property, shown as ⑤.

Details about achieving ①, ②, and ③ can be found in Figure. 10. Here, the relations between the input and output of each pass is defined from R_1 to R_5 , which parameters with an analysis result as argument.

Details. The module Module in RUSC is instantiated as high- and low-level module set Γ and Π . And the linking operation \oplus in RUSC is instantiated as the set union operation \cup .

Here, because we have an additional input *analysis result* for compiler. The module set relation R is defined as a mapping from *analysis result* to a set of pairs of module set ($MdSet$).

$$(MdSetRel) \quad R \in SyncEA \rightarrow \mathcal{P}(MdSet \times MdSet)$$

And a module set relation is well-defined, if it satisfies *VerComp* and *Adequacy* properties. In Sec 4, we define a module local simulation to instantiate the module set relation. The module set relation will be instantiated as that each source module has a module local simulation with its corresponding target module. The module local simulation takes an *analysis result* as parameter. The instantiation can be found in Sec. 4.

Definition 6 (Well-defined Module Set Relation). $wd(R)$ holds, iff the following holds:

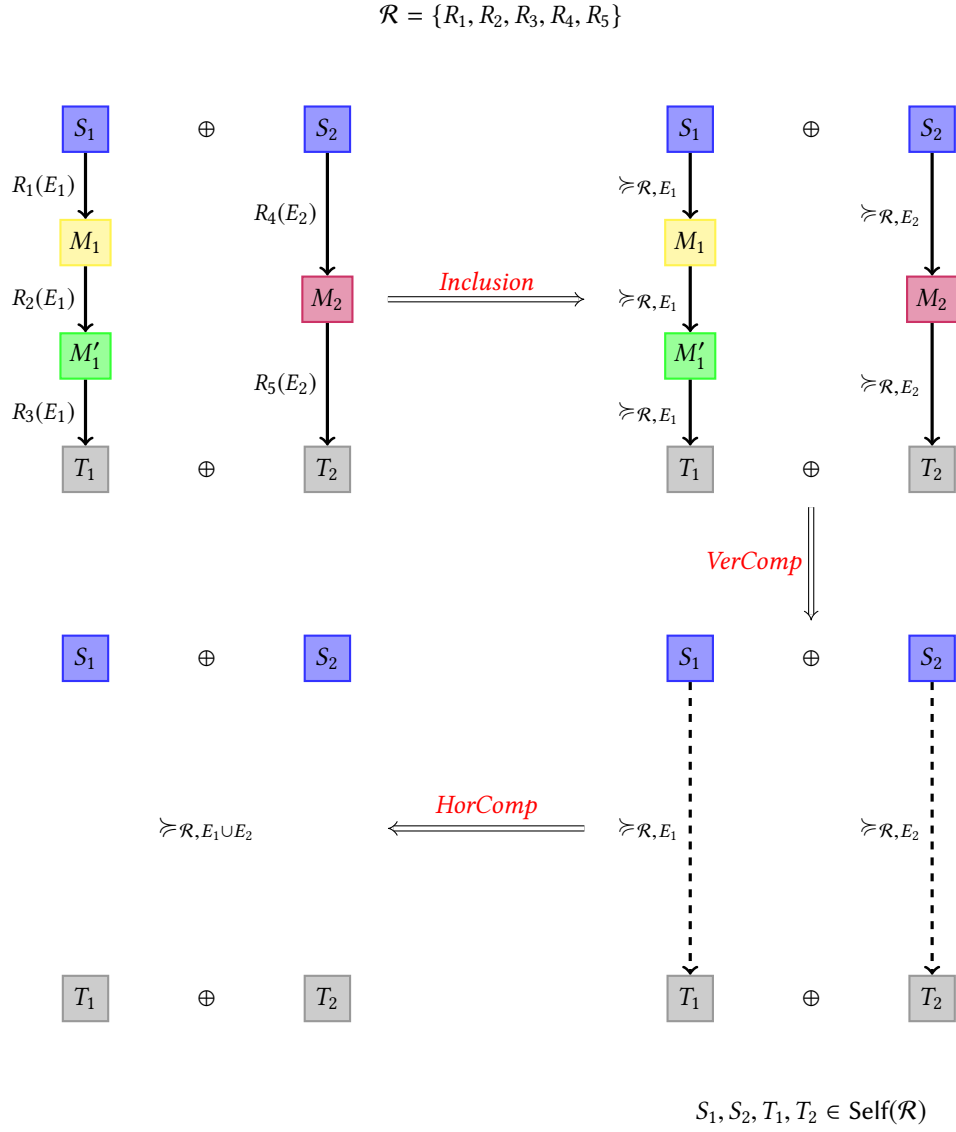


Fig. 10. Compositionality achievement

- (1) For any $\Gamma, \Gamma', \Pi, \Pi' \in \text{MdSet}$, $E, E' \in \text{SyncEA}$,
if $(\Gamma, \Pi) \in R(E)$ and $(\Gamma', \Pi') \in R(E')$, then $(\Gamma \cup \Gamma', \Pi \cup \Pi') \in R(E \cup E')$;
- (2) For any $\Gamma, \Pi \in \text{MdSet}$, $E \in \text{SyncEA}$,
if $(\Gamma, \Pi) \in R(E)$, then for any f_1, \dots, f_n , **let** Γ **in** $f_1 \mid \dots \mid f_n \supseteq_E$ **let** Π **in** $f_1 \mid \dots \mid f_n$.

Here, $\hat{\mathbb{P}} \supseteq_E \hat{P}$ (defined in Fig. 11) is an auxiliary refinement relation, which means the target program not only preserves the event trace of source program, but also its safety, data-race-free, analysis correctness and reach

$$\begin{aligned}
\text{RC_Trans}(\widehat{W}, \mathbb{S}) &\stackrel{\text{def}}{=} (\forall \widehat{W}'. (\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}') \implies \Delta \subseteq (\text{curF}(\widehat{W}) \cup \mathbb{S})) \\
&\quad \vee (\forall \widehat{W}'. (\widehat{W} \xrightarrow[\text{emp}]{o} \widehat{W}' \wedge o \neq \tau) \implies \text{closed}(\widehat{W}', \mathbb{S})) \\
\text{RC_prog}(\hat{P}) &\stackrel{\text{def}}{=} \forall \widehat{W}, \widehat{W}', \mathbb{S}. (\hat{P} \xrightarrow{\text{load}} \widehat{W} \wedge \mathbb{S} = \text{dom}(\widehat{W}. \Sigma) \wedge \widehat{W} \Rightarrow^* \widehat{W}') \implies \text{RC_Trans}(\widehat{W}', \mathbb{S}) \\
\text{ProgProp}(\hat{P}, E) &\stackrel{\text{def}}{=} \text{NPDRF}(\hat{P}) \wedge \text{Safe}(\hat{P}) \wedge \text{RC_prog}(\hat{P}) \wedge \text{AC}(\hat{P}, E) \\
\hat{P} \sqsupseteq \hat{P} &\stackrel{\text{def}}{=} \forall \mathcal{B}. \text{PEtr}(\hat{P}, \mathcal{B}) \implies \text{PEtr}(\hat{P}, \mathcal{B}) \\
\hat{P} \sqsupseteq_E \hat{P} &\stackrel{\text{def}}{=} \text{ProgProp}(\hat{P}, E) \implies (\text{ProgProp}(\hat{P}, E) \wedge \hat{P} \sqsupseteq \hat{P})
\end{aligned}$$

Fig. 11. Auxiliary Refinement Relation

closed transition properties.

$$\mathcal{R} \in \mathcal{P}(\{R \mid \text{wd}(R)\})$$

We define the refinement relation under self-related contexts below:

$$\begin{aligned}
\Gamma \succ_{\mathcal{R}, E} \Pi &\stackrel{\text{def}}{=} \forall c_1, c_2 \in \text{Self}(\mathcal{R}), f_1, \dots, f_n. \\
&\quad \mathbf{let} (c_1 \cup \Gamma \cup c_2) \mathbf{in} f_1 \mid \dots \mid f_n \supseteq_E \mathbf{let} (c_1 \cup \Pi \cup c_2) \mathbf{in} f_1 \mid \dots \mid f_n \\
\text{Self}(\mathcal{R}) &\stackrel{\text{def}}{=} \{c \in \text{MdSet} \mid \forall R \in \mathcal{R}, E \in \text{SyncEA}. (c, c) \in R(E)\}
\end{aligned}$$

We can prove that the refinement relation under self-related contexts satisfies the following properties.

Lemma 7 (Inclusion). For any $\Gamma, \Pi, R \in \mathcal{R}, E$, if $(\Gamma, \Pi) \in R(E)$, then $\Gamma \succ_{\mathcal{R}, E} \Pi$.

Lemma 8 (Adequacy). For any Γ, Π, E , if $\Gamma \succ_{\mathcal{R}, E} \Pi$, then

$$\forall f_1, \dots, f_n. \mathbf{let} \Gamma \mathbf{in} f_1 \mid \dots \mid f_n \supseteq_E \mathbf{let} \Pi \mathbf{in} f_1 \mid \dots \mid f_n.$$

Lemma 9 (VerComp). For any Γ, Π', Π, E , if $\Gamma \succ_{\mathcal{R}, E} \Pi'$ and $\Pi' \succ_{\mathcal{R}, E} \Pi$, then $\Gamma \succ_{\mathcal{R}, E} \Pi$.

Lemma 10 (HorComp).

For any $\Gamma, \Gamma', \Pi, \Pi' \in \text{Self}(\mathcal{R}), E, E'$, if $\Gamma \succ_{\mathcal{R}, E} \Pi$ and $\Gamma' \succ_{\mathcal{R}, E'} \Pi'$, then $(\Gamma \cup \Gamma') \succ_{\mathcal{R}, E \cup E'} (\Pi \cup \Pi')$.

Lemma 11 (SelfComp). For any $\Gamma, \Gamma' \in \text{Self}(\mathcal{R})$, $\Gamma \cup \Gamma' \in \text{Self}(\mathcal{R})$ holds.

We mode a sequential compiler SeqComp as following:

$$\begin{aligned}
\text{SeqComp} &::= (\text{CodeT}, \varphi), \\
&\quad \text{where CodeT} \in (\text{Module} \times \text{SyncEA}) \rightarrow \text{Module}, \varphi \in \text{Addr} \rightarrow \text{Addr}
\end{aligned}$$

Here, as we have introduced, the inputs of code transformation CodeT are source module and analysis result.

Definition 12 (Sequential Compiler Correctness). $\text{Correct}(\text{SeqComp}, sl, tl, \mathcal{R})$ holds iff

for any γ, π, ge, ge' and E , if $\text{SeqComp.CodeT}(\gamma, E) = \pi$, $\lfloor \text{SeqComp}.\varphi \rfloor(ge) = ge'$, then there exists M_0, \dots, M_n such that:

- (1) $M_0 = \{(sl, ge, \gamma)\}$ and $M_n = \{(tl, ge', \pi)\}$; and
- (2) $\forall i \in [0, n), \exists R \in \mathcal{R}, (M_i, M_{i+1}) \in R(E)$.

Theorem 13 (Final Theorem). For any $\text{SeqComp}_1, \dots, \text{SeqComp}_m, sl_1, \dots, sl_m, tl_1, \dots, tl_m$ such that $\forall i \in \{1, \dots, m\}$, we have $\text{Correct}(\text{SeqComp}_i, sl_i, tl_i, \mathcal{R})$, and for any $f_1, \dots, f_n, \varphi, E = E_1 \cup \dots \cup E_m, \Gamma = \{(sl_1, ge_1, \gamma_1), \dots, (sl_m, ge_m, \gamma_m)\}$ and $\Pi = \{(tl_1, ge'_1, \pi_1), \dots, (tl_m, ge'_m, \pi_m)\}$, if

- (1) $\forall i \in \{1, \dots, m\}. (\text{SeqComp}_i.\text{CodeT}(\gamma_i, E_i) = \pi_i) \wedge \text{injective}(\varphi)$
 $\wedge (\text{SeqComp}_i.\varphi = \varphi) \wedge \lfloor \varphi \rfloor(ge_i) = ge'_i \wedge \{(sl_i, ge_i, \gamma_i), (tl_i, ge'_i, \pi_i)\} \in \text{Self}(\mathcal{R});$
- (2) $\text{Safe}(\text{let } \Gamma \text{ in } f_1 \parallel \dots \parallel f_n), \text{DRF}(\text{let } \Gamma \text{ in } f_1 \parallel \dots \parallel f_n);$
- (3) $\text{AC}(\text{let } \Gamma \text{ in } f_1 \parallel \dots \parallel f_n, E);$ (* the analysis result is correct *)
- (4) $\forall i \in \{1, \dots, m\}. \text{ReachClose}(sl_i, ge_i, \gamma_i);$

then $\text{let } \Gamma \text{ in } f_1 \parallel \dots \parallel f_n \sqsupseteq \text{let } \Pi \text{ in } f_1 \parallel \dots \parallel f_n.$

PROOF. According to assumption (1), the definition of sequential compilation correctness (Def. 12), and Lemma 14, we get:

$$\forall i \in \{1, \dots, m\}. \{(sl_i, ge_i, \gamma_i)\} \succ_{\mathcal{R}, E_i} \{(tl_i, ge'_i, \pi_i)\} \quad (3.1)$$

According to assumption (3.1), (2) and Lemma 9 (HorComp), we get:

$$\{(sl_1, ge_1, \gamma_1), \dots, (sl_m, ge_m, \gamma_m)\} \succ_{\mathcal{R}, E} \{(tl_1, ge'_1, \pi_1), \dots, (tl_m, ge'_m, \pi_m)\}$$

So that we get:

$$\Gamma \succ_{\mathcal{R}, E} \Pi \quad (3.2)$$

From (3.2) and Lemma 8, the following holds:

$$\text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n \sqsupseteq_E \text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n \quad (3.3)$$

According to Lemma 16, 17, 18, 15, and (3.3), we get:

$$\text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n \sqsupseteq \text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n$$

And the safety, no-data-race, reach closed transition and analysis correctness properties are also preserved during compilation.

$$\begin{aligned} & \text{NPDRF}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n), \text{Safe}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n), \\ & \text{RC_prog}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n), \text{AC}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n, E) \end{aligned}$$

The CASCompCert proof has told us that if the source and target program have refinement relation under non-preemptive semantics and the source program is data-race-free (Lemma 19), they also have refinement relation under preemptive semantics. So, we finish the proof. \square

Lemma 14 (Modular Correctness). For any $\text{SeqComp}, sl, tl, \gamma, \pi, ge, ge', E, \mathcal{R}$, if $\text{Correct}(\text{SeqComp}, sl, tl, \mathcal{R})$, $\text{SeqComp}.\text{CodeT}(\gamma, E) = \pi$, $\lfloor \text{SeqComp}.\varphi \rfloor(ge) = ge'$, then $\{(sl, ge, \gamma)\} \succ_{\mathcal{R}, E} \{(tl, ge', \pi)\}$.

PROOF. This lemma can be proved by applying Lemma 7 and 9. \square

Lemma 15 (ReachClosed Program). For any $i \in \{1, \dots, m\}. \text{ReachClose}(sl_i, ge_i, \gamma_i)$ and $\Gamma = \{(sl_1, ge_1, \gamma_1), \dots, (sl_m, ge_m, \gamma_m)\}$, then $\text{RC_prog}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n).$

Lemma 16. For any f_1, \dots, f_n and Π , if $\text{Safe}(\text{let } \Pi \text{ in } f_1 \parallel \dots \parallel f_n)$, then $\text{Safe}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n).$

Lemma 17. For any f_1, \dots, f_n and Π , if $\text{DRF}(\text{let } \Pi \text{ in } f_1 \parallel \dots \parallel f_n)$, then $\text{NPDRF}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n).$

Lemma 18. For any f_1, \dots, f_n, E and Π , if $\text{AC}(\text{let } \Pi \text{ in } f_1 \parallel \dots \parallel f_n, E)$, then $\text{AC}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n, E).$

Lemma 19 (Semantics Equivalence).

For any Π, f_1, \dots, f_m , if $\text{DRF}(\text{let } \Pi \text{ in } f_1 \parallel \dots \parallel f_m)$, then $\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_m \approx \text{let } \Pi \text{ in } f_1 \parallel \dots \parallel f_m.$

4 MODULE SET RELATION

In this section, we define a simulation relation to instantiate the module set relation R introduced in the previous section. The simulation relation is based on the *footprint-preserving* simulation defined in CASCompCert and can prove the correctness of lock elision optimization pass.

$$\begin{aligned}
f\{\mathbb{S}\} &\stackrel{\text{def}}{=} \{l' \mid \exists l. l \in \mathbb{S} \wedge f(l) = l'\} \\
f|_{\mathbb{S}} &\stackrel{\text{def}}{=} \{(l, f(l)) \mid l \in (\mathbb{S} \cap \text{dom}(f))\} \\
\text{FPmatch}(\mu, \Delta, \delta) &\text{ iff } (\delta.rs \cap \mu.S \subseteq \mu.f\{\Delta.rs \cup \Delta.ws\}) \wedge (\delta.ws \cap \mu.S \subseteq \mu.f\{\Delta.ws\}) \\
\widehat{f}(v) &\stackrel{\text{def}}{=} \begin{cases} v, & \text{if } v \notin \text{Addr} \\ f(v), & \text{if } v \in \text{Addr} \wedge v \in \text{dom}(f) \\ \text{undefined}, & \text{otherwise} \end{cases} \\
\text{Inv}(f, \Sigma, \sigma) &\text{ iff } \forall l, l'. (l \in \text{dom}(\Sigma) \wedge f(l) = l') \implies (l' \in \text{dom}(\sigma) \wedge \widehat{f}(\Sigma(l)) = \sigma(l')) \\
\text{HG}(\Delta, \Sigma, \mathbb{F}, \mathbb{S}) &\text{ iff } \Delta \subseteq (\mathbb{F} \cup \mathbb{S}) \wedge \text{closed}(\mathbb{S}, \Sigma) \\
\text{LG}(\mu, (\delta, \sigma, F), (\Delta, \Sigma)) &\text{ iff } \delta \subseteq (F \cup \mu.S) \wedge \text{closed}(\mu.S, \sigma) \wedge \text{FPmatch}(\mu, \Delta, \delta) \wedge \text{Inv}(\mu.f, \Sigma, \sigma) \\
\text{R}(\Sigma, \Sigma', \mathbb{F}, \mathbb{S}) &\text{ iff } (\Sigma \stackrel{\mathbb{F}}{=} \Sigma') \wedge \text{closed}(\mathbb{S}, \Sigma') \wedge \text{forward}(\Sigma, \Sigma') \\
\text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma, \sigma', F)) &\text{ iff } \text{R}(\Sigma, \Sigma', \mathbb{F}, \mu.\mathbb{S}) \wedge \text{R}(\sigma, \sigma', F, \mu.S) \wedge \text{Inv}(\mu.f, \Sigma', \sigma') \\
\lfloor \varphi \rfloor(ge) &\stackrel{\text{def}}{=} \begin{cases} \{(\varphi(l), \widehat{\varphi}(v)) \mid (l, v) \in ge\}, & \text{if } (\text{dom}(ge) \cup (\text{range}(ge) \cap \text{Addr})) \subseteq \text{dom}(\varphi) \\ \text{undefined}, & \text{otherwise} \end{cases} \\
\text{initM}(\varphi, ge, \Sigma, \sigma) &\text{ iff } ge \subseteq \Sigma \wedge \text{closed}(\Sigma) \wedge \text{dom}(\sigma) = \varphi\{\text{dom}(\Sigma)\} \wedge \text{Inv}(\varphi, \Sigma, \sigma) \\
\iota \in E &\stackrel{\text{def}}{=} (\iota = \text{id.EntA} \wedge (\text{id}, _) \in E) \vee (\iota = \text{id.ExtA} \wedge (_, \text{id}) \in E)
\end{aligned}$$

Fig. 12. Footprint matching and rely/guarantee conditions

Local downward simulation. We introduce the triple μ below to record the key information about the shared memory at the source and the target and the thread-local accessing locations in source.

$$\mu \stackrel{\text{def}}{=} (\mathbb{S}, S, f), \quad \text{where } \mathbb{S}, S \in \mathcal{P}(\text{Addr}) \text{ and } f \in \text{Addr} \rightarrow \text{Addr}.$$

Here \mathbb{S} and S specify the shared memory locations at the source and the target respectively. The partial mapping f maps locations at the source level to those at the target. We require μ to be well-formed:

$$\text{wf}(\mu) \text{ iff } \text{injective}(\mu.f) \wedge \text{dom}(\mu.f) = \mu.\mathbb{S} \wedge \mu.f\{\mu.\mathbb{S}\} = \mu.S$$

Below we define $(sl, ge, \gamma) \preceq_{\varphi, E} (tl, ge', \pi)$ to relate the *non-preemptive* executions of the source module (sl, ge, γ) and the target one (tl, ge', π) . The *injective function* φ maps source addresses to the target ones.

Definition 20 (Module-Local Downward Simulation).

$$(sl, ge, \gamma) \preceq_{\varphi, E} (tl, ge', \pi) \text{ iff}$$

- (1) $\lfloor \varphi \rfloor(ge) = ge'$;
- (2) for all $f, \mathbb{k}, \Sigma, \sigma, \mathbb{F}, F$, and $\mu = (\text{dom}(\Sigma), \text{dom}(\sigma), \varphi|_{\text{dom}(\Sigma)})$, if $sl.\text{InitCore}(\gamma, f) = \mathbb{k}, \mathbb{F} \cap \text{dom}(\Sigma) = F \cap \text{dom}(\sigma) = \emptyset$, and $\text{initM}(\varphi, ge, \Sigma, \sigma)$, then there exists $i \in \text{index}, \kappa$ such that:
 $tl.\text{InitCore}(\pi, f) = \kappa$, and $(\mathbb{F}, (\mathbb{k}, \Sigma), \text{emp}) \preceq_{\mu}^{i, E} (F, (\kappa, \sigma), \text{emp})$,
 where $(\mathbb{F}, (\mathbb{k}, \Sigma), \Delta) \preceq_{\mu}^{i, E} (F, (\kappa, \sigma), \delta)$ is defined in Def. 2.

The case that the switch points of source and target program do not match seems a little strange. The target program does not interact with the environment, but we do not use the σ' to establish simulation continuously. The reason is that when source program enters and exits critical section but the target program can't, we will

undo the previous executions of target from the last switch point and redo them when it switches back. When redoing these executions, the state of target program will satisfy σ'' .

Then, we define $\hat{R}(\varphi)$ below as an instantiation of the module set relation:

$$\hat{R}(\varphi) \stackrel{\text{def}}{=} \lambda E. \{(\Gamma, \Pi) \mid |\Gamma| = |\Pi| \wedge (\forall i \in \{1, \dots, |\Gamma|\}. \Gamma(i) = (sl_i, ge_i, \gamma_i) \wedge \Pi(i) = (tl_i, ge'_i, \pi_i) \wedge (\exists E_i \subseteq E. (sl_i, ge_i, \gamma_i) \preceq_{\varphi}^{E_i} (tl_i, ge'_i, \pi_i)) \wedge \text{wd}(sl_i) \wedge \text{wd}(tl_i) \wedge \text{det}(tl_i))\}$$

The subset of E is the set of synchronization pairs that the compiler is responsible to eliminate. The reason why we choose a subset of E is because some passes don't eliminate synchronization, and we require the synchronization in E_i must be eliminated to avoid that the compiler only eliminates only `lock()` or `unlock()` operation of a synchronization. For example, we need to avoid the following situation occurring:

$$\begin{array}{ccc} \text{lock()}^{\text{id}_1}; & & \text{lock()}^{\text{id}_1}; \\ x := 2; & \xrightarrow{\text{compile}} & x := 2; \\ \text{unlock()}^{\text{id}_2}; & & \end{array}$$

We need to prove that $\hat{R}(\varphi)$ is well-defined. A module set relation is well-defined, if it satisfies the *HorComp* and *Adequacy* properties as shown in Def. 6. The injective function φ will be instantiated as *identity*, *extension* and *injection* in the proof of compilation correctness. And the set \mathcal{R} is instantiated as the following form in the proof of compilation correctness.

$$\mathcal{R} \stackrel{\text{def}}{=} \{\hat{R}(\varphi) \mid \varphi \in \{\text{identity}, \text{extension}, \text{injection}\}\}$$

Theorem 21 (HorComp).

For any $\Gamma, \Gamma', \Pi, \Pi', E, E'$, and φ , if $(\Gamma, \Pi) \in \hat{R}(\varphi)(E)$ and $(\Gamma', \Pi') \in \hat{R}(\varphi)(E')$, then $(\Gamma \cup \Gamma', \Pi \cup \Pi') \in \hat{R}(\varphi)(E \cup E')$.

PROOF. The correctness proof of this lemma can be achieved from the definition of $\hat{R}(\varphi)$. The only thing we need to prove that if there exists $E_i \subseteq E$ and $(sl_i, ge_i, \gamma_i) \preceq_{\varphi, E_i} (tl_i, ge'_i, \pi_i)$, then the following holds:

$$\exists E_i \subseteq (E \cup E'). (sl_i, ge_i, \gamma_i) \preceq_{\varphi, E_i} (tl_i, ge'_i, \pi_i)$$

Its correctness proof is straight-forward. □

Theorem 22 (Adequacy). For any Γ, Π, E and φ , if $(\Gamma, \Pi) \in \hat{R}(\varphi)(E)$ holds, then

$$\forall f_1, \dots, f_n. \text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_1 \supseteq_E \text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n$$

PROOF. We first unfold $(\Gamma, \Pi) \in \hat{R}(\varphi)(E)$ according to its definition, and we get that for any $i \in \{1, \dots, |\Gamma|\}$, there exists $E_i \subseteq E$, such that

$$(sl_i, ge_i, \gamma_i) \preceq_{\varphi, E_i} (tl_i, ge'_i, \pi_i) \wedge \text{wd}(sl_i) \wedge \text{wd}(tl_i) \wedge \text{det}(tl_i) \quad (4.1)$$

Let $\hat{\mathbb{P}} = \text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n$, and $\hat{P} = \text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n$. Then we unfold the proof goal by its definition, and do intro:

$$\text{NPDRF}(\hat{\mathbb{P}}) \quad (4.2)$$

$$\text{Safe}(\hat{\mathbb{P}}) \quad (4.3)$$

$$\text{RC_prog}(\hat{\mathbb{P}}) \quad (4.4)$$

$$\text{AC}(\hat{\mathbb{P}}, E) \quad (4.5)$$

And we need to prove that the target program preserves these properties of source program:

$$\hat{\mathbb{P}} \supseteq \hat{P} \wedge \text{NPDRF}(\hat{P}) \wedge \text{Safe}(\hat{P}) \wedge \text{RC_prog}(\hat{P}) \wedge \text{AC}(\hat{P}, E) \quad (\text{g})$$

According to (4.1) (4.4) (4.2) (4.5) and Lemma 23, we know that the module local simulation can compose to a whole program downward simulation,

$$\text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n \preceq_E \text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n \quad (4.6)$$

By applying Lemma 24 on (E.1), we get that the whole program downward simulation can flip to a whole program upward simulation,

$$\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n \leq_E \text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n \quad (4.7)$$

According to (4.7) and Lemma 29, 25, 28 and 27, we know that the target program preserves the event trace, data-race-free, safety, reachclose and analysis correctness properties of source program. We finish the proof. \square

Lemma 23 (Compositionality). For any $f_1, \dots, f_n, \varphi, E, E_1, \dots, E_m, \Gamma = \{(sl_i, ge_i, \gamma_i), \dots, (sl_m, ge_m, \gamma_m)\}, \Pi = \{(tl_i, ge'_i, \pi_i), \dots, (tl_m, ge'_m, \pi_m)\}$, if

- $\forall i \in \{1, \dots, m\}. (sl_i, ge_i, \gamma_i) \preceq_{\varphi, E_i} (tl_i, ge'_i, \pi_i) \wedge \text{wd}(sl_i) \wedge \text{wd}(tl_i) \wedge E_i \subseteq E$;
- $\text{RC_prog}(\text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n), \text{NPDRF}(\text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n), \text{AC}(\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n, E)$;

then $\text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n \preceq_E \text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n$.

PROOF. The details of the proof of compositionality can be found in Sec. C \square

Lemma 24 (Flip). For any $f_1, \dots, f_n, \varphi, \Gamma = \{(sl_i, ge_i, \gamma_i), \dots, (tl_m, ge'_m, \gamma_m)\}, \Pi = \{(tl_i, ge'_i, \pi_i), \dots, (tl_m, ge'_m, \pi_m)\}$, if $\forall 1 \leq i \leq m. \text{det}(tl_i), \text{Safe}(\text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n)$ and $\text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n \preceq_E \text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n$, then

$$\text{let } \Pi \text{ in } f_1 \mid \dots \mid f_n \leq_E \text{let } \Gamma \text{ in } f_1 \mid \dots \mid f_n.$$

PROOF. The details of the proof of flip can be found in Sec. D. \square

Lemma 25 (Safety Preservation). For any $\hat{P} \leq_E \hat{\mathbb{P}}$ and $\text{Safe}(\hat{\mathbb{P}})$, then $\text{Safe}(\hat{P})$.

Lemma 26 (NPDRF Preservation). For any $\hat{\mathbb{P}}, \hat{P}, E$, if $\hat{P} \leq_E \hat{\mathbb{P}}, \text{AC}(\hat{\mathbb{P}}, E)$, and $\text{NPDRF}(\hat{\mathbb{P}})$, then $\text{NPDRF}(\hat{P})$.

PROOF. Details of proof of NPDRF Preservation can be found in Sec. E. \square

Lemma 27 (Simulation ensures ReachClose). If $\hat{P} \leq_E \hat{\mathbb{P}}$, then $\text{RC_prog}(\hat{P})$.

PROOF. We finish the proof by the definition of whole program downward simulation and Lemma 86. (* on paper proof *) \square

Lemma 28 (Analysis correctness Preservation). If $\hat{P} \leq_E \hat{\mathbb{P}}$, and $\text{AC}(\hat{\mathbb{P}}, E)$, then $\text{AC}(\hat{P}, E)$.

PROOF. Details of proof of AC Preservation can be found in Sec. F. \square

Lemma 29 (Soundness). If $\hat{P} \leq_E \hat{\mathbb{P}}$, then $\hat{\mathbb{P}} \sqsupseteq \hat{P}$.

4.1 Whole program simulation

We define whole program simulation in this section. In CASCompCert, there are two types of whole program simulation. The whole program downward simulation constructs the target program execution according to the execution of source program. The whole program downward simulation can be flipped to a whole program upward simulation and can be used to prove the DRF preserving in source program.

$$\begin{array}{c}
\frac{(T_0, \sigma_0) \Rightarrow_{B \setminus \{t\}}^\mu (T, \sigma) \quad T(t) = (tl, F, \kappa) \quad B(t) = (n, \delta, \widehat{W})}{(T, \sigma) \Rightarrow_\emptyset^\mu (T, \sigma)} \quad \frac{F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau}^n (\kappa', \sigma') \quad \delta \subseteq (F \cup \mu.S) \quad T' = T\{t \rightsquigarrow (tl, F, \kappa')\}}{(T_0, \sigma_0) \Rightarrow_B^\mu (T', \sigma')} \\
\\
d_1 \leq d_2 \stackrel{\text{def}}{=} d_1 = d_2 \vee (\exists \text{id}. d_1 = 0 \wedge d_2 = \text{id})
\end{array}$$

Fig. 13. Auxiliary definitions for Whole program downward simulation

Whole-program downward simulation. We introduce a tuple p , which records the some information for the previous switching point:

$$p \stackrel{\text{def}}{=} (\widehat{W}, B) \quad (\text{where } B \stackrel{\text{def}}{=} \{t \rightsquigarrow (n, \delta, \widehat{W})\}^*)$$

Definition 30 (Whole-Program Downward Simulation). Let $\hat{P} = \mathbf{let} \Gamma \mathbf{in} f_1 \mid \dots \mid f_n$, $\hat{P} = \mathbf{let} \Pi \mathbf{in} f_1 \mid \dots \mid f_n$, where $\Gamma = \{(sl_1, ge_1, \gamma_1), \dots, (sl_m, ge_m, \gamma_m)\}$, $\Pi = \{(tl_1, ge'_1, \pi_1), \dots, (tl_m, ge'_m, \pi_m)\}$. We say $\hat{P} \preceq_E \hat{P}$ iff

- $\exists \varphi. (\forall 1 \leq i \leq m. [\varphi]ge_i = ge'_i)$, and
- for any \widehat{W} , if $\hat{P} \xrightarrow{\text{load}} \widehat{W}$, then there exists $\widehat{W}, i \in \text{index}, \mu$, such that:

$$\hat{P} \xrightarrow{\text{load}} \widehat{W} \wedge (\widehat{W}, \text{emp}) \preceq_{(\widehat{W}, \emptyset), 0}^{i, \mu, E} (\widehat{W}, \text{emp}, \text{emp}).$$

Here we define $(\widehat{W}, \Delta_0) \preceq_{p, n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$ as the largest relation such that whenever $(\widehat{W}, \Delta_0) \preceq_{p, n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$ holds, then there exists t , where $\widehat{W}.t = \widehat{W}.t = t$, such that the following are true:

- (1) $t \notin \text{dom}(B)$, $\text{dom}(p.B) \subseteq \text{dom}(\mathbb{T})$, $\widehat{W}.d(t) \leq \widehat{W}.d(t)$ and $p.\widehat{W} \xrightarrow[\delta_r \cup \delta_0]{\tau}^{n_0} \widehat{W}$;
- (2) $\forall \widehat{W}', \Delta$. if $\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}'$, then one of the following holds:
 - (a) $\exists j < i. (\widehat{W}', \Delta_0 \cup \Delta) \preceq_{p, n_0}^{j, \mu, E} (\widehat{W}, \delta_0, \delta_r)$; or
 - (b) $\exists \widehat{W}', \delta, j, n > 0$ such that:
 - (i) $\widehat{W} \xrightarrow[\delta]{\tau}^n \widehat{W}'$, and
 - (ii) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$, and
 - (iii) $(\widehat{W}', \Delta_0 \cup \Delta) \preceq_{p, n_0+n}^{j, \mu, E} (\widehat{W}', \delta_0 \cup \delta, \delta_r)$;
- (3) $\forall \mathbb{T}', t', \Sigma', o, d'$, if $o \neq \tau$, and $\widehat{W} \xrightarrow[\text{emp}]{o} (\mathbb{T}', t', d', \Sigma')$, then one of the following holds:
 - (a) (* switch point match *) (* update the last switch point, and remove t'' from reorder buffer *)
 $\exists \widehat{W}', \delta, T', \sigma', d'_1, j$, for any $t'' \in \text{dom}(\mathbb{T}')$, let $p' = ((T', t'', d'_1, \sigma'), B \setminus \{t''\})$, such that
 - (i) $\widehat{W} \xrightarrow[\delta]{\tau}^* \widehat{W}'$, and $\widehat{W}' \xrightarrow[\text{emp}]{o} (T', t'', d'_1, \sigma')$;
 - (ii) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta)$;
 - (iii) $\exists T'', \sigma''. (T', \sigma') \Rightarrow_{p.B}^\mu (T'', \sigma'') \wedge \text{closed}(\mu.S, \sigma'')$;
 - (iv) if $p.B(t'') = (n'', \delta'', \widehat{W}'')$, then:
 - $\exists \widehat{W}_1''. (T', t'', d'_1, \sigma') \xrightarrow[\delta'']{\tau}^{n''} \widehat{W}_1'' \wedge \widehat{W}'' \xrightarrow[\delta'']{\delta''} \widehat{W}_1''$; (* redo executions of t'' in buffer *)

- For any \widehat{W}_1'' , if $(T', t'', d'_1, \sigma') : \xrightarrow[\delta'']{n''} \widehat{W}_1''$ and $\widehat{W}'' \xrightarrow{\delta''} \widehat{W}_1''$, then

$$((T', t'', d', \Sigma'), \text{emp}) \preceq_{p', n''}^{j, \mu, E} (\widehat{W}_1'', \text{emp}, \delta'');$$
 - else (* no execution of t'' in buffer *)

$$((T', t'', d', \Sigma'), \text{emp}) \preceq_{p', 0}^{j, \mu, E} ((T', t'', d'_1, \sigma'), \text{emp}, \text{emp});$$
 or
 - (b) (* switch point not match *)

$$\exists \widehat{W}', \delta, n, j, \text{ for any } t'' \in \text{dom}(T'), \text{ such that}$$
 - (i) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}'$, $(n = 0 \implies j < i, o \in E; \text{ (* synchronization eliminated must in } E \text{ *)})$
 - (ii) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta)$;
 - (iii) (* undo the executions of current thread, and rollback to the last switch point *)

$$\text{there exists } B = p.B\{t \rightsquigarrow (n_0 + n, \delta_r \cup \delta_0 \cup \delta, \widehat{W}')\}, \text{ and } p' = (p.\widehat{W}^{t''}, B \setminus \{t''\}) \text{ such that:}$$

$$\text{if } B(t'') = (n'', \delta'', \widehat{W}''), \text{ then}$$
 - $\exists \widehat{W}_1'' . p.\widehat{W}^{t''} : \xrightarrow[\delta'']{n''} \widehat{W}_1'' \wedge \widehat{W}'' \xrightarrow{\delta''} \widehat{W}_1''; \text{ (* redo executions of } t'' \text{ in buffer *)}$
 - For any \widehat{W}_1'' , if $p.\widehat{W}^{t''} : \xrightarrow[\delta'']{n''} \widehat{W}_1''$ and $\widehat{W}'' \xrightarrow{\delta''} \widehat{W}_1''$, then

$$((T', t'', d', \Sigma'), \text{emp}) \preceq_{p', n''}^{j, \mu, E} (\widehat{W}_1'', \text{emp}, \delta'');$$
 - else (* no execution of t'' in buffer *)

$$((T', t'', d', \Sigma'), \text{emp}) \preceq_{p', 0}^{j, \mu, E} (p.\widehat{W}^{t''}, \text{emp}, \text{emp});$$
 - (4) if $\widehat{W} \xrightarrow[\text{emp}]{\tau} \text{done}$, then $\exists \widehat{W}', \delta$.
 - (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}'$, $\widehat{W}' \xrightarrow[\text{emp}]{\tau} \text{done}$;
 - (b) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta)$;
- (** Done case proof **)

Lemma 31 (Whole Program Downward Simulation implies Done Preserving - II). For any $\widehat{W}, \Delta_0, \widehat{W}, \delta_0, \delta_r, i, \mu, \widehat{W}_0, E, B$, and n_0 , if $\widehat{W} \Rightarrow^+ \text{done}$, and $(\widehat{W}, \Delta_0) \preceq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$, then $\exists t. \text{Etr}(\widehat{W}_0^t, \text{done})$.

PROOF. Prove by induction on the number of source steps. (* on paper proof *) \square

(** Silent diverge case proof **)

Lemma 32 (A thread executes forever existing). For any $\widehat{W}, \widehat{W}, \Delta_0, \delta_0, \delta_r, \widehat{W}_0, B, k, \mu, E, lt$, if $(\widehat{W}, \Delta_0) \preceq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$, $\widehat{W}_0 \Downarrow B$, $\text{Etr}(\widehat{W}, \epsilon)$, $lt \subseteq \text{dom}(\widehat{W}_0.T)$, $\text{isdone}_{\mu, E}(\widehat{W}, \widehat{W}_0, B \cup \{\widehat{W}_0.t \rightsquigarrow (n_0, \delta_r \cup \delta_0, \widehat{W})\}, lt)$, $k = |\widehat{W}_0.T| - |lt|$, then $\exists t \in \text{dom}(\widehat{W}_0.T)$, $\widehat{W}', \Delta'_0, \widehat{W}', \delta'_0, \delta'_r, t_0, B', n'_0, j, lt'$,

$$\begin{aligned} \widehat{W} \Rightarrow^* \widehat{W}' \wedge (\widehat{W}', \Delta'_0) &\preceq_{(\widehat{W}_0^{t_0}, B'), n'_0}^{j, \mu, E} (\widehat{W}', \delta'_0, \delta'_r) \wedge \text{Etr}(\widehat{W}', \epsilon) \wedge \widehat{W}_0 \Downarrow B' \wedge lt' \subseteq \text{dom}(\widehat{W}_0.T) \\ &\wedge \neg \text{willdone}_{\mu, E}(\widehat{W}', \widehat{W}_0, lt', t) \wedge \text{isdone}_{\mu, E}(\widehat{W}', \widehat{W}_0, B' \cup \{t_0 \rightsquigarrow (n'_0, \delta'_r \cup \delta'_0, \widehat{W}')\}, lt'). \end{aligned}$$

PROOF. Prove by induction on the number of active threads k . (* on paper proof *) \square

$$\begin{array}{c}
\widehat{W} \Rightarrow^* \widehat{W}' \quad \widehat{W}' \xRightarrow{o} \widehat{W}'' \quad \widehat{W}_0^t \Rightarrow^* \widehat{W}'_0 \quad \widehat{W}'_0 \xRightarrow{o} \widehat{W}''_0 \\
(\widehat{W}'', \Delta_0) \leq_{(\widehat{W}_0'', B), n_0}^{i, \mu, E} (\widehat{W}'', \delta_0) \quad \widehat{W}_0'' \Downarrow B \quad Etr(\widehat{W}'', \epsilon) \quad o \notin \{\tau, e\} \\
\hline
SPmatchEx_{\mu, E}(\widehat{W}, \widehat{W}_0) \\
(\widehat{W}, \Delta_0) \leq_{p, n_0}^{i, \mu, E} (\widehat{W}, \delta_0) \stackrel{\text{def}}{=} (\widehat{W}, \Delta_0) \leq_{p, n_0}^{i, \mu, E} (\widehat{W}, \delta_0) \wedge \neg SPmatchEx_{\mu, E}(\widehat{W}, p. \widehat{W}_0) \\
(n_1, \delta_1, \widehat{W}_1) \geq_{\widehat{W}_0} \perp \stackrel{\text{def}}{=} \widehat{W}_0 \xRightarrow[\delta_1]{\tau} n_1 \widehat{W}_1 \\
(n_1, \delta_1, \widehat{W}_1) \geq_{\widehat{W}_0} (n_2, \delta_2, \widehat{W}_2) \stackrel{\text{def}}{=} \exists n, \delta. n_1 = n + n_2 \wedge \widehat{W}_1 \xRightarrow[\delta]{\tau} n \widehat{W}_2 \wedge \delta \cup \delta_2 = \delta_1 \\
(n_1, \delta_1, \widehat{W}_1) >_{\widehat{W}_0} \perp \stackrel{\text{def}}{=} n_1 > 0 \wedge (n_1, \delta_1, \widehat{W}_1) \geq_{\widehat{W}_0} \perp \\
(n_1, \delta_1, \widehat{W}_1) >_{\widehat{W}_0} (n_2, \delta_2, \widehat{W}_2) \stackrel{\text{def}}{=} n_1 > n_2 \wedge (n_1, \delta_1, \widehat{W}_1) \geq_{\widehat{W}_0} (n_2, \delta_2, \widehat{W}_2) \\
B' >_{t, \widehat{W}_0} B \stackrel{\text{def}}{=} B'(t) >_{\widehat{W}_0^t} B(t) \wedge (\forall t' \in \text{dom}(B'), t' \neq t. B'(t') \geq_{\widehat{W}_0^{t'}} B(t')) \\
\text{isdone}_{\mu, E}(\widehat{W}, \widehat{W}_0, B, lt) \stackrel{\text{def}}{=} \forall \widehat{W}', \widehat{W}', \Delta'_0, \delta'_0, \delta'_r, i, B_0, n_0, t. \\
(\widehat{W} \Rightarrow^* \widehat{W}' \wedge (\widehat{W}', \Delta'_0) \leq_{(\widehat{W}_0^t, B_0), n_0}^{i, \mu, E} (\widehat{W}', \delta'_0, \delta'_r) \wedge \widehat{W}_0 \Downarrow B_0 \\
\wedge (B_0 \cup \{t \rightsquigarrow (n_0, \delta'_r \cup \delta'_0, \widehat{W}')\}) >_{t, \widehat{W}_0} B \wedge Etr(\widehat{W}', \epsilon)) \implies t \notin lt \\
\text{willdone}_{\mu, E}(\widehat{W}, \widehat{W}_0, lt, t) \stackrel{\text{def}}{=} \exists \widehat{W}', \widehat{W}', i, B, n_0, \Delta_0, \delta_0, \delta_r, t_0. \\
\widehat{W} \Rightarrow^* \widehat{W}' \wedge (\widehat{W}', \Delta_0) \leq_{(\widehat{W}_0^{t_0}, B), n_0}^{i, \mu, E} (\widehat{W}', \delta_0, \delta_r) \wedge Etr(\widehat{W}', \epsilon) \wedge \widehat{W}_0 \Downarrow B \\
\wedge \text{isdone}_{\mu, E}(\widehat{W}', \widehat{W}_0, B \cup \{t_0 \rightsquigarrow (n_0, \delta_r \cup \delta_0, \widehat{W}')\}, \{t\} \cup lt) \\
\widehat{W}_0 \Downarrow B \stackrel{\text{def}}{=} \forall t \in \text{dom}(B). B(t) = (n, \delta, \widehat{W}) \implies \widehat{W}_0^t \xRightarrow[\delta]{\tau} n \widehat{W} \\
(T, t, \mathbb{d}, \sigma) \stackrel{\delta}{=} (T', t', \mathbb{d}', \sigma') \stackrel{\text{def}}{=} t = t' \wedge T(t) = T'(t') \wedge \sigma \xRightarrow{\delta. rs \cup \delta. ws} \sigma'
\end{array}$$

Fig. 14. Auxiliary definition in proving revised whole program downward simulation implies refinement

Lemma 33 (Silent diverge implies a thread progress). For any $\widehat{W}, \widehat{W}, \Delta_0, \delta_0, i, \mu, E, \widehat{W}_0$ and n_0 , if $(\widehat{W}, \Delta_0) \leq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r), \widehat{W}_0 \Downarrow B$ and $Etr(\widehat{W}, \epsilon)$, then $\exists \widehat{W}', \widehat{W}', n'_0, \Delta'_0, \delta'_0, \delta'_r, j, t, B'$.

$$\begin{aligned}
& \widehat{W} \Rightarrow^* \widehat{W}' \wedge (\widehat{W}', \Delta'_0) \leq_{(\widehat{W}_0^t, B'), n'_0}^{j, \mu, E} (\widehat{W}', \delta'_0, \delta'_r) \wedge \widehat{W}_0 \Downarrow B' \\
& \wedge (B' \cup \{t \rightsquigarrow (n'_0, \delta'_r \cup \delta'_0, \widehat{W}')\}) >_{t, \widehat{W}_0} (B \cup \{\widehat{W}_0.t \rightsquigarrow (n_0, \delta_r \cup \delta_0, \widehat{W})\}) \wedge Etr(\widehat{W}', \epsilon).
\end{aligned}$$

PROOF. Prove by induction on the index i . (* on paper proof *) □

Lemma 34 (Lemma for source silent diverge implies target diverge). For any $\widehat{W}_0, \widehat{W}, \Delta_0, \widehat{W}, \delta_0, \delta_r, \widehat{W}_0, B, n_0, i, \mu, E, lt$ and t , if $\widehat{W}_0 \Rightarrow^* \widehat{W}, (\widehat{W}, \Delta_0) \leq_{(\widehat{W}_0^t, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r), \neg \text{willdone}_{\mu, E}(\widehat{W}, \widehat{W}_0, lt, t), lt \subseteq \text{dom}(\widehat{W}_0.T), \text{isdone}_{\mu, E}(\widehat{W}, \widehat{W}_0, B \cup \{t \rightsquigarrow (n_0, \delta_r \cup \delta_0, \widehat{W})\}, lt), \widehat{W}_0 \Downarrow B, Etr(\widehat{W}, \epsilon)$, then $Etr(\widehat{W}, \epsilon)$.

PROOF. Prove by cofix. (* on paper proof *) □

Lemma 35 (Source silent diverge implies target diverge). For any $\widehat{W}, \Delta_0, \delta_0, \delta_r, i, \mu, E, \widehat{W}_0, B$, and n_0 , if $(\widehat{W}, \Delta_0) \leq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r), \widehat{W}_0 \Downarrow B$ and $Etr(\widehat{W}, \epsilon)$, then $\exists t. Etr(\widehat{W}_0^t, \epsilon)$.

PROOF. We finish the proof by applying Lemma 32, 33, and 34. (* on paper proof *) \square

Lemma 36 (Source diverge and switch point match existing implies target diverge). For any $\widehat{W}, \widehat{W}', \widehat{W}'', \widehat{W}_0, \widehat{W}_0', \widehat{W}_0'', \widehat{W}''', t, o, \Delta_0, \delta_0, \delta_r, i, \mu, E, B$, and n_0 , if $\widehat{W} \Rightarrow^* \widehat{W}', \widehat{W}' \xRightarrow{o} \widehat{W}'', \widehat{W}_0^t \Rightarrow^* \widehat{W}_0', \widehat{W}_0' \xRightarrow{o} \widehat{W}_0'', o \notin \{\tau, e\}$, $(\widehat{W}'', \Delta_0) \leq_{(\widehat{W}_0'', B), n_0}^{i, \mu, E} (\widehat{W}'', \delta_0, \delta_r), \widehat{W}_0'' \Downarrow B$ and $Etr(\widehat{W}'', \epsilon)$, then $Etr(\widehat{W}_0^t, \epsilon)$.

PROOF. Prove by cofix. (* on paper proof *) \square

Lemma 37. $isdone(\widehat{W}, \widehat{W}_0, B, \emptyset)$

Lemma 38. $(isdone(\widehat{W}, \widehat{W}_0, B, lt) \wedge B' >_{t, \widehat{W}_0} B) \implies isdone(\widehat{W}, \widehat{W}_0, B', lt)$

Lemma 39. $(\widehat{W} \Rightarrow^* \widehat{W}' \wedge isdone(\widehat{W}, \widehat{W}_0, B, lt)) \implies isdone(\widehat{W}', \widehat{W}_0, B, lt)$

Lemma 40. $(\widehat{W} \Rightarrow^* \widehat{W}' \wedge \neg willdone(\widehat{W}, \widehat{W}_0, lt, t)) \implies \neg willdone(\widehat{W}', \widehat{W}_0, lt, t)$

Lemma 41 (Buffer execution exists). For any B and \widehat{W}_0 , if $(\forall t, n, \delta, \widehat{W}. B(t) = (n, \delta, \widehat{W}) \implies (\exists \widehat{W}_1. \widehat{W}_0^t \xRightarrow{\tau}_{\delta} n \widehat{W}_1 \wedge \widehat{W} \xRightarrow{\delta} \widehat{W}_1))$, then $\exists B'. \widehat{W}_0 \Downarrow B' \wedge B' \simeq B \wedge \text{dom}(B') = \text{dom}(B)$.

PROOF. Prove by induction on the number of elements in buffer B . \square

$$B' \simeq B \stackrel{\text{def}}{=} \forall t \in \text{dom}(B'), n, \delta, \widehat{W}'. B'(t) = (n, \delta, \widehat{W}') \implies \exists \widehat{W}. B(t) = (n, \delta, \widehat{W}) \wedge \widehat{W} \xRightarrow{\delta} \widehat{W}'$$

Lemma 42 (Buffer execution simulation holds). For any $\widehat{W}, \Delta_0, \widehat{W}, \delta_0, \delta_r, i, \mu, E, \widehat{W}_0, B, n_0$ and B' , if $(\widehat{W}, \Delta_0) \leq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r), B' \simeq B$ and $\text{dom}(B') = \text{dom}(B)$, then $(\widehat{W}, \Delta_0) \leq_{(\widehat{W}_0, B'), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$.

PROOF. Prove by cofix. \square

Lemma 43 (Buffer increasing). For any $\widehat{W}_0, B, t, n_0, \delta_0, \widehat{W}, \delta$ and \widehat{W}' , if $\widehat{W}_0 \Downarrow B, B(t) = (n_0, \delta_0, \widehat{W}), \widehat{W} \xRightarrow{\tau}_{\delta} n \widehat{W}'$ and $n > 0$, then $B\{t \rightsquigarrow (n_0 + n, \delta_0 \cup \delta, \widehat{W}')\} >_{t, \widehat{W}_0} B$.

Lemma 44 (Switch point match or not). For any $\widehat{W}, \widehat{W}_0, \Delta_0, \delta_0, i, \mu, E, B, n_0$, if $(\widehat{W}, \Delta_0) \leq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0)$ and $Etr(\widehat{W}, \epsilon)$ then $\text{SPmatchEx}_{\mu, E}(\widehat{W}, \widehat{W}_0) \vee \neg \text{SPmatchEx}_{\mu, E}(\widehat{W}, \widehat{W}_0)$.

PROOF. Prove according to the Law of Excluded Middle. \square

Lemma 45 (Whole Program Downward Simulation implies Diverge Preserving - II). For any $\widehat{W}, \Delta_0, \widehat{W}, \widehat{W}_0, \delta_0, \delta_r, i, \mu, E, B$, and n_0 , if $Etr(\widehat{W}, \epsilon), \widehat{W}_0 \Downarrow B$ and $(\widehat{W}, \Delta_0) \leq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$, then $\exists t. Etr(\widehat{W}_0^t, \epsilon)$.

(** Event case proof **)

Lemma 46 (Event preservation). For any $\widehat{W}, \Delta_0, \widehat{W}', e, \widehat{W}, \delta_0, \delta_r, \widehat{W}_0, i, E, \mu$ and n_0 , if $\widehat{W} \xRightarrow{e} \widehat{W}', (\widehat{W}, \Delta_0) \leq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$, then $\exists t, j, \widehat{W}_0', \widehat{W}', B', \delta'. \widehat{W}_0^t \xRightarrow{e} \widehat{W}_0' \wedge (\widehat{W}', \text{emp}) \leq_{(\widehat{W}_0', B'), n'}^{j, \mu, E} (\widehat{W}', \text{emp}, \delta') \wedge \widehat{W}_0' \Downarrow B'$.

PROOF. This lemma can be proved by induction on the number of source steps. (* on paper proof *) \square

Lemma 47 (Whole program downward sim implies event preservation aux). For any $\widehat{W}, \widehat{W}', t, \widehat{W}_0, \widehat{W}'_0, e, B, n_0, \delta_r$, if $\widehat{W} \xRightarrow{e} \widehat{W}'$, $\widehat{W}_0^t \xRightarrow{e} \widehat{W}'_0$, $Etr(\widehat{W}', B)$, $\widehat{W}'_0 \Downarrow B$ and $(\widehat{W}', \text{emp}) \preceq_{(\widehat{W}'_0, B), n_0}^{i, \mu, E} (\widehat{W}', \text{emp}, \delta_r)$, then $Etr(\widehat{W}_0^t, e :: B)$.

PROOF. The lemma can be proved by cofix. \square

Lemma 48 (Whole program downward sim implies event preservation). For any $\widehat{W}, \Delta_0, \widehat{W}', e, \widehat{W}, \delta_0, \delta_r, \widehat{W}_0, i, E, \mu$ and n_0 , if $\widehat{W} \xRightarrow{e} \widehat{W}'$, $(\widehat{W}, \Delta_0) \preceq_{(\widehat{W}_0, B), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r)$ then $\exists t. Etr(\widehat{W}_0^t, e :: B)$.

PROOF. Prove by applying Lemma 46 and 47. \square

(Whole program downward simulation implies refinement **)**

Theorem 49 (Whole-program Downward Simulation implies Refinement).

$$\hat{P} \preceq_E \hat{P} \implies \hat{P} \sqsubseteq \hat{P}$$

where $\hat{P} = \mathbf{let} \Gamma \mathbf{in} f_1 \mid \dots \mid f_n$, $\hat{P} = \mathbf{let} \Pi \mathbf{in} f_1 \mid \dots \mid f_n$, and $\Gamma = \{(sl_1, ge_1, \gamma_1), \dots, (sl_m, ge_m, \gamma_m)\}$, $\Pi = \{(tl_1, ge'_1, \gamma_1), \dots, (tl_m, ge'_m, \pi_m)\}$.

PROOF. We first under $\hat{P} \preceq_E \hat{P}$ according to its definition (Def. 30) and get:

$$\exists \varphi. (\forall 1 \leq i \leq m. \lfloor \varphi \rfloor ge_i = ge'_i) \quad (4.8)$$

$$\begin{aligned} \forall \widehat{W}. (\hat{P} \xRightarrow{\text{load}} \widehat{W}) \implies \\ \exists \widehat{W}, i \in \text{index}, \mu. (\hat{P} \xRightarrow{\text{load}} \widehat{W}) \wedge (\widehat{W}, \text{emp}) \preceq_{(\widehat{W}, \emptyset), 0}^{i, \mu, E} (\widehat{W}, \text{emp}, \text{emp}) \end{aligned} \quad (4.9)$$

And we need to prove: $PEtr(\hat{P}, \mathcal{B}) \implies PEtr(\hat{P}, \mathcal{B})$.

We unfold $PEtr(\hat{P}, \mathcal{B})$ according to its definition and get there exists \widehat{W} , such that:

$$\hat{P} \xRightarrow{\text{load}} \widehat{W} \quad (4.10)$$

$$Etr(\widehat{W}, \mathcal{B}) \quad (4.11)$$

From (4.9) and (4.10), we get there exists $\widehat{W}, i \in \text{index}$, and μ such that:

$$\hat{P} \xRightarrow{\text{load}} \widehat{W} \quad (4.12)$$

$$(\widehat{W}, \text{emp}) \preceq_{(\widehat{W}, \emptyset), 0}^{i, \mu, E} (\widehat{W}, \text{emp}, \text{emp}) \quad (4.13)$$

We destruct \mathcal{B} , and discuss each case respectively in the following:

- if $\mathcal{B} = \mathbf{done}$, we do inversion on (4.11) and get:

$$\widehat{W} \xRightarrow{+} \mathbf{done} \quad (4.14)$$

According to (4.14) (4.13) and Lemma 31, we get:

$$\exists t. Etr(\widehat{W}^t, \mathbf{done})$$

- if $\mathcal{B} = e :: \mathcal{B}'$, we do inversion on (4.11) and get:

$$\widehat{W} \xRightarrow{e} \widehat{W}' \wedge Etr(\widehat{W}', \mathcal{B}) \quad (4.15)$$

According to (4.15) (4.13) and Lemma 48, we get:

$$\exists t. Etr(\widehat{W}^t, e :: \mathcal{B}')$$

$$\begin{array}{c}
\frac{\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}' \quad \widehat{W}' \xrightarrow[\text{emp}]{o} \widehat{W}''}{\widehat{W} \xrightarrow[\Delta]{o} \widehat{W}''} \quad \frac{\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}' \quad \widehat{W}' \xrightarrow[\text{emp}]{o} \widehat{W}'' \quad \widehat{W}'.t = \widehat{W}'''.t \quad o \in E}{\widehat{W} \xrightarrow[\Delta]{e\text{-syn}(E)} \widehat{W}''} \\
\frac{\widehat{W} \xrightarrow[\Delta]{e\text{-syn}(E)} \widehat{W}' \quad \widehat{W}' \xrightarrow[\Delta_0]{e\text{-syn}(E)} \widehat{W}''}{\widehat{W} \xrightarrow[\Delta_0 \cup \Delta]{e\text{-syn}(E)} \widehat{W}''} \quad \frac{\widehat{W} \xrightarrow[\text{emp}]{e\text{-syn}(E)} \widehat{W}}{\widehat{W} \xrightarrow[\Delta_0 \cup \Delta]{e\text{-syn}(E)} \widehat{W}} \quad \widehat{W} \xrightarrow[\Delta]{e\text{-syn}(E)} \widehat{W}' \stackrel{\text{def}}{=} \exists \Delta, n. \widehat{W} \xrightarrow[\Delta]{e\text{-syn}(E)} \widehat{W}'
\end{array}$$

Fig. 15. Auxiliary Definition in Whole Program Upward Simulation

- if $\mathcal{B} = \epsilon$, we get:

$$Etr(\widehat{W}, \epsilon) \quad (4.16)$$

According to (4.16), $\widehat{W} \Downarrow \emptyset$, (4.13) and Lemma 45, we get:

$$\exists t. Etr(\widehat{W}^t, \epsilon)$$

□

Whole-Program Upward Simulation. We define the whole-program upward simulation here.

Definition 50 (Whole-Program Upward Simulation). Let $\hat{P} = \mathbf{let} \Gamma \mathbf{in} f_1 \mid \dots \mid f_n$, $\hat{P} = \mathbf{let} \Pi \mathbf{in} f_1 \mid \dots \mid f_n$, $\Gamma = \{(sl_1, ge_1, \gamma_1), \dots, (sl_m, ge_m, \gamma_m)\}$, $\Pi = \{(tl_1, ge'_1, \pi_1), \dots, (tl_m, ge'_m, \pi_m)\}$. We say $\hat{P} \leq_E \hat{P}$ iff.

- $\exists \varphi. (\forall 1 \leq i \leq m. \lfloor \varphi \rfloor ge_i = ge'_i)$, and
- for any \widehat{W} , if $\hat{P} \xrightarrow{\text{load}} \widehat{W}$, then there exists $\widehat{W}, i \in \text{index}, \mu$, such that $\hat{P} \xrightarrow{\text{load}} \widehat{W} \wedge (\widehat{W}, \text{emp}) \leq_{\mu}^{i,E} (\widehat{W}, \text{emp})$.

Here we define $(\widehat{W}, \delta_0) \leq_{\mu}^{i,E} (\widehat{W}, \Delta_0)$ as the largest relation such that whenever $(\widehat{W}, \delta_0) \leq_{\mu}^{i,E} (\widehat{W}, \Delta_0)$ holds, then there exists t , where $\widehat{W}.t = \widehat{W}.t = t$, such that the following are true:

- (1) $\widehat{W}.d \setminus \{t\} = \widehat{W}.d \setminus \{t\}$, $\widehat{W}.d(t) \leq \widehat{W}.d(t)$;
- (2) $\forall \widehat{W}', \delta$, if $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}'$, then one of the following holds:
 - (a) $\exists j < i. (\widehat{W}', \delta_0 \cup \delta) \leq_{\mu}^{j,E} (\widehat{W}, \Delta_0)$; or
 - (b) $\exists \widehat{W}', \Delta, j$ such that
 - (i) $\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}'$;
 - (ii) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$;
 - (iii) $(\widehat{W}', \delta_0 \cup \delta) \leq_{\mu}^{j,E} (\widehat{W}', \Delta_0 \cup \Delta)$; or
 - (c) $\exists \widehat{W}', \Delta, j$ such that (* high-level executes a eliminated synchronization *)
 - (i) $\widehat{W} \xrightarrow[\Delta]{e\text{-syn}(E)} \widehat{W}'$;
 - (ii) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$;
 - (iii) $(\widehat{W}', \text{emp}) \leq_{\mu}^{j,E} (\widehat{W}', \text{emp})$; or
 - (d) $\exists \widehat{W}_1, \widehat{W}', \Delta_1, \Delta$ such that $\widehat{W}.d(t) = 0$ and (* high-level executes some eliminated synchronizations *)
 - (i) $\widehat{W} \xrightarrow[\Delta_1]{e\text{-syn}(E)} \widehat{W}_1$, $\widehat{W}_1 \xrightarrow[\Delta]{\tau} \widehat{W}'$;

- (ii) $\text{FPmatch}(\mu, \Delta_0 \cup \Delta_1, \delta_0)$, and $\delta \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta, \delta)$;
- (iii) $(\widehat{W}', \delta) \leq_{\mu}^{j,E} (\widehat{W}', \Delta)$; or
- (e) $\exists \widehat{W}_1, \widehat{W}', \Delta_1, \Delta$ such that $\widehat{W}.\text{dl}(t) = 0$ and (* high-level executes some eliminated synchronizations *)
 - (i) $\widehat{W} \xrightarrow[\Delta_1]{\text{e-syn}(E)} \widehat{W}_1, \widehat{W}_1 \xrightarrow[\Delta]{\text{e-syn}(E)} \widehat{W}'$;
 - (ii) $\text{FPmatch}(\mu, \Delta_0 \cup \Delta_1, \delta_0)$, and $\delta \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta, \delta)$;
 - (iii) $(\widehat{W}', \text{emp}) \leq_{\mu}^{j,E} (\widehat{W}', \text{emp})$;
- (3) $\forall T', \text{dl}', \sigma', o, t', \text{if } o \neq \tau$, and $\widehat{W} \xrightarrow[\text{emp}]{o} (T', t', \text{dl}', \sigma')$, then $\text{closed}(\mu.S, \sigma')$, and one of the following holds:
 - (a) $\exists \widehat{W}', T', \Sigma', \Delta, j$ such that, for any $t'' \in \text{dom}(T')$, we have
 - (i) $\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}', \widehat{W}' \xrightarrow[\text{emp}]{o} (T', t'', \text{dl}', \Sigma')$;
 - (ii) $\delta_0 \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0)$;
 - (iii) $((T', t'', \text{dl}', \sigma'), \text{emp}) \leq_{\mu}^{j,E} ((T', t'', \text{dl}', \Sigma'), \text{emp})$; or
 - (b) $\exists \widehat{W}_1, \widehat{W}_2, \Delta_1, \Delta, T', \Sigma', j$ such that, for any $t'' \in \text{dom}(T')$, we have (* high-level executes some eliminated synchronizations *)
 - (i) $\widehat{W} \xrightarrow[\Delta_1]{\text{e-syn}(E)} \widehat{W}_1, \widehat{W}_1 \xrightarrow[\Delta]{\tau} \widehat{W}_2, \widehat{W}_2 \xrightarrow[\text{emp}]{o} (T', t'', \text{dl}', \Sigma')$;
 - (ii) $\delta_0 \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta_1, \delta_0)$;
 - (iii) $((T', t'', \text{dl}', \sigma'), \text{emp}) \leq_{\mu}^{j,E} ((T', t'', \text{dl}', \Sigma'), \text{emp})$;
- (4) if $\widehat{W} \xrightarrow[\text{emp}]{\tau} \text{done}$, then one of the following are true:
 - (a) $\exists \widehat{W}', \Delta$, such that:
 - (i) $\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}', \widehat{W}' \xrightarrow[\text{emp}]{\tau} \text{done}$, and
 - (ii) $\delta_0 \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, and $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0)$; or
 - (b) $\exists \widehat{W}_1, \widehat{W}_2, \Delta_1, \Delta$, such that (* high-level executes some eliminated synchronizations *)
 - (i) $\widehat{W} \xrightarrow[\Delta_1]{\text{e-syn}(E)} \widehat{W}_1, \widehat{W}_1 \xrightarrow[\Delta]{\tau} \widehat{W}_2, \widehat{W}_2 \xrightarrow[\text{emp}]{\tau} \text{done}$;
 - (ii) $\delta_0 \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta_1, \delta_0)$.

REFERENCES

- [1] Jonathan Aldrich, Emin Gün Sirer, Craig Chambers, and Susan J.Eggersa. 2003. Comprehensive synchronization elimination for Java. *Science of Computer Programming* 47 (May-June 2003), 91–120. Issue 2-3.
- [2] Bogda Jeff and Hölzle Urs. 1999. Removing unnecessary synchronization in Java. In *OOPSLA*. 35–46.
- [3] Hanru Jiang, Hongjin Liang, Siyang Xiao, Junpeng Zha, and Xinyu Feng. 2019. Towards Certified Separate Compilation for Concurrent Programs. In *PLDI*. 111–125.
- [4] Youngju Song, Minki Cho, Dongjoo Kim, Yonghyun Kim, Jeehoon Kang, and Chung-Kil Hur. 2020. CompCertM: CompCert with C-Assembly Linking and Lightweight Modular Verification. In *POPL*.

$$\begin{array}{c}
 \frac{P \xRightarrow{load} W \quad W \Rightarrow^* W' \quad W' \Longrightarrow \text{Race}}{P \Longrightarrow \text{Race}} \\
 \\
 \frac{\text{predict}(W, t_1, (\delta_1, d_1)) \quad \text{predict}(W, t_2, (\delta_2, d_2)) \quad t_1 \neq t_2 \quad (\delta_1, d_1) \frown (\delta_2, d_2)}{W \Longrightarrow \text{Race}} \text{ Race} \\
 \\
 \frac{W = (T, _, 0, \sigma) \quad T(t) = (F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau} (\kappa', \sigma')}{\text{predict}(W, t, (\delta, 0))} \text{ Predict-0} \\
 \\
 \frac{W = (T, _, 0, \sigma) \quad T(t) = (F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{id. EntA}} (\kappa', \sigma) \quad F \vdash (\kappa', \sigma) \xrightarrow[\delta]{\tau} (\kappa'', \sigma'')}{\text{predict}(W, t, (\delta, \text{id}))} \text{ Predict-1}
 \end{array}$$

Fig. 16. Data races in preemptive semantics

$$\begin{array}{c}
 \frac{\hat{P} \xRightarrow{load} \hat{W} \quad \hat{W} \Rightarrow^* \hat{W}' \quad \hat{W}' \Longrightarrow \text{Race}}{\hat{P} \Longrightarrow \text{Race}} \\
 \\
 \frac{\hat{P} \xRightarrow{load} \hat{W} \quad t_1 \neq t_2 \quad (\delta_1, d_1) \frown (\delta_2, d_2) \quad \text{NPpredict}(\hat{W}, t_1, (\delta_1, d_1)) \quad \text{NPpredict}(\hat{W}, t_2, (\delta_2, d_2))}{\hat{P} \Longrightarrow \text{Race}} \\
 \\
 \frac{\hat{W} \xrightarrow[\text{emp}]{o} \hat{W}' \quad o \neq \tau \quad t_1 \neq t_2 \quad (\delta_1, d_1) \frown (\delta_2, d_2) \quad \text{NPpredict}(\hat{W}', t_1, (\delta_1, d_1)) \quad \text{NPpredict}(\hat{W}', t_2, (\delta_2, d_2))}{\hat{W} \Longrightarrow \text{Race}} \text{ Race}_{\text{np}} \\
 \\
 \frac{\hat{W} = (T, _, \text{dl}, \sigma) \quad T(t) = (F, \kappa) \quad F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau} (\kappa', \sigma') \quad \text{dl}(t) = d}{\text{NPpredict}(\hat{W}, t, (\delta, d))} \text{ Predict}_{\text{np}}
 \end{array}$$

Fig. 17. Data Race in Non-Preemptive Semantics

A DATA-RACE-FREEDOM

Below we first define the conflict of footprints.

$$\begin{array}{ll}
 \delta_1 \frown \delta_2 & \text{iff } (\delta_1.\text{ws} \cap \delta_2 \neq \emptyset) \vee (\delta_2.\text{ws} \cap \delta_1 \neq \emptyset) \\
 (\delta_1, d_1) \frown (\delta_2, d_2) & \text{iff } (\delta_1 \frown \delta_2) \wedge (d_1 = 0 \vee d_2 = 0)
 \end{array}$$

Then we define DRF(P) and NPDRF(\hat{P}):

$$\begin{array}{ll}
 \text{DRF}(P) & \text{iff } \neg(P \Longrightarrow \text{Race}) \\
 \text{NPDRF}(\hat{P}) & \text{iff } \neg(\hat{P} \Longrightarrow \text{Race})
 \end{array}$$

$$\begin{aligned}
\sigma &\stackrel{rs}{=} \sigma' \quad \text{iff} \quad \forall l \in rs. l \notin (\text{dom}(\sigma) \cup \text{dom}(\sigma')) \vee l \in (\text{dom}(\sigma) \cap \text{dom}(\sigma')) \wedge \sigma(l) = \sigma'(l) \\
\delta &\subseteq \delta' \quad \text{iff} \quad (\delta.rs \subseteq \delta'.rs) \wedge (\delta.ws \subseteq \delta'.ws) \\
\delta \cup \delta' &\stackrel{\text{def}}{=} (\delta.rs \cup \delta'.rs, \delta.ws \cup \delta'.ws) \\
\text{forward}(\sigma, \sigma') &\text{ iff } (\text{dom}(\sigma) \subseteq \text{dom}(\sigma')) \\
\text{LEqPre}(\sigma_1, \sigma_2, \delta, F) &\text{ iff } \sigma_1 \stackrel{\delta.rs}{=} \sigma_2 \wedge (\text{dom}(\sigma_1) \cap \delta.ws) = (\text{dom}(\sigma_2) \cap \delta.ws) \wedge (\text{dom}(\sigma_1) \cap F) = (\text{dom}(\sigma_2) \cap F) \\
\text{LEqPost}(\sigma_1, \sigma_2, \delta, F) &\text{ iff } \sigma_1 \stackrel{\delta.ws}{=} \sigma_2 \wedge (\text{dom}(\sigma_1) \cap F) = (\text{dom}(\sigma_2) \cap F) \\
\text{LEffect}(\sigma_1, \sigma_2, \delta, F) &\text{ iff } \sigma_1 \stackrel{\text{dom}(\sigma_1) - \delta.ws}{=} \sigma_2 \wedge (\text{dom}(\sigma_2) - \text{dom}(\sigma_1)) \subseteq (\delta.ws \cap F)
\end{aligned}$$

Fig. 18. Auxiliary definitions about states and footprints

B WELL-DEFINED LANGUAGE AND REACHCLOSED MODULE

Definition 51 (Well-Defined Languages). $\text{wd}(tl)$ iff, for any execution step $F \vdash (\kappa, \sigma) \xrightarrow[\delta]{l} (\kappa', \sigma')$ in this language, all of the following hold (some auxiliary definitions are in Fig. 18):

- (1) $\text{forward}(\sigma, \sigma')$;
- (2) $\text{LEffect}(\sigma, \sigma', \delta, F)$;
- (3) For any σ_1 , if $\text{LEqPre}(\sigma, \sigma_1, \delta, F)$, then there exists σ'_1 such that $F \vdash (\kappa, \sigma_1) \xrightarrow[\delta]{l} (\kappa', \sigma'_1)$ and $\text{LEqPost}(\sigma', \sigma'_1, \delta, F)$.
- (4) Let $\delta_0 = \bigcup \{ \delta \mid \exists \kappa', \sigma'. F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau} (\kappa', \sigma') \}$.

For any σ_1 , if $\text{LEqPre}(\sigma, \sigma_1, \delta_0, F)$, then for any $\kappa'_1, \sigma'_1, \iota_1, \delta_1$,

$$F \vdash (\kappa, \sigma_1) \xrightarrow[\delta_1]{\iota_1} (\kappa'_1, \sigma'_1) \implies \exists \sigma'. F \vdash (\kappa, \sigma) \xrightarrow[\delta_1]{\iota_1} (\kappa'_1, \sigma').$$

Definition 52 (Reach Closed Module). $\text{ReachClose}(sl, ge, \gamma)$ iff, for all $f, \mathbb{k}, \Sigma, \mathbb{F}$ and \mathbb{S} , if $sl.\text{InitCore}(\gamma, f) = \mathbb{k}$, $\mathbb{S} = \text{dom}(\Sigma)$, $ge \subseteq \Sigma$, $\mathbb{F} \cap \mathbb{S} = \emptyset$, and $\text{closed}(\mathbb{S}, \Sigma)$, then $\text{RC}(\mathbb{F}, \mathbb{S}, (\mathbb{k}, \Sigma))$.

Here RC is defined as the largest relation such that, whenever $\text{RC}(\mathbb{F}, \mathbb{S}, (\mathbb{k}, \Sigma))$, then for all Σ' such that $R(\Sigma, \Sigma', \mathbb{F}, \mathbb{S})$, and for all $\mathbb{k}', \Sigma', \Sigma'', \iota$ and Δ such that $\mathbb{F} \vdash (\mathbb{k}, \Sigma') \xrightarrow[\Delta]{\iota} (\mathbb{k}', \Sigma'')$, we have $\text{HG}(\Delta, \Sigma'', \mathbb{F}, \mathbb{S})$, and $\text{RC}(\mathbb{F}, \mathbb{S}, (\mathbb{k}', \Sigma''))$.

$$\begin{array}{c}
B \simeq (T, \sigma) \stackrel{\text{def}}{=} \forall t \in \text{dom}(B), n, \delta, \widehat{W}. B(t) = (n, \delta, \widehat{W}) \implies (\widehat{W}.T(t) = T(t) \wedge \widehat{W}.t = t \wedge \widehat{W}.\sigma \stackrel{\delta.rs \cup \delta.ws}{=} \sigma) \\
\\
\frac{\widehat{W} \xrightarrow{(\text{emp}, \text{emp})} \mu, E \widehat{W}}{\widehat{W} \xrightarrow{(\text{emp}, \text{emp})} \mu, E \widehat{W}} \quad \frac{\widehat{W}^t \xrightarrow{(\Delta_0, \delta_0)} \mu, E \widehat{W}_1^t \quad \text{FMatch}(\mu, \Delta, \delta) \quad \widehat{W}_1^t \xrightarrow{\tau} \widehat{W}_2^t \quad \widehat{W}_2^t \xrightarrow{\text{id.ExtA}} \widehat{W}_3^t}{\widehat{W}^t \xrightarrow{(\Delta_0 \cup \Delta, \delta_0 \cup \delta)} \mu, E \widehat{W}_3^t} \quad \frac{\widehat{W}^t \xrightarrow{(\Delta_0, \delta_0)} \mu, E \widehat{W}_1^t \quad \widehat{W}_1^t \xrightarrow{\tau} \widehat{W}_2^t \quad \widehat{W}_2^t \xrightarrow{\text{id.ExtA}} \widehat{W}_3^t}{\widehat{W}_2.\text{dl}(t) = \text{id}_0 \quad (\text{id}_0, \text{id}) \in E \quad \text{FMatch}(\mu, \Delta, \delta)} \\
\\
\frac{\widehat{W} \xRightarrow{\mu, E} \widehat{W}_1 \quad B(t).\delta = \delta \quad \widehat{W}_1^t \xrightarrow{(\Delta, \delta)} \mu, E \widehat{W}'}{\widehat{W} \xRightarrow{\mu, E} \widehat{W}'} \quad \frac{\widehat{W} \xRightarrow{\mu, E} \widehat{W}}{\widehat{W} \xRightarrow{\mu, E} \widehat{W}}
\end{array}$$

$\text{wdPresw}(\mu, n_0, \widehat{W}, (\widehat{W}_0, B), \delta)$ iff

- (1) $\widehat{W}_0 \xrightarrow{\tau} \widehat{W} \wedge \widehat{W}.t \notin \text{dom}(B) \wedge \delta \subseteq (\text{curF}(\widehat{W}_0) \cup \mu.S)$;
- (2) $\forall t_1, t_2 \in \text{dom}(B), t_1 \neq t_2. (B(t_1).\delta = \delta_1 \wedge B(t_2).\delta = \delta_2) \implies \neg(\delta_1 \frown \delta_2) \wedge \neg(\delta_1 \dashv \delta_2)$.

$$\begin{aligned}
\text{dl}' \lesssim_{\mathcal{E}} \text{dl} &\stackrel{\text{def}}{=} (\forall t \in \text{dom}(\text{dl}), \text{id}.(\text{dl}(t) = \text{dl}'(t) = \text{id}) \implies (\text{id}, _) \notin \mathcal{E}(t)) \\
&\quad \wedge (\forall t \in \text{dom}(\text{dl}), \text{id}.(\text{dl}(t) = \text{id} \wedge \text{dl}'(t) = 0) \implies (\text{id}, _) \in \mathcal{E}(t)) \wedge (\forall t \in \text{dom}(\text{dl}), \text{dl}'(t) \leq \text{dl}(t))
\end{aligned}$$

Fig. 19. Auxiliary Definitions for Proof of Compositionality - II

C PROOF OF COMPOSITIONALITY

We prove that our module local simulation can compose to the revised whole program downward simulation in this section.

Lemma 53 (Lemma for compositionality).

- (1) $\forall i \in \text{dom}(\mathbb{T}), i \neq t. \mathbb{T}(i) = (sl_i, \mathbb{F}_i, \mathbb{k}_i) \wedge T(i) = (tl_i, F_i, \kappa_i) \wedge \mathcal{E}(i) \subseteq E \wedge$
 $(\forall \sigma', \Sigma'. \text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}_i), (\sigma, \sigma', F_i)) \implies$
 $\exists \text{id}x'_i. (\beta_i = \circ \implies \text{id}x'_i = \text{id}x_i) \wedge (\mathbb{F}_i, (\mathbb{k}_i, \Sigma'), \text{emp}) \lesssim_{\mu}^{\text{id}x'_i, \mathcal{E}(i)} (F_i, (\kappa_i, \sigma'), \text{emp}));$
- (2) $(\mathbb{F}, (\mathbb{k}, \Sigma), \Delta_0) \lesssim_{\mu}^{\text{id}x_t, \mathcal{E}(t)} (F, (\kappa, \sigma), \delta_0) \wedge \mathbb{T}(t) = (sl, \mathbb{F}, \mathbb{k}) \wedge T(t) = (tl, F, \kappa) \wedge \mathcal{E}(t) \subseteq E$;
- (3) $(T_c, \sigma_c) \xRightarrow{\mu}_B (T, \sigma) \wedge \text{wdPresw}(\mu, n_0, (T_c, t, \text{dl}', \sigma_c), (\widehat{W}_0, B), \delta_r \cup \delta_0) \wedge \text{dl}' \lesssim_{\mathcal{E}} \text{dl} \wedge B \simeq (T, \sigma)$;
- (4) $\widehat{W} \xRightarrow{\mu, E}_B \widehat{W}_0 \wedge \widehat{W}_0^t \xrightarrow{(\Delta_r, \delta_r)} \mu, E \widehat{W}'_0 \wedge \widehat{W}'_0 \xrightarrow{\tau}_{\Delta_0} (T, t, \text{dl}, \Sigma)$;
- (5) $\neg(\hat{\mathbb{P}} \vdash \text{Race}) \wedge \neg(\hat{\mathbb{P}}, E \vdash \text{WA})$, and $(\hat{\mathbb{P}} \xRightarrow{\text{load}} \widehat{W}_p \wedge \widehat{W}_p \xRightarrow{*} \widehat{W}'_p \wedge \widehat{W}'_p \xRightarrow[\text{emp}]{o} \widehat{W})$;
- (6) $\forall \widehat{W}'. ((T, t, \text{dl}, \Sigma) \xRightarrow{*} \widehat{W}') \implies \text{RC_Trans}(\widehat{W}', \mu.S)$;
- (7) for all i, j in $\text{dom}(\mathbb{T})$, and $i \neq j: \mathbb{F}_i \cap \mathbb{F}_j = F_i \cap F_j = \emptyset$, and $\text{wd}(sl_i), \text{wd}(tl_i)$;

then $((T, t, \text{dl}, \Sigma), \Delta_0) \lesssim_{(\widehat{W}_0, B), n_0}^{\text{id}x_t, \mu, E} ((T_c, t, \text{dl}', \sigma_c), \delta_0, \delta_r)$. (where $\text{id}x = ((\beta_1, \text{id}x_1), \dots, (\circ, \text{id}x_t), \dots, (\beta_n, \text{id}x_n))$)

PROOF. Prove by co-induction, and we need to prove the following:

- $t \notin \text{dom}(B), \text{dom}(B) \subseteq \text{dom}(\mathbb{T})$, and $\widehat{W}_0 \xRightarrow[\delta_r \cup \delta_0]{\tau} \widehat{W}$; (* The correctness proof of this case is staright-forward from assumption (3). *)

- if $(\mathbb{T}, t, \mathbb{d}, \Sigma) \xRightarrow[\Delta]{\tau} \widehat{\mathbb{W}}'$, from the high-level step, we get:

$$\widehat{\mathbb{W}}' = (\mathbb{T}', t, \mathbb{d}, \Sigma') \quad (\text{C.1})$$

$$F \vdash (\mathbb{k}, \Sigma) \xRightarrow[\Delta]{\tau} (\mathbb{k}', \Sigma') \quad (\text{C.2})$$

$$\mathbb{T}(t) = (s, \mathbb{F}, \mathbb{k}), \mathbb{T}' = \mathbb{T}\{t \rightsquigarrow (s, \mathbb{F}, \mathbb{k}')\} \quad (\text{C.3})$$

From (C.1), assumption (2) module local simulation, and (7) reachclosed transition, we get that the target may either executes zero or multiply steps, and we discuss each case repectively:

- $\exists \text{idx}'_t < \text{idx}_t. (\mathbb{F}, (\mathbb{k}', \Sigma'), \Delta_0 \cup \Delta) \preceq_{\mu}^{\text{idx}'_t, \mathcal{E}(t)} (F, (\kappa, \sigma), \delta_0)$;
By applying co-inductive hypothesis, we can prove: $\exists \text{idx}' . ((\beta_1, \text{idx}_1), \dots, (\circ, \text{idx}'_t), \dots, (\beta_n, \text{idx}_n))$

$$\text{idx}' < \text{idx} \wedge ((\mathbb{T}', t, \mathbb{d}, \Sigma'), \Delta_0 \cup \Delta) \preceq_{(\widehat{W}_0, B), n_0}^{\text{idx}', \mu, E} ((T_c, t, \mathbb{d}', \sigma_c), \delta_0, \delta_r)$$

And we finish the prove of this case.

- $\exists \kappa', \sigma', \delta, \text{idx}'_t$, such that

$$F \vdash (\kappa, \sigma) \xRightarrow[\delta]{\tau}^n (\kappa', \sigma'), n > 0 \quad (\text{C.4})$$

$$(\delta_0 \cup \delta) \subseteq (F \cup \mu.S), \text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta) \quad (\text{C.5})$$

$$(\mathbb{F}, (\mathbb{k}', \Sigma'), \Delta_0 \cup \Delta) \preceq_{\mu}^{\text{idx}'_t, \mathcal{E}(t)} (F, (\kappa', \sigma'), \delta_0 \cup \delta) \quad (\text{C.6})$$

Here, we need to prove that the footprint δ has no conflict with the footprints recorded in buffer B , which means: $(\forall t' \in \text{dom}(B). B(t').\delta = \delta \implies \neg(\delta' \frown \delta))$. We prove it by contridiction, we assume that there exists a thread t' , where $B(t').\delta = \delta'$ and $(\delta' \frown (\delta_0 \cup \delta))$. So, we get there exists $l \in \mu.S$, and $(l \in (\delta'.ws \cap (\delta_0 \cup \delta)) \vee l \in ((\delta_0 \cup \delta).ws \cup \delta'))$. According to Lemma 66, we know that such location l doesn't exists, so we get:

$$\forall t \in \text{dom}(B). B(t).\delta = \delta' \implies \neg(\delta' \frown \delta) \quad (\text{C.7})$$

From assumption (3), (C.4), (C.7) and Lemma 63, we get there exists σ'_c , such that: $(\text{* reorder}(T_c, \sigma_c) \Rightarrow_B^\mu (T, \sigma) \wedge F \vdash (\kappa, \sigma) \xRightarrow[\delta]{\tau}^n (\kappa', \sigma') \text{*})$

$$F \vdash (\kappa, \sigma_c) \xRightarrow[\delta]{\tau}^n (\kappa', \sigma'_c) \wedge (T_c, \sigma'_c) \Rightarrow_B^\mu (T, \sigma') \quad (\text{C.8})$$

Because t doesn't in the domain of B , according to Lemma 67 we get: $(T_c\{t \rightsquigarrow (tl, F, \kappa')\}, \sigma'_c) \Rightarrow_B^\mu (T\{t \rightsquigarrow (tl, F, \kappa')\}, \sigma')$; From assumption (3), (C.4), (C.7), Lemma 54 and 55, we get: $B \simeq (T\{t \rightsquigarrow (tl, F, \kappa')\}, \sigma')$. From (C.5) (C.8), let $T'_c = T_c\{t \rightsquigarrow (tl, F, \kappa')\}$, and we can prove that:

$$\text{* } (T_c, t, \mathbb{d}', \sigma_c) \xRightarrow[\delta]{\tau}^n (T'_c, t, \mathbb{d}', \sigma'_c);$$

$$\text{* } (\delta_0 \cup \delta) \subseteq (F \cup \mu.S), \text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta);$$

Applying co-inductive hypothesis, we can prove that: $\exists \text{idx}' = ((\beta_1, \text{idx}_1), \dots, (\circ, \text{idx}'_t), \dots, (\beta_n, \text{idx}_n))$

$$\text{* } ((\mathbb{T}', t, \mathbb{d}, \Sigma'), \Delta_0 \cup \Delta) \preceq_{(\widehat{W}_0, B), n_0 + n}^{\text{idx}', \mu, E} ((T'_c, t, \mathbb{d}', \sigma'_c), \delta_0 \cup \delta, \delta_r);$$

We finish the proof of this case.

- if $(\mathbb{T}, t, \mathbb{d}, \Sigma) \xrightarrow[\text{emp}]{o} (\mathbb{T}', t', \mathbb{d}_1, \Sigma) \wedge o \neq \tau$. Here, we consider the high-level program executes existing atomic block operation. The other cases are similar to it. From the high-level step, where $o = \text{id.ExtA}$, we get:

$$\mathbb{T}(t) = (sl, \mathbb{F}, k), \mathbb{d}(t) = \text{id}_0 \quad (\text{C.9})$$

$$t' \in \text{dom}(\mathbb{T}) \quad (\text{C.10})$$

$$\mathbb{F} \vdash (k, \Sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (k', \Sigma) \quad (\text{C.11})$$

$$\mathbb{T}' = \mathbb{T}\{t \rightsquigarrow (sl, \mathbb{F}, k')\} \quad (\text{C.12})$$

$$\mathbb{d}_1 = \mathbb{d}\{t \rightsquigarrow 0\} \quad (\text{C.13})$$

From (C.11), assumption (2) module local simulation, and (7) reachclosed transition, we get that either the target program exits the atomic block, or the atomic block is eliminated in the target program. We discuss each case respectively:

- $\exists \kappa', \kappa'', \sigma', \delta$, such that (* switch points match *)

$$F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau}^* (\kappa', \sigma') \wedge F \vdash (\kappa', \sigma') \xrightarrow[\text{emp}]{\text{id.ExtA}} (\kappa'', \sigma') \quad (\text{C.14})$$

$$(_, \text{id}) \notin \mathcal{E}(t) \quad (\text{C.15})$$

$$\text{LG}(\mu, (\delta_0 \cup \delta, \sigma', F), (\Delta_0, \Sigma')) \quad (\text{C.16})$$

$$\forall \sigma'', \Sigma'. \text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma', \sigma'', F)) \implies \exists \text{id}_t'. (\mathbb{F}, (k', \Sigma'), \text{emp}) \preceq_{\mu}^{\text{id}_t', \mathcal{E}(t)} (F, (\kappa'', \sigma''), \text{emp}) \quad (\text{C.17})$$

Here, we need to prove that the footprint δ has no conflict with the footprints recorded in buffer B , which means: $(\forall t' \in \text{dom}(B). B(t').\delta = \delta' \implies \neg(\delta' \frown \delta))$. We prove it by contradiction, we assume there exists a thread t' , where $B(t').\delta = \delta'$ and $(\delta' \frown \delta)$. So, we get there exists $l \in \mu.S$, and $(l \in (\delta'.ws \cap \delta) \vee l \in (\delta.ws \cap \delta'))$. According to Lemma 66, we know that such location l doesn't exist, so we get:

$$\forall t \in \text{dom}(B). B(t).\delta = \delta' \implies \neg(\delta' \frown \delta) \quad (\text{C.18})$$

From (C.14) (C.18), assumption (3), Lemma 63, and the current thread t doesn't in the domain of reorder buffer B , so we get there exists σ'_c , such that:

$$(* \text{reorder } (T_c, \sigma_c) \Rightarrow_B^\mu (T, \sigma) \wedge F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau}^* (\kappa', \sigma') \wedge F \vdash (\kappa', \sigma') \xrightarrow[\text{emp}]{\text{id.ExtA}} (\kappa'', \sigma') *)$$

$$F \vdash (\kappa, \sigma_c) \xrightarrow[\delta]{\tau}^* (\kappa', \sigma'_c) \wedge F \vdash (\kappa', \sigma'_c) \xrightarrow[\text{emp}]{\text{id.ExtA}} (\kappa'', \sigma'_c) \wedge (T_c, \sigma'_c) \Rightarrow_B^\mu (T, \sigma') \quad (\text{C.19})$$

We discuss why the source and target atomic bit are equal. Assuming the source and target atomic bit are not equal, we get $(\mathbb{d}(t) = \text{id}_0) \wedge \mathbb{d}'(t) = 0$, then according to $\mathbb{d} \lesssim_{\mathcal{E}}^t \mathbb{d}'$, we get $\exists \text{id}_0'. (\text{id}_0, \text{id}_0') \in \mathcal{E}(t) \subseteq E$. From the correctness of analysis result, we get $\text{id}_0' = \text{id}$, and $(\text{id}_0, \text{id}) \in \mathcal{E}(t)$. This is contradiction with $(_, \text{id}) \notin \mathcal{E}(t)$.

From assumption (3), (C.14), (C.18), Lemma 54, and 55, we get:

$$B \simeq (T\{t \rightsquigarrow (tl, F, \kappa'')\}, \sigma') \quad (\text{C.20})$$

According to (C.14) and (C.16), we can prove that for any $t'' \in \text{dom}(\mathbb{T}')$,

$$\begin{aligned}
& * (T_c, t, \mathbb{d}', \sigma_c) \xrightarrow[\delta]{\tau}^* (T'_c, t, \mathbb{d}', \sigma'_c) \wedge (T'_c, t, \mathbb{d}', \sigma'_c) \xrightarrow[\text{emp}]{o} (T''_c, t'', \mathbb{d}'_1, \sigma'_c) \\
& \quad \wedge T'_c = T_c \{t \rightsquigarrow (tl, F, \kappa')\} \wedge T''_c = T'_c \{t \rightsquigarrow (tl, F, \kappa'')\} \wedge \mathbb{d}'_1 = \mathbb{d}' \{t \rightsquigarrow 0\}; \\
& * (\delta_0 \cup \delta) \subseteq (F \cup \mu.S) \wedge \text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta);
\end{aligned}$$

Because t doesn't in the domain of reorder buffer, we get the following according to Lemma 67:

$$(T''_c, \sigma'_c) \Rightarrow_B^\mu (T', \sigma') \quad (* \text{ where } T''_c = T_c \{t \rightsquigarrow (tl, F, \kappa'')\}, T' = T \{t \rightsquigarrow (tl, F, \kappa'')\} *) \quad (\text{C.21})$$

From (C.21) and (C.16), we prove that:

$$* (T''_c, \sigma'_c) \Rightarrow_B^\mu (T', \sigma') \wedge \text{closed}(\mu.S, \sigma');$$

Then, we discuss whether new thread t'' is in the reorder buffer B .

We need to first prove the following. From $\widehat{W} \Rightarrow_B^{\mu, \mathcal{E}} \widehat{W}_0 \wedge \widehat{W}_0^t \xrightarrow{(\Delta_r, \delta_r)}_{\mu, \mathcal{E}} \widehat{W}'_0 \wedge \widehat{W}'_0 : \xrightarrow[\Delta_0]{\tau}^* (T, t, \mathbb{d}, \Sigma) \wedge (T, t, \mathbb{d}, \Sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (T', t'', \mathbb{d}_1, \Sigma)$, and Lemma 69 and 70, let $\widehat{W}_0.t = t_0$, we get: $\exists \widehat{W}_1, \widehat{W}_2, \widehat{W}_3$.

$$\widehat{W}.t \xrightarrow{(\Delta_r, \delta_r)}_{\mu, E} \widehat{W}_1 \wedge \widehat{W}_1 : \xrightarrow[\Delta_0]{\tau}^* \widehat{W}_2 \wedge \widehat{W}_2 \xrightarrow[\text{emp}]{\text{id.ExtA}} \widehat{W}_3 \wedge \widehat{W}_3 \Rightarrow_B^{\mu, E} (T', t_0, \mathbb{d}_1, \Sigma) \quad (\text{C.22})$$

* if $B(t'') = (n'', \delta'', \widehat{W}'')$.

From (C.21), assumption (3), and Lemma 64, we get there exists $\sigma''_c, \kappa_{t''}, \kappa'_{t''}$, such that:

$$\begin{aligned}
T''_c(t'') &= (tl_{t''}, F_{t''}, \kappa_{t''}) \wedge T'(t'') = (tl_{t''}, F_{t''}, \kappa'_{t''}) \wedge \\
F_{t''} &\vdash (\kappa_{t''}, \sigma'_c) \xrightarrow[\delta'']{\tau}^{n''} (\kappa'_{t''}, \sigma''_c) \wedge (T''_c \{t'' \rightsquigarrow (tl_{t''}, F_{t''}, \kappa'_{t''})\}, \sigma''_c) \Rightarrow_{B \setminus \{t''\}}^\mu (T', \sigma')
\end{aligned} \quad (\text{C.23})$$

From (C.20) and $B(t'') = (n'', \delta'', \widehat{W}'')$, we get:

$$\widehat{W}'' . T(t'') = T'(t'') \wedge \widehat{W}'' . t = t'' \wedge \widehat{W}'' . \sigma \xrightarrow{\delta.rs \cup \delta.ws} \sigma' \quad (\text{C.24})$$

From (C.23), (C.24), assumption (3) buffer no conflict, and Lemma 59, we get:

$$\widehat{W}'' . \sigma \xrightarrow{\delta.rs \cup \delta.ws} \sigma''_c \quad (\text{C.25})$$

From (C.23) (C.24) and (C.25), we get that $\exists \widehat{W}_1'' = (\{T''_c \{t'' \rightsquigarrow (tl_{t''}, F_{t''}, \kappa'_{t''})\}, t'', \mathbb{d}'_1, \sigma''_c\})$, such that:

$$(T''_c, t'', \mathbb{d}'_1, \sigma'_c) \xrightarrow[\delta'']{\tau}^{n''} \widehat{W}_1'' \wedge \widehat{W}'' \xrightarrow{\delta''} \widehat{W}_1''$$

Then, we do intro and get the following holds:

$$(T''_c, t'', \mathbb{d}'_1, \sigma'_c) \xrightarrow[\delta'']{\tau}^{n''} \widehat{W}_2'' \wedge \widehat{W}'' \xrightarrow{\delta''} \widehat{W}_2'' \quad (\text{C.26})$$

From Lemma 61, we get: $\widehat{W}_1'' = \widehat{W}_2''$. We can apply co-inductive hypothesis to prove that source and target program can establish simulation relation continuously.

According to (C.22), $B(t'') = (n'', \delta'', \widehat{W}'')$ and Lemma 68, then $\exists \widehat{W}_4, \Delta''$.

$$\widehat{W}_3 \Rightarrow_{B \setminus \{t''\}}^{\mu, E} \widehat{W}_4 \wedge \widehat{W}_4^{t''} \xrightarrow{(\Delta'', \delta'')}_{\mu, E} (T', t'', \mathbb{d}_1, \Sigma)$$

By applying co-inductive hypothesis,

we get there exists $\widehat{W}_0' = (T_c'', t'', d_1', \sigma_c')$ and $\text{idx}' = ((\beta_1, \text{idx}_1), \dots, (\bullet, \text{idx}_t), \dots, (\circ, \text{idx}_{t''}), \dots, (\beta_n, \text{idx}_n))$, such that:

$$((T', t'', d_1, \Sigma), \text{emp}) \preceq_{(\widehat{W}_0', B \setminus \{t''\}), n''}^{\text{idx}', \mu, E} (\widehat{W}_1'', \text{emp}, \delta'')$$

* else, t'' doesn't in the reorder buffer, so by applying co-inductive hypothesis, we know there exists $\widehat{W}_0' = (T_c'', t'', d_1', \sigma_c')$ and $\text{idx}' = ((\beta_1, \text{idx}_1), \dots, (\bullet, \text{idx}_t), \dots, (\circ, \text{idx}_{t''}), \dots, (\beta_n, \text{idx}_n))$ such that:

$$((T', t'', d_1, \Sigma), \text{emp}) \preceq_{(\widehat{W}_0', B), 0}^{\text{idx}', \mu, E} ((T_c'', t'', d_1', \sigma_c'), \text{emp}, \text{emp})$$

– $\exists \kappa', \sigma', \delta, n, \text{idx}_t'$, such that (* switch point not match *)

$$F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau}^n (\kappa', \sigma') \quad (\text{C.27})$$

$$(_, \text{id}) \in \mathcal{E}(t) \quad (\text{C.28})$$

$$n = 0 \implies \text{idx}_t' < \text{idx}_t \quad (\text{C.29})$$

$$\text{LG}(\mu, (\delta_0 \cup \delta, \sigma', F), (\Delta_0, \Sigma)) \quad (\text{C.30})$$

$$\begin{aligned} \forall \sigma'', \Sigma'. \text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma', \sigma'', F)) \implies \\ (\mathbb{F}, (\kappa', \Sigma'), \text{emp}) \preceq_{\mu}^{\text{idx}_t', \mathcal{E}(t)} (F, (\kappa', \sigma''), \text{emp}) \end{aligned} \quad (\text{C.31})$$

Here, we need to prove that the footprint δ has no conflict with the footprints recorded in buffer B , which means: $(\forall t' \in \text{dom}(B). B(t').\delta = \delta' \implies \neg(\delta \frown \delta'))$. We prove it by contradiction, we assume that there exists a thread t' , where $B(t').\delta = \delta'$ and $(\delta' \frown \delta)$. So, we get there exists $l \in \mu.S$, such that $(l \in (\delta'.ws \cap \delta) \vee l \in (\delta.ws \cap \delta'))$. According to Lemma 66, we know that such location l doesn't exist, so we get:

$$\forall t \in \text{dom}(B). B(t).\delta = \delta' \implies \neg(\delta' \frown \delta) \quad (\text{C.32})$$

From (C.27), (C.32), assumption (3) and Lemma 63, so we get there exists σ_c' , such that: (* reorder $(T_c, \sigma_c) \Rightarrow_B^\mu (T, \sigma) \wedge F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau}^n (\kappa', \sigma') *$)

$$F \vdash (\kappa, \sigma_c) \xrightarrow[\delta]{\tau}^n (\kappa', \sigma_c') \wedge (T_c, \sigma_c') \Rightarrow_B^\mu (T, \sigma') \quad (\text{C.33})$$

Because the current thread t doesn't in the domain of reorder buffer and according to Lemma 67, we get:

$$(T_c', \sigma_c') \Rightarrow_B^\mu (T', \sigma') \quad (\text{let } T_c' = T_c \{t \rightsquigarrow (tl, F, \kappa')\}, T' = T \{t \rightsquigarrow (tl, F, \kappa')\}) \quad (\text{C.34})$$

We discuss why the source and target atomic bit are not equal. Assuming the source and target atomic bit are equal, we get $(d(t) = d'(t) = \text{id}_0)$, then according to $d \lesssim_{\mathcal{E}} d'$, we get $(\text{id}_0, _) \notin \mathcal{E}(t)$. We also know there exists id_0' , such that $(\text{id}_0', \text{id}) \in \mathcal{E}(t) \subseteq E$. According to the correctness of analysis result, we get $\text{id}_0 = \text{id}_0'$ and $(\text{id}_0, \text{id}) \in \mathcal{E}(t)$. This is contradicted with $(\text{id}_0, _) \notin \mathcal{E}(t)$.

We can prove that: $d(t) = \text{id}_0 \wedge d'(t) = 0 \wedge (\text{id}_0, \text{id}) \in \mathcal{E}(t) \subseteq E$.

According to (C.33), and (C.30), we can prove that

for any $t'' \in \text{dom}(T')$, $\exists \text{idx}' = ((\beta_1, \text{idx}_1), \dots, (\circ, \text{idx}_t'), \dots, (\circ, \text{idx}_{t''}'), \dots, (\beta_n, \text{idx}_n))$ such that

* $(T_c, t, d', \sigma_c') \xrightarrow[\delta]{\tau}^n (T_c \{t \rightsquigarrow (tl, F, \kappa')\}, t, d', \sigma_c')$;

* $(n = 0 \implies \text{idx}' < \text{idx}), \text{id.ExtA} \in E$;

* $(\delta_0 \cup \delta) \subseteq (F \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta)$;

Let $B' = B\{t \rightsquigarrow (n_0 + n, \delta_r \cup \delta_0 \cup \delta, (T_c\{t \rightsquigarrow (tl, F, \kappa')\}, t, dl', \sigma'_c))\}$. From (C.33) (C.30) (C.34) (C.32), assumption (3) and Lemma 65, we get:

$$(\widehat{W}_0.T, \widehat{W}_0.\sigma) \Rightarrow_{B'}^\mu (T', \sigma') \quad (\text{C.35})$$

We discuss whether t'' is in reorder buffer.

Before discuss whether the t'' is in the reorder buffer, we first prove the following:

From $\widehat{W} \Rightarrow_B^{\mu, E} \widehat{W}_0, \widehat{W}_0^t \xrightarrow{(\Delta_r, \delta_r)}_{\mu, E} \widehat{W}'_0, \widehat{W}'_0 \xrightarrow[\Delta_0]{\tau} (T, t, dl, \Sigma), (T, t, dl, \Sigma) \xrightarrow[\text{emp}]{\text{id.ExtA}} (T', t'', dl_1, \Sigma), dl(t) = \text{id}_0$, $\text{FPmatch}(\mu, \Delta_0, \delta_0 \cup \delta)$, we get:

$$\widehat{W} \Rightarrow_B^{\mu, E} \widehat{W}_0 \wedge \widehat{W}_0^t \xrightarrow{(\Delta_r \cup \Delta_0, \delta_r \cup \delta_0 \cup \delta)}_{\mu, E} (T', t, dl_1, \Sigma)$$

We can merge them together and get: $\widehat{W} \Rightarrow_{B'}^{\mu, E} (T', t, dl_1, \Sigma)$

From (C.34), $\sigma'_c \xrightarrow{(\delta_r \cup \delta_0 \cup \delta).rs \cup (\delta_r \cup \delta_0 \cup \delta).ws} \sigma'_c$ and Lemma 60, we get: $\sigma'_c \xrightarrow{(\delta_r \cup \delta_0 \cup \delta).rs \cup (\delta_r \cup \delta_0 \cup \delta).ws} \sigma'$
According to $B \simeq (T, \sigma)$, (C.32), (C.27), $t \notin \text{dom}(B)$, and Lemma 54 and 55, we get: $B \simeq (T', \sigma')$
Finally, we can prove:

$$B' \simeq (T', \sigma') \quad (\text{C.36})$$

* If $B'(t'') = (n'', \delta'', \widehat{W}'')$, from (C.22) and Lemma 64, we get there exists $\sigma_0, \kappa_{t''}, \kappa'_{t''}$, such that:

$$\begin{aligned} \widehat{W}_0.T(t'') &= (tl_{t''}, F_{t''}, \kappa_{t''}) \wedge T'(t'') = (tl_{t''}, F_{t''}, \kappa_{t''}) \\ &\wedge F_{t''} \vdash (\kappa_{t''}, \widehat{W}_0.\sigma) \xrightarrow[\delta'']{\tau} (\kappa'_{t''}, \sigma_0) \wedge (\widehat{W}_0.T\{t'' \rightsquigarrow (tl_{t''}, F_{t''}, \kappa'_{t''})\}, \sigma_0) \Rightarrow_{B' \setminus \{t''\}}^\mu (T', \sigma') \end{aligned} \quad (\text{C.37})$$

And we get:

$$\widehat{W}_0^{t''} \xrightarrow[\delta'']{\tau} (\widehat{W}_0.T\{t'' \rightsquigarrow (tl_{t''}, F_{t''}, \kappa'_{t''})\}, t'', \widehat{W}_0.dl, \sigma_0) \quad (\text{C.38})$$

$$(\widehat{W}_0.T\{t'' \rightsquigarrow (tl_{t''}, F_{t''}, \kappa'_{t''})\}, t'', \widehat{W}_0.dl, \sigma_0) \Rightarrow_{B' \setminus \{t''\}}^\mu (T', \sigma') \quad (\text{C.39})$$

According to (C.36), $B'(t'') = (n'', \delta'', \widehat{W}'')$, we get:

$$\widehat{W}'' . T(t'') = T'(t'') \wedge \widehat{W}'' . t = t'' \wedge \widehat{W}'' . \sigma \xrightarrow{\delta.rs \cup \delta.ws} \sigma' \quad (\text{C.40})$$

From (C.40) (C.39) and Lemma 59, the following holds:

$$\widehat{W}'' . \sigma \xrightarrow{\delta.rs \cup \delta.ws} \sigma_0 \quad (\text{C.41})$$

Then, we can prove the following holds, according to (C.39) (C.40) (C.41) and Lemma 62:

$$(\widehat{W}_0.T\{t'' \rightsquigarrow (tl_{t''}, F_{t''}, \kappa'_{t''})\}, t'', \widehat{W}_0.dl, \sigma_0) \xrightarrow{\delta} \widehat{W}'' \quad (\text{C.42})$$

We do intro and get the following holds:

$$\widehat{W}_0^{t''} \xrightarrow[\delta'']{\tau} \widehat{W}_2'' \wedge \widehat{W}_2'' \xrightarrow{\delta} \widehat{W}'' \quad (\text{C.43})$$

From (C.38) (C.42) (C.43) and Lemma 61, we get: $(\widehat{W}_0.T\{t'' \rightsquigarrow (tl_{t''}, F_{t''}, \kappa'_{t''})\}, t'', \widehat{W}_0.dl, \sigma_0) = \widehat{W}_2''$, we can apply co-inductive hypothesis to prove that source and target program can establish simulation

relation continuously.

According to $\widehat{W} \Rightarrow_{B'}^{\mu, E} (\mathbb{T}', t, \mathbb{d}_1, \Sigma)$ and Lemma 68, we get: $\exists \widehat{W}_1, \Delta''$.

$$\widehat{W} \Rightarrow_{B' \setminus \{t''\}}^{\mu, E} \widehat{W}_1 \wedge \widehat{W}_1^{t''} \xRightarrow{(\Delta'', \delta'')}_{\mu, E} (\mathbb{T}', t'', \mathbb{d}_1, \Sigma)$$

By applying co-inductive hypothesis, we get:

$$((\mathbb{T}', t'', \mathbb{d}_1, \Sigma), \text{emp}) \preceq_{(\widehat{W}_0^{t''}, B' \setminus \{t''\}), n''}^{\text{id}x', \mu, E} ((\widehat{W}_0.T\{t'' \rightsquigarrow (tl_{t''}, F_{t''}, \kappa'_{t''})\}, t'', \widehat{W}_0.\mathbb{d}, \sigma_0), \text{emp}, \delta'')$$

* else, if t'' doesn't in reorder buffer B' , by applying co-inductive hypothesis, we get:

$$((\mathbb{T}', t'', \mathbb{d}_1, \Sigma), \text{emp}) \preceq_{(\widehat{W}_0^{t''}, B'), 0}^{\text{id}x', \mu, E} (\widehat{W}_0^{t''}, \text{emp}, \text{emp})$$

We finish the proof of this case.

- The proof of case **done** is staright-forward.

□

Lemma 54 (No conflict steps buffer sim stable). For any $B, T, \kappa, \sigma, \kappa'$ and σ' , if $B \simeq (T, \sigma)$, $F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau}^n (\kappa', \sigma')$ and $(\forall t \in \text{dom}(B). B(t).\delta = \delta' \implies \neg(\delta' \frown \delta))$, then $B \simeq (T, \sigma')$.

PROOF. Prove by induction on the number of elements in buffer. □

Lemma 55 (Upd no buffer thread buffer sim stable). For any B, T, σ and t , if $B \simeq (T, \sigma)$ and $t \notin \text{dom}(B)$, then $B \simeq (T\{t \rightsquigarrow (tl, F, \kappa)\}, \sigma)$.

Lemma 56 (Buffer eliminate buffer sim stable). For any B, T, σ and t , if $B \simeq (T, \sigma)$, then $B \setminus \{t\} \simeq (T, \sigma)$.

Lemma 57 (Location no effect equal). For any σ, σ', δ and δ' , if $\sigma \xrightarrow[\delta'.rs \cup \delta'.ws]{\text{dom}(\sigma) - \delta.ws} \sigma'$, $(\text{dom}(\sigma') - \text{dom}(\sigma)) \subseteq \delta.ws$, $\neg(\delta' \frown \delta)$, then $\sigma \xrightarrow[\delta'.rs \cup \delta'.ws]{} \sigma'$.

Lemma 58 (Memory set equal transitivy). For any $\sigma, \sigma', \sigma''$ and δ , if $\sigma \xrightarrow[\delta.rs \cup \delta.ws]{\delta.rs \cup \delta.ws} \sigma'$ and $\sigma' \xrightarrow[\delta.rs \cup \delta.ws]{\delta.rs \cup \delta.ws} \sigma''$, then $\sigma \xrightarrow[\delta.rs \cup \delta.ws]{} \sigma''$.

Lemma 59 (Location no effect apply buffer stable). For any $T_c, \sigma_c, T', \sigma', \sigma_0$ and δ , if $(T_c, \sigma_c) \Rightarrow_B^\mu (T', \sigma')$, $\sigma_0 \xrightarrow[\delta.rs \cup \delta.ws]{\delta.rs \cup \delta.ws} \sigma'$ and $(\forall t \in \text{dom}(B). B(t).\delta = \delta' \implies \neg(\delta' \frown \delta))$, then $\sigma_0 \xrightarrow[\delta.rs \cup \delta.ws]{\delta.rs \cup \delta.ws} \sigma_c$.

Lemma 60 (Location no effect apply buffer stable - II). For any $T_c, \sigma_c, T', \sigma', \sigma_0$ and δ , if $(T_c, \sigma_c) \Rightarrow_B^\mu (T', \sigma')$, $\sigma_0 \xrightarrow[\delta.rs \cup \delta.ws]{\delta.rs \cup \delta.ws} \sigma_c$ and $(\forall t \in \text{dom}(B). B(t).\delta = \delta' \implies \neg(\delta' \frown \delta))$, then $\sigma_0 \xrightarrow[\delta.rs \cup \delta.ws]{\delta.rs \cup \delta.ws} \sigma'$.

Lemma 61 (Same effect world equal). For any $\widehat{W}_0, \widehat{W}, \widehat{W}_1, \widehat{W}_2, \delta$ and n , if $\widehat{W}_0 \xrightarrow[\delta]{\tau}^n \widehat{W}_1, \widehat{W}_1 \xrightarrow[\delta]{\delta} \widehat{W}, \widehat{W}_0 \xrightarrow[\delta]{\tau}^n \widehat{W}_2$ and $\widehat{W}_2 \xrightarrow[\delta]{\delta} \widehat{W}$, then $\widehat{W}_1 = \widehat{W}_2$.

The correctness proof of Lemma 54, 59, 60 and 61 depends on the languages are well defined.

Lemma 62 (Apply buffer not in domain thread state stable). For any $T_c, \sigma_c, T, \sigma, \mu, B$ and t , if $(T_c, \sigma_c) \Rightarrow_B^\mu (T, \sigma)$, $t \notin \text{dom}(B)$, then $T_c(t) = T(t)$.

Lemma 63 (Reordering Reorder Buffer and Steps No-conflict).

For any $T_c, T, \sigma_c, \kappa, \sigma, \kappa', \sigma', \delta, n, \mu$ and B , if $(T_c, \sigma_c) \Rightarrow_B^\mu (T, \sigma)$, $F \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau}^n (\kappa', \sigma')$, and $(\forall t \in \text{dom}(B). B(t). \delta = \delta' \implies \neg(\delta \frown \delta'))$, then $\exists \sigma'_c. F \vdash (\kappa, \sigma_c) \xrightarrow[\delta]{\tau}^n (\kappa', \sigma'_c) \wedge (T_c, \sigma'_c) \Rightarrow_B^\mu (T, \sigma')$.

PROOF. We prove it by induction on the number of elements in buffer B . (* on paper proof *) \square

Lemma 64 (Redo executions in reorder buffer). For $T_c, T, \sigma_c, \sigma, \delta, \mu$ and n , if $(T_c, \sigma_c) \Rightarrow_B^\mu (T, \sigma)$, $B(t) = (n, \delta)$, and $(\forall t_1, t_2 \in \text{dom}(B), t_1 \neq t_2. (B(t_1). \delta = \delta_1 \wedge B(t_2). \delta = \delta_2) \implies \neg(\delta_1 \frown \delta_2))$, then:

$$\begin{aligned} \exists \sigma'_c, \kappa, \kappa'. T_c(t) = (tl, F, \kappa) \wedge T(t) = (tl, F, \kappa') \\ \wedge F \vdash (\kappa, \sigma_c) \xrightarrow[\delta]{\tau}^n (\kappa', \sigma'_c) \wedge (T_c\{t \rightsquigarrow (tl, F, \kappa')\}, \sigma'_c) \Rightarrow_{B \setminus \{t\}}^\mu (T, \sigma). \end{aligned}$$

PROOF. We prove it by induction on the number of elements in buffer B . \square

Lemma 65 (Applybuffer merge). For any $F_0, \kappa_0, \sigma_0, \kappa'_0, \delta_0, n_0, T_0, \mu, B, T', \sigma'$ and \widehat{W}_0 ,

if $T_0(t_0) = (tl_0, F_0, \kappa_0)$, $F_0 \vdash (\kappa_0, \sigma_0) \xrightarrow[\delta_0]{\tau}^{n_0} (\kappa'_0, \sigma')$, $t_0 \notin \text{dom}(B)$, $\delta_0 \subseteq (F_0 \cup \mu.S)$, $(T_0\{t_0 \rightsquigarrow (tl_0, F_0, \kappa_0)\}, \sigma) \Rightarrow_B^\mu (T', \sigma')$, then $(T_0, \sigma_0) \Rightarrow_{B \cup \{t_0 \rightsquigarrow (n_0, \delta_0, \widehat{W}_0)\}}^\mu (T', \sigma')$.

PROOF. Prove by induction on the number of elements in buffer B . (* on paper proof *) \square

Lemma 66 (Contridiction for fp-conflict in reorder buffer).

For any $\widehat{W}, \widehat{W}_0, \widehat{W}', \mu, B, E, \Delta, \delta, t, t', \delta', \hat{P}, \widehat{W}_p, \widehat{W}'_p$ and o , if

- (1) $\widehat{W} \Rightarrow_B^{\mu, E} \widehat{W}_0$, $\widehat{W}_0 \xrightarrow[\Delta]{\tau}^* \widehat{W}'$, $t \notin \text{dom}(B)$;
- (2) $\text{FPmatch}(\mu, \Delta, \delta), B(t'). \delta = \delta', t' \neq t$;
- (3) $\neg((\hat{P}, E) \vdash \text{WA}) \wedge \neg(\hat{P} \vdash \text{Race})$;
- (4) $\hat{P} \xrightarrow{\text{load}} \widehat{W}_p \wedge \widehat{W}_p \Rightarrow^* \widehat{W}'_p \wedge \widehat{W}'_p \xrightarrow[\text{emp}]{o} \widehat{W}$;

then $\neg(\exists l \in \mu.S. (l \in (\delta'. \text{ws} \cap \delta)) \vee (l \in (\delta. \text{ws} \cap \delta')))$.

PROOF. We prove it by induction on the number of elements in buffer B . (* on paper proof *) \square

Lemma 67 (Applying reorder buffer stable). For any $T, \sigma, T', \sigma', B, \mu, t, tl, F$, and κ , if $(T, \sigma) \Rightarrow_B^\mu (T', \sigma')$ and $t' \notin \text{dom}(B)$, then $(T\{t \rightsquigarrow (tl, F, \kappa)\}, \sigma) \Rightarrow_B^\mu (T'\{t \rightsquigarrow (tl, F, \kappa)\}, \sigma')$.

Lemma 68 (Source apply buffer splitting). For any $\widehat{W}, \widehat{W}_1, B, t, n, \delta, E, \mu, \hat{P}, \widehat{W}_p, \widehat{W}'_p$ and o , if

- (1) $\widehat{W} \Rightarrow_B^{\mu, E} \widehat{W}_1$, $B(t) = (n, \delta)$;
- (2) $\neg((\hat{P}, E) \vdash \text{WA}) \wedge \neg(\hat{P} \vdash \text{Race})$;
- (3) $\hat{P} \xrightarrow{\text{load}} \widehat{W}_p \wedge \widehat{W}_p \Rightarrow^* \widehat{W}'_p \wedge \widehat{W}'_p \xrightarrow[\text{emp}]{o} \widehat{W}$;

then $\exists \widehat{W}_0, \Delta. \widehat{W} \Rightarrow_{B \setminus \{t\}}^{\mu, E} \widehat{W}_0 \wedge \widehat{W}_0 \xrightarrow[\mu, E]{(\Delta, \delta)} \widehat{W}_1$.

PROOF. Prove by induction on the number of elements in B , and applying Lemma 72. (* on paper proof *) \square

Lemma 69 (Reordering of reorder buffer and thread local step).

For any $\widehat{W}_0, \widehat{W}_1, \widehat{W}_2, \widehat{W}, t_1, t_2, \mu, B, \Delta, \delta, \hat{P}, \widehat{W}_p, \widehat{W}'_p, E$ and o , if

- (1) $\widehat{W}_0 \Rightarrow_B^{\mu, E} \widehat{W}_1^{t_1}, \widehat{W}_1^{t_2} \xrightarrow[\mu, E]{(\Delta, \delta)} \widehat{W}, t_2 \notin \text{dom}(B)$;

- (2) $\neg((\hat{P}, E) : \Longrightarrow WA) \wedge \neg(\hat{P} : \Longrightarrow \text{Race});$
- (3) $\hat{P} \xrightarrow{\text{load}} \hat{W}_p \wedge \hat{W}_p \Rightarrow^* \hat{W}'_p \wedge \hat{W}'_p \xrightarrow[\text{emp}]{o} \hat{W}_0;$

then $\exists \hat{W}_2, t. \hat{W}_0^{t_2} \xrightarrow{(\Delta, \delta)}_{\mu, E} \hat{W}_2 \wedge \hat{W}_2^t \Rightarrow^{\mu, E}_B \hat{W}^{t_1}.$

PROOF. Prove by induction on the number of elements in B , and applying Lemma 72. (* on paper proof *) \square

Lemma 70 (Reordering of reorder buffer and tau step).

For any $\hat{W}_0, \hat{W}_1, \hat{W}_2, \hat{W}, t_1, t_2, \mu, B, \Delta, \hat{P}, \hat{W}_p, \hat{W}'_p, E$ and o , if

- (1) $\hat{W}_0 \Rightarrow^{\mu, E}_B \hat{W}_1^{t_1}, \hat{W}_1^{t_2} \xrightarrow[\Delta]{\tau}^* \hat{W}, t_2 \notin \text{dom}(B);$
- (2) $\neg((\hat{P}, E) : \Longrightarrow WA) \wedge \neg(\hat{P} : \Longrightarrow \text{Race});$
- (3) $\hat{P} \xrightarrow{\text{load}} \hat{W}_p \wedge \hat{W}_p \Rightarrow^* \hat{W}'_p \wedge \hat{W}'_p \xrightarrow[\text{emp}]{o} \hat{W}_0;$

then $\exists \hat{W}_2, t. \hat{W}_0^{t_2} \xrightarrow[\Delta]{\tau}^* \hat{W}_2 \wedge \hat{W}_2^t \Rightarrow^{\mu, E}_B \hat{W}^{t_1}.$

PROOF. Prove by induction on the number of elements in B , and applying Lemma 73. (* on paper proof *) \square

Lemma 71 (Contridiction for fp-conflict in thread local step and tau step).

For any $\hat{W}, \hat{W}_1, \hat{W}_2, t, t_1, \Delta, \delta, \Delta', \delta', \mu, E, \hat{P}, \hat{W}_p, \hat{W}'_p$ and o , if

- (1) $\hat{W}^t \xrightarrow{(\Delta, \delta)}_{\mu, E} \hat{W}_1, \hat{W}_1^{t_1} \xrightarrow[\Delta']{\tau}^* \hat{W}_2, t \neq t_1;$
- (2) $\text{FPmatch}(\mu, \Delta', \delta');$
- (3) $\neg((\hat{P}, E) : \Longrightarrow WA) \wedge \neg(\hat{P} : \Longrightarrow \text{Race});$
- (4) $\hat{P} \xrightarrow{\text{load}} \hat{W}_p \wedge \hat{W}_p \Rightarrow^* \hat{W}'_p \wedge \hat{W}'_p \xrightarrow[\text{emp}]{o} \hat{W};$

then $\neg(\exists l \in \mu.S. (l \in (\delta'.ws \cap \delta)) \vee (l \in (\delta.ws \cap \delta'))).$

Lemma 72 (Reordering of thread local step). For any $\hat{W}, \hat{W}_1, \hat{W}_2, t, t_1, \hat{P}, E, \hat{W}_p, \hat{W}'_p$ and o , if

- (1) $\hat{W}^t \xrightarrow{(\Delta, \delta)}_{\mu, E} \hat{W}_1, \hat{W}_1^{t_1} \xrightarrow{(\Delta_1, \delta_1)}_{\mu, E} \hat{W}_2, t \neq t_1;$
- (2) $\neg((\hat{P}, E) : \Longrightarrow WA) \wedge \neg(\hat{P} : \Longrightarrow \text{Race});$
- (3) $\hat{P} \xrightarrow{\text{load}} \hat{W}_p \wedge \hat{W}_p \Rightarrow^* \hat{W}'_p \wedge \hat{W}'_p \xrightarrow[\text{emp}]{o} \hat{W};$

then $\exists \hat{W}_3. \hat{W}^{t_1} \xrightarrow{(\Delta_1, \delta_1)}_{\mu, E} \hat{W}_3 \wedge \hat{W}_3^t \xrightarrow{(\Delta, \delta)}_{\mu, E} \hat{W}_2^t.$

PROOF. Prove by induction on the steps of thread local steps, and applying Lemma 73 (* on paper proof *) \square

Lemma 73 (Reordering of thread local step and tau step).

For any $\hat{W}, \hat{W}_1, \hat{W}_2, t, t_1, \Delta, \Delta_1, \delta, \hat{P}, E, \hat{W}_p, \hat{W}'_p$ and o , if

- (1) $\hat{W}^t \xrightarrow{(\Delta, \delta)}_{\mu, E} \hat{W}_1, \hat{W}_1^{t_1} \xrightarrow[\Delta_1]{\tau}^* \hat{W}_2, t \neq t_1;$
- (2) $\neg((\hat{P}, E) : \Longrightarrow WA) \wedge \neg(\hat{P} : \Longrightarrow \text{Race});$
- (3) $\hat{P} \xrightarrow{\text{load}} \hat{W}_p \wedge \hat{W}_p \Rightarrow^* \hat{W}'_p \wedge \hat{W}'_p \xrightarrow[\text{emp}]{o} \hat{W};$

then $\exists \hat{W}_3. \hat{W}^{t_1} \xrightarrow[\Delta_1]{\tau}^* \hat{W}_3 \wedge \hat{W}_3^t \xrightarrow{(\Delta, \delta)}_{\mu, E} \hat{W}_2^t.$

We use some figures to illustrate the technologies of compositionality proof. We first need to know:

- (1) The memory of target program used in *module local simulation* is the memory **after** applying reorder buffer;
- (2) The memory of target program used in *whole program downward simulation* is the memory **before** applying reorder buffer.

We consider each case respectively.

- The source program executes τ -step:

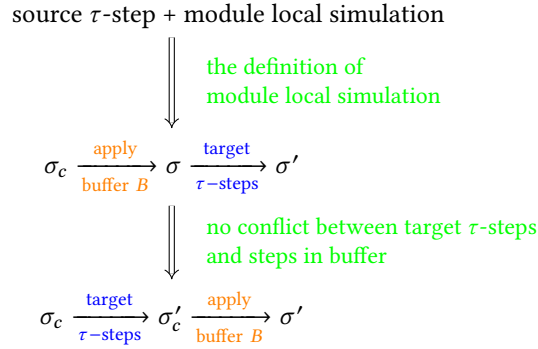


Fig. 20. Technology in Compositionality Proof — τ -step

- The source program executes switch step:

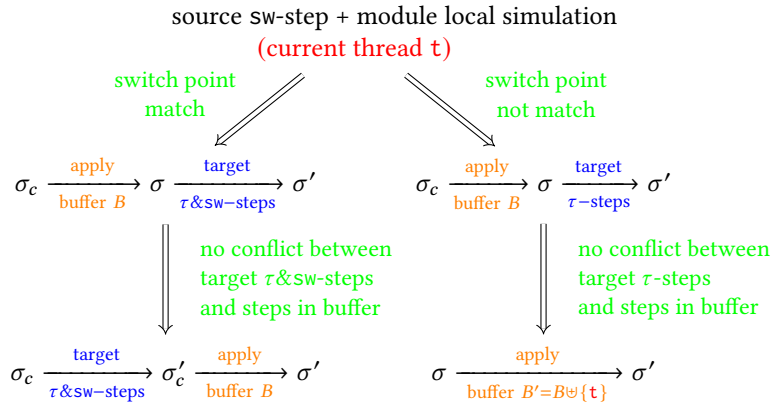


Fig. 21. Technology in Compositionality Proof — sw-step

When the program (source and target) switches to the new thread t' , we need to get the buffered step of t' from reorder buffer and do the following proof.

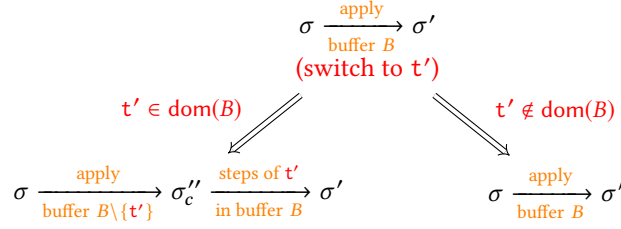


Fig. 22. Technology in Compositionality Proof — redo buffered step

D PROOF OF FLIP

Definition 74 (Indexed Whole-Program Relation). $(\widehat{W}, \Delta_0) \times_{\widehat{W}_0}^{\mu, n, E} (\widehat{W}, \delta_0)$ holds, iff there exists t , such that $\widehat{W}.t = \widehat{W}.t = t$, $\widehat{W}.d \setminus \{t\} = \widehat{W}.d \setminus \{t\}$, $\widehat{W}.d(t) \leq \widehat{W}.d(t)$, and one of the following are true:

- (1) either $\exists \widehat{W}', \widehat{W}', \delta, \delta_r, \Delta, j, n_0$ such that
 - (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}', \widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}'$;
 - (b) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$;
 - (c) $(\widehat{W}', \Delta_0 \cup \Delta) \preceq_{(\widehat{W}_0, \emptyset), n_0}^{j, \mu, E} (\widehat{W}', \delta_0 \cup \delta, \delta_r)$;
- (2) or $\exists \widehat{W}', \widehat{W}', \delta, \delta_r, \Delta, j, n_0$ such that
 - (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}', \widehat{W} \xrightarrow[\Delta]{\text{e-syn}(E)} \widehat{W}'$;
 - (b) $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$;
 - (c) $(\widehat{W}', \text{emp}) \preceq_{(\widehat{W}_0, \emptyset), n_0}^{j, \mu, E} (\widehat{W}', \text{emp}, \delta_r \cup \delta_0 \cup \delta)$;
- (3) or $\exists \widehat{W}', \widehat{W}_1, \widehat{W}', \delta, \delta_r, \Delta_1, \Delta, j, n_0$ such that
 - (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}', \widehat{W}.d(t) = 0$, and $\widehat{W} \xrightarrow[\Delta_1]{\text{e-syn}(E)} \widehat{W}_1, \widehat{W}_2 \xrightarrow[\Delta]{\tau} \widehat{W}'$;
 - (b) $\text{FPmatch}(\mu, \Delta_0 \cup \Delta_1, \delta_0)$, and $\delta \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta, \delta)$;
 - (c) $(\widehat{W}', \Delta) \preceq_{(\widehat{W}_0, \emptyset), n_0}^{j, \mu, E} (\widehat{W}', \delta, \delta_r \cup \delta_0)$;
- (4) or $\exists \widehat{W}', \widehat{W}_1, \widehat{W}_2, \widehat{W}', \delta, \delta_r, \Delta_1, \Delta, j, n_0$ such that
 - (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}', \widehat{W}.d(t) = 0$, and $\widehat{W} \xrightarrow[\Delta_1]{\text{e-syn}(E)} \widehat{W}_1, \widehat{W}_2 \xrightarrow[\Delta]{\text{e-syn}(E)} \widehat{W}'$;
 - (b) $\text{FPmatch}(\mu, \Delta_0 \cup \Delta_1, \delta_0)$, and $\delta \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta, \delta)$;
 - (c) $(\widehat{W}', \text{emp}) \preceq_{(\widehat{W}_0, \emptyset), n_0}^{j, \mu, E} (\widehat{W}', \text{emp}, \delta_r \cup \delta_0 \cup \delta)$;
- (5) or $\exists \widehat{W}', T', d', \sigma', o \neq \tau, \widehat{W}', T', \Delta, j$, for any $t' \in \text{dom}(T')$, such that
 - (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}', \widehat{W}' \xrightarrow[\text{emp}]{o} (T', t', d', \sigma')$, $\text{closed}(\mu.S, \sigma')$;
 - (b) $\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}', \widehat{W}' \xrightarrow[\text{emp}]{o} (T', t', d', \Sigma')$;
 - (c) $((T', t', d', \Sigma'), \text{emp}) \preceq_{((T', t', d', \sigma'), \emptyset), 0}^{j, \mu, E} ((T', t', d', \sigma'), \text{emp}, \text{emp})$;
- (6) or $\exists \widehat{W}', T', d', \sigma', o \neq \tau, \widehat{W}_1, \widehat{W}_2, T', \Delta_1, \Delta, j$, for any $t' \in \text{dom}(T')$, such that
 - (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}', \widehat{W}' \xrightarrow[\text{emp}]{o} (T', t', d', \sigma')$, $\text{closed}(\mu.S, \sigma')$;

- (b) $\widehat{W} \xrightarrow[\Delta_1]{e\text{-syn}(E)} \widehat{W}_1, \widehat{W}_1 \xrightarrow[\Delta]{\tau} \widehat{W}_2, \widehat{W}_2 \xrightarrow[\text{emp}]{o} (\mathbb{T}', \mathbf{t}', \mathbf{d}', \Sigma');$
- (c) $\text{FPmatch}(\mu, \Delta_0 \cup \Delta_1, \delta_0)$, and $\delta_0 \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta, \delta);$
- (d) $((\mathbb{T}', \mathbf{t}', \mathbf{d}', \Sigma'), \text{emp}) \preceq_{((T', \mathbf{t}', \mathbf{d}', \sigma'), \emptyset), 0}^{j, \mu, E} ((T', \mathbf{t}', \mathbf{d}', \sigma'), \text{emp}, \text{emp});$
- (7) or $\exists \widehat{W}', \delta, \widehat{W}', \Delta$, such that:
- (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}', \widehat{W}' \xrightarrow[\text{emp}]{\tau} \text{done}, \widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}', \widehat{W}' \xrightarrow[\text{emp}]{\tau} \text{done};$
- (b) $(\delta \cup \delta_0) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta);$
- (8) or $\exists \widehat{W}', \widehat{W}_1, \widehat{W}_2, \delta, \Delta_1, \Delta$, such that:
- (a) $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}', \widehat{W}' \xrightarrow[\text{emp}]{\tau} \text{done};$
- (b) $\widehat{W} \xrightarrow[\Delta_1]{e\text{-syn}(E)} \widehat{W}_1, \widehat{W}_1 \xrightarrow[\Delta]{\tau} \widehat{W}_2, \widehat{W}_2 \xrightarrow[\Delta]{\tau} \text{done};$
- (c) $\delta \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0 \cup \Delta_1, \delta_0)$, $\text{FPmatch}(\mu, \Delta, \delta).$

Lemma 75 (Index Relation). For any $\widehat{W}, \widehat{W}, \Delta_0, \delta_0, \delta_r, i, n_0, \mu, E, \widehat{W}_0$ and \mathbf{t} , if

- $\text{Safe}(\widehat{W}), \widehat{W}.\mathbf{t} = \widehat{W}.\mathbf{t} = \mathbf{t};$
- $(\widehat{W}, \Delta_0) \preceq_{(\widehat{W}_0, \emptyset), n_0}^{i, \mu, E} (\widehat{W}, \delta_0, \delta_r);$
- $\widehat{W}.\mathbf{d} \setminus \{\mathbf{t}\} = \widehat{W}.\mathbf{d} \setminus \{\mathbf{t}\}, \widehat{W}.\mathbf{d}(\mathbf{t}) \leq \widehat{W}.\mathbf{d}(\mathbf{t});$

then $\exists n. (\widehat{W}, \Delta_0) \times_{\widehat{W}_0}^{\mu, n, E} (\widehat{W}, \delta_0).$

PROOF. Prove by induction on the index i and applying Lemma 77. □

Lemma 76 (Lemma for Flip). For any $\widehat{W}, \widehat{W}, \Delta_0, \delta_0, \mu, E, \widehat{W}_0$ and n , if $\text{Safe}(\widehat{W})$ and all the target languages are deterministic, then

$$(\widehat{W}, \Delta_0) \times_{\widehat{W}_0}^{\mu, n, E} (\widehat{W}, \delta_0) \implies (\widehat{W}, \delta_0) \leq_{\mu}^{n, E} (\widehat{W}, \Delta_0)$$

PROOF. The proof of this lemma can be achieved by co-induction and applying Lemma 75 and 78. □

Lemma 77. If $\widehat{W} \xrightarrow[\Delta]{\tau} \widehat{W}'$ and $(\widehat{W}', \Delta_0 \cup \Delta) \times_{\widehat{W}_0}^{\mu, n, E} (\widehat{W}, \delta, \delta_r)$, then $(\widehat{W}, \Delta_0) \times_{\widehat{W}_0}^{\mu, n+1, E} (\widehat{W}, \delta_0).$

Lemma 78. If $\widehat{W} \xrightarrow[\text{emp}]{o} \widehat{W}', \delta_0 \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$, $\text{FPmatch}(\mu, \Delta_0, \delta_0)$ and $(\widehat{W}', \text{emp}) \times_{\widehat{W}_0}^{\mu, n, E} (\widehat{W}, \text{emp})$, then $(\widehat{W}, \Delta_0) \times_{\widehat{W}_0}^{\mu, n, E} (\widehat{W}, \delta_0).$

The proof of Lemma 24 can be achieved by applying Lemma 75 and Lemma 76.

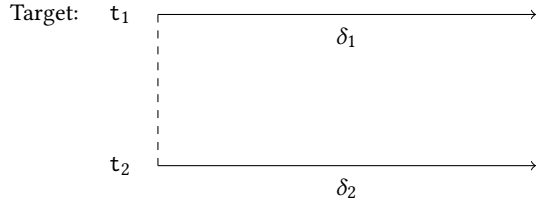


Fig. 23. Target conflicting footprint generating

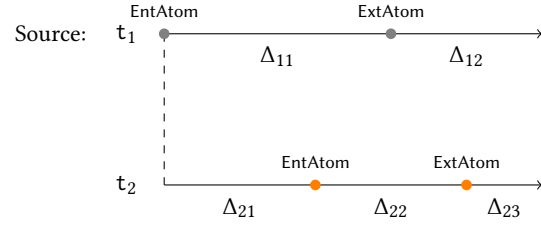


Fig. 24. Source conflicting footprint generating

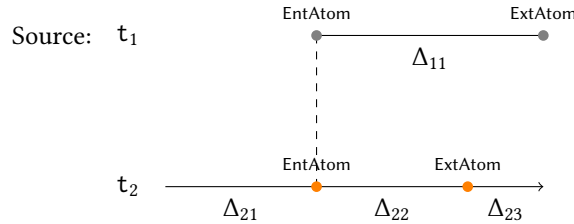
Fig. 25. Example of conflicting footprint generating

E PROOF OF DRF PRESERVING

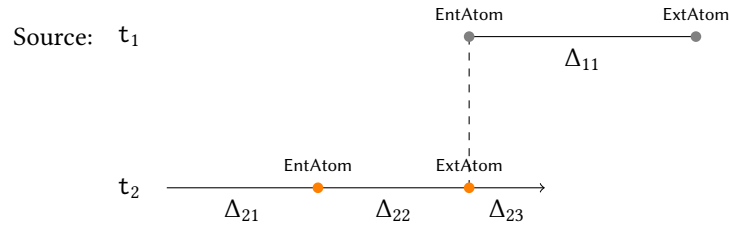
We prove the Lemma 26 in this section. The proof of DRF preserving in our work is more difficult than in CASCompCert. In CASCompCert, supposing there are two threads t_1 and t_2 generating δ_1 and δ_2 respectively in target program, we can know that there exists corresponding executions of t_1 and t_2 , generating Δ_1 and Δ_2 , in source program occurring footprint conflicting. However, the situation in our work is more complex, because of some eliminated atomic blocks during compilation in source program.

In Figure. 25, the execution of t_1 and t_2 in source program will execute some atomic blocks eliminated during compilation. Supposing there is a footprint conflicting in target program ($\delta_1 \frown \delta_2$). And we need to consider how to construct data race in source execution.

- (1) Supposing ($\Delta_{11} \frown (\Delta_{21} \cup \Delta_{22} \cup \Delta_{23})$), we discuss each case. If $\Delta_{11} \frown \Delta_{21}$, we construct a data race in source execution. Otherwise, we let the steps of t_2 generating Δ_{21} go first as shown below:

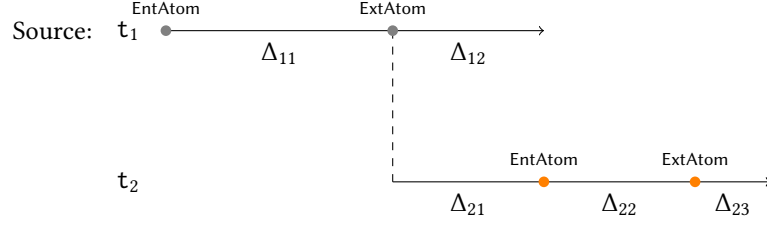


Then, we discuss whether $\Delta_{11} \frown \Delta_{22}$. We know that the execution of eliminated atomic block will only assess the thread local memory. So, Δ_{11} and Δ_{22} will never conflict. We let the steps of t_2 generating Δ_{21} go first as shown below:



We know that Δ_{11} must conflict with Δ_{23} , because ($\Delta_{11} \frown (\Delta_{21} \cup \Delta_{22} \cup \Delta_{23})$), and $\neg(\Delta_{11} \frown \Delta_{21})$ and $\neg(\Delta_{11} \frown \Delta_{22})$. We construct a data race in source execution.

- (2) If $\neg(\Delta_{11} \frown (\Delta_{21} \cup \Delta_{22} \cup \Delta_{23}))$, we let the steps of t_1 generating Δ_{11} go first as shown below:



We know $(\Delta_{12} \frown (\Delta_{12} \cup \Delta_{22} \cup \Delta_{23}))$, and discuss whether $(\Delta_{12} \frown \Delta_{21})$, or $(\Delta_{12} \frown \Delta_{22})$, or $(\Delta_{12} \frown \Delta_{23})$ in the similar way as before.

PROOF. We first do intro and unfold NPDRF, we get the following assumptions:

$$\hat{P} \leqslant_E \hat{\mathbb{P}} \quad (\text{E.1})$$

$$\text{Safe}(\hat{\mathbb{P}}) \quad (\text{E.2})$$

$$\text{AC}(\hat{\mathbb{P}}, E) \quad (\text{E.3})$$

$$\hat{P} \Longrightarrow \text{Race} \quad (\text{E.4})$$

And we need to prove the following goal:

$$\hat{\mathbb{P}} \Longrightarrow \text{Race} \quad (\text{E.5})$$

We do inversion on (E.4) and get two case. We just consider one of the case here, and the correctness proof of the other one is similar.

$$\hat{P} \xrightarrow{\text{load}} \hat{W} \quad (\text{E.6})$$

$$\hat{W} \Rightarrow^* \hat{W}' \quad (\text{E.7})$$

$$\hat{W}' \Longrightarrow \text{Race} \quad (\text{E.8})$$

By applying Lemma 79 on (E.1), (E.6) and (E.7), we get there exists $i, \mu, \hat{\mathbb{W}}, \hat{\mathbb{W}}', \Delta_0, \delta_0$, such that:

$$\hat{\mathbb{P}} \xrightarrow{\text{load}} \hat{\mathbb{W}} \quad (\text{E.9})$$

$$\hat{\mathbb{W}} \Rightarrow^* \hat{\mathbb{W}}' \quad (\text{E.10})$$

$$(\hat{W}', \delta_0) \leqslant_{\mu}^{i, E} (\hat{\mathbb{W}}', \Delta_0) \quad (\text{E.11})$$

Then, we do inversion on (E.8) and get:

$$\hat{W}' \xrightarrow[\text{emp}]{o} (T, t, \mathbb{d}, \sigma), o \neq \tau \quad (\text{E.12})$$

$$t_1 \neq t_2 \quad (\text{E.13})$$

$$(\delta_1, \mathbb{d}(t_1)) \frown (\delta_2, \mathbb{d}(t_2)) \quad (\text{E.14})$$

$$T(t_1) = (tl_1, F_1, \kappa_1), F_1 \vdash (\kappa_1, \sigma) \xrightarrow[\delta_1]{\tau}^* (\kappa'_1, \sigma'_1) \quad (\text{E.15})$$

$$T(t_2) = (tl_2, F_2, \kappa_2), F_2 \vdash (\kappa_2, \sigma) \xrightarrow[\delta_2]{\tau}^* (\kappa'_2, \sigma'_2) \quad (\text{E.16})$$

And we get the following hold:

$$(T, t_1, \mathbb{d}, \sigma) \xrightarrow[\delta_1]{\tau}^* (T\{t_1 \rightsquigarrow (tl_1, F_1, \kappa_1)\}, t_1, \mathbb{d}, \sigma'_1) \quad (\text{E.17})$$

$$(T, t_2, \mathbb{d}, \sigma) \xrightarrow[\delta_2]{\tau}^* (T\{t_2 \rightsquigarrow (tl_2, F_2, \kappa_2)\}, t_2, \mathbb{d}, \sigma'_2) \quad (\text{E.18})$$

From (E.12) (E.11), we get there exists $\widehat{W}'', \mathbb{T}, \Sigma, \Delta, j$, for any $t'' \in \text{dom}(T)$, the following hold:

$$\widehat{W}' \Rightarrow^* \widehat{W}'' \quad (\text{E.19})$$

$$\widehat{W}'' \xrightarrow[\text{emp}]{o} (\mathbb{T}, t'', \mathbb{d}, \Sigma) \quad (\text{E.20})$$

$$((T, t'', \mathbb{d}, \sigma), \text{emp}) \leq_{\mu}^{j,E} ((\mathbb{T}, t'', \mathbb{d}, \Sigma), \text{emp}) \quad (\text{E.21})$$

According to Lemma 80, (E.17), (E.18) and (E.2), we discuss each case:

- $\exists \widehat{W}'_1, \delta'_1, \widehat{W}'_1, \Delta'_1$, such that
 - $(T, t_1, \mathbb{d}, \sigma) \xrightarrow[\delta'_1]{\tau}^* \widehat{W}'_1, \delta_1 \subseteq \delta'_1$;
 - $(\mathbb{T}, t_1, \mathbb{d}, \Sigma) \xrightarrow[\Delta'_1]{\tau}^* \widehat{W}'_1, \text{FPmatch}(\mu, \Delta'_1, \delta'_1)$;
- and $\exists \widehat{W}'_2, \delta'_2, \widehat{W}'_2, \Delta'_2$, such that
 - $(T, t_2, \mathbb{d}, \sigma) \xrightarrow[\delta'_2]{\tau}^* \widehat{W}'_2, \delta_2 \subseteq \delta'_2$;
 - $(\mathbb{T}, t_2, \mathbb{d}, \Sigma) \xrightarrow[\Delta'_2]{\tau}^* \widehat{W}'_2, \text{FPmatch}(\mu, \Delta'_2, \delta'_2)$;

According to (E.15), Lemma 86, we get there exists $l \in \mu.S$:

$$(l \in \delta'_1.ws \wedge l \in \delta'_2) \vee (l \in \delta'_2.ws \wedge l \in \delta'_1)$$

According to the definition of FPmatch, we get there exists $l' \in \mu.S$:

$$(l' \in \Delta'_1.ws \wedge l' \in \Delta'_2) \vee (l' \in \Delta'_2.ws \wedge l' \in \Delta'_1)$$

And we construct a data race in source level.

- $\exists \widehat{W}'_1, \delta'_1, \widehat{W}'_1, \Delta'_1$, such that
 - $(T, t_1, \mathbb{d}, \sigma) \xrightarrow[\delta'_1]{\tau}^* \widehat{W}'_1, \delta_1 \subseteq \delta'_1$;
 - $(\mathbb{T}, t_1, \mathbb{d}, \Sigma) \xrightarrow[\Delta'_1]{\tau}^* \widehat{W}'_1, \text{FPmatch}(\mu, \Delta'_1, \delta'_1)$;
- and $\exists \widehat{W}_2, \delta'_2, \widehat{W}_2, \Delta_2, \Delta'_2$, such that
 - $(T, t_2, \mathbb{d}, \sigma) \xrightarrow[\delta'_2]{\tau}^* \widehat{W}_2, \delta_2 \subseteq \delta'_2$;
 - $(\mathbb{T}, t_2, \mathbb{d}, \Sigma) \xrightarrow[\Delta_2]{e\text{-syn}(E)+} \widehat{W}_2, \widehat{W}_2 \xrightarrow[\Delta'_2]{\tau}^* \widehat{W}'_2, \text{FPmatch}(\mu, \Delta_2 \cup \Delta'_2, \delta_2)$;

According to (E.15), Lemma 86, we get there exists $l \in \mu.S$:

$$(l \in \delta'_1.ws \wedge l \in \delta'_2) \vee (l \in \delta'_2.ws \wedge l \in \delta'_1)$$

According to the definition of FPmatch, we get there exists $l' \in \mu.S$:

$$(l' \in \Delta'_1.ws \wedge l' \in (\Delta_2 \cup \Delta'_2)) \vee (l' \in (\Delta_2 \cup \Delta'_2).ws \wedge l' \in \Delta'_1)$$

According to Lemma 83, we can prove that there is a data race in source level.

- The proof of this case is similar with the previous one.
- $\exists \widehat{W}'_1, \delta'_1, \widehat{W}'_1, \widehat{W}''_1, \Delta_1, \Delta'_1$, such that
 - $(T, t_1, dl, \sigma) \xRightarrow[\delta_1]{\tau} \widehat{W}'_1, \delta_1 \subseteq \delta'_1$;
 - $(T, t_1, dl, \Sigma) \xRightarrow[\Delta_1]{e\text{-syn}(E)} \widehat{W}'_1, \widehat{W}''_1 \xRightarrow[\Delta'_1]{\tau} \widehat{W}''_1, \text{FPmatch}(\mu, \Delta_1 \cup \Delta'_1, \delta'_1)$;
 and $\exists \widehat{W}'_2, \delta'_2, \widehat{W}'_2, \widehat{W}''_2, \Delta_2, \Delta'_2$, such that
 - $(T, t_2, dl, \sigma) \xRightarrow[\delta_2]{\tau} \widehat{W}'_2, \delta_2 \subseteq \delta'_2$;
 - $(T, t_2, dl, \Sigma) \xRightarrow[\Delta_2]{e\text{-syn}(E)} \widehat{W}'_2, \widehat{W}''_2 \xRightarrow[\Delta'_2]{\tau} \widehat{W}''_2, \text{FPmatch}(\mu, \Delta_2 \cup \Delta'_2, \delta'_2)$;

According to (E.15), Lemma 86, we get there exists $l \in \mu.S$:

$$(l \in \delta'_1.ws \wedge l \in \delta'_2) \vee (l \in \delta'_2.ws \wedge l \in \delta'_1)$$

According to the definition of FPmatch, we get there exists $l' \in \mu.S$:

$$(l' \in (\Delta_1 \cup \Delta'_1).ws \wedge l' \in (\Delta_2 \cup \Delta'_2)) \vee (l' \in (\Delta_2 \cup \Delta'_2).ws \wedge l' \in (\Delta_1 \cup \Delta'_1))$$

According to Lemma 84, we can prove that there is a data race in source level.

□

Lemma 79. For any $\hat{P}, \hat{P}, \widehat{W}, \widehat{W}'$, and E , if $\hat{P} \leq_E \hat{P}, \hat{P} \xRightarrow{load} \widehat{W}, \widehat{W} \Rightarrow^* \widehat{W}'$, then there exists $i, \mu, \widehat{W}, \widehat{W}', \Delta_0, \delta_0$, such that: $\hat{P} \xRightarrow{load} \widehat{W} \wedge \widehat{W} \Rightarrow^* \widehat{W}' \wedge (\widehat{W}', \delta_0) \leq_{\mu}^{i,E} (\widehat{W}', \Delta_0)$.

Lemma 80. If $(\widehat{W}, \delta_0) \leq_{\mu}^{i,E} (\widehat{W}, \Delta_0)$, $\text{Safe}(\widehat{W})$, $\widehat{W} \xRightarrow[\delta_1]{\tau} \widehat{W}_1$, then one of the following holds:

- (1) either, $\exists \widehat{W}', \delta, \widehat{W}', \Delta$, such that
 - $\widehat{W} \xRightarrow[\delta]{\tau} \widehat{W}', \delta_1 \subseteq \delta$;
 - $\widehat{W} \xRightarrow[\Delta]{\tau} \widehat{W}', \text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$;
- (2) or, $\exists \widehat{W}', \delta, \widehat{W}', \widehat{W}'', \Delta, \Delta'$, such that
 - $\widehat{W} \xRightarrow[\delta]{\tau} \widehat{W}', \delta_1 \subseteq \delta, \widehat{W}.dl(\widehat{W}.t) = 0$;
 - $\widehat{W} \xRightarrow[\Delta]{e\text{-syn}(E)} \widehat{W}', \widehat{W}'' \xRightarrow[\Delta']{\tau} \widehat{W}'', \text{FPmatch}(\mu, \Delta_0 \cup \Delta \cup \Delta', \delta_0 \cup \delta)$;

PROOF. We prove this lemma by induction on step n . If $n = 0$, we finish the proof by applying Lemma 81. If $n > 0$, we finish the proof by applying inductive hypothesis. □

Lemma 81. If $(\widehat{W}, \delta_0) \leq_{\mu}^{i,E} (\widehat{W}, \Delta_0)$, $\text{Safe}(\widehat{W})$, then one of the following holds:

- (1) either, $\exists \widehat{W}', \delta, \widehat{W}', \Delta$, such that
 - $\widehat{W} \xRightarrow[\delta]{\tau} \widehat{W}'$;
 - $\widehat{W} \xRightarrow[\Delta]{\tau} \widehat{W}', \text{FPmatch}(\mu, \Delta_0 \cup \Delta, \delta_0 \cup \delta)$;
- (2) or, $\exists \widehat{W}', \delta, \widehat{W}', \widehat{W}'', \Delta, \Delta'$, such that
 - $\widehat{W} \xRightarrow[\delta]{\tau} \widehat{W}', \widehat{W}.dl(\widehat{W}.t) = 0$;
 - $\widehat{W} \xRightarrow[\Delta]{e\text{-syn}(E)} \widehat{W}', \widehat{W}'' \xRightarrow[\Delta']{\tau} \widehat{W}'', \text{FPmatch}(\mu, \Delta_0 \cup \Delta \cup \Delta', \delta_0 \cup \delta)$;

PROOF. We prove this Lemma by induction on index i . (* on paper proof *) □

Lemma 82 (Constructing source conflict aux).

For any $\widehat{W}_0, o, \mathbb{T}, t, \mathbb{d}, \Sigma, t_1, \widehat{W}_1, \Delta_1, t_2, \widehat{W}_2, \Delta_2, n > 0$ and E , if

- $\widehat{W}_0 \xrightarrow[\text{emp}]{o} (\mathbb{T}, t, \mathbb{d}, \Sigma), o \neq \tau, t_1 \neq t_2;$
- $(\mathbb{T}, t_1, \mathbb{d}, \Sigma) \xrightarrow[\Delta_1]{\tau}^* \widehat{W}_1;$
- $(\mathbb{T}, t_2, \mathbb{d}, \Sigma) \xrightarrow[\Delta_2]{\text{e-syn}(E)} \widehat{W}_2;$
- $\Delta_1 \frown \Delta_2;$
- $\neg(\exists \widehat{W}. \widehat{W}_0 \Rightarrow^* \widehat{W} \wedge (\widehat{W}, E) \Longrightarrow \text{WA});$

then $\widehat{W}_0 \Longrightarrow \text{Race}$.

PROOF. (* on paper proof *) □

Lemma 83 (Constructing source conflict - I).

For any $\widehat{W}_0, o, \mathbb{T}, t, \mathbb{d}, \Sigma, t_1, \widehat{W}_1, \Delta_1, t_2, \widehat{W}_2, \widehat{W}'_2, \Delta_2, \Delta'_2, n > 0$ and E , if

- $\widehat{W}_0 \xrightarrow[\text{emp}]{o} (\mathbb{T}, t, \mathbb{d}, \Sigma), o \neq \tau, t_1 \neq t_2;$
- $(\mathbb{T}, t_1, \mathbb{d}, \Sigma) \xrightarrow[\Delta_1]{\tau}^* \widehat{W}_1;$
- $(\mathbb{T}, t_2, \mathbb{d}, \Sigma) \xrightarrow[\Delta_2]{\text{e-syn}(E)}^n \widehat{W}_2, \widehat{W}_2 \xrightarrow[\Delta'_2]{\tau}^* \widehat{W}'_2;$
- $\Delta_1 \frown (\Delta_2 \cup \Delta'_2);$
- $\neg(\exists \widehat{W}. \widehat{W}_0 \Rightarrow^* \widehat{W} \wedge (\widehat{W}, E) \Longrightarrow \text{WA});$

then $\exists \widehat{W}. \widehat{W}_0 \Rightarrow^* \widehat{W} \wedge \widehat{W} \Longrightarrow \text{Race}$.

PROOF. We prove it by induction on n and apply Lemma 82. (* on paper proof *) □

Lemma 84 (Constructing source conflict - II).

For any $\widehat{W}_0, \mathbb{T}, t, \mathbb{d}, \Sigma, t_1, \widehat{W}_1, \Delta_1, t_2, \widehat{W}_2, \widehat{W}'_2, \Delta_2, \Delta'_2, n > 0, m > 0$ and E , if

- $\widehat{W}_0 \xrightarrow[\text{emp}]{o} (\mathbb{T}, t, \mathbb{d}, \Sigma), o \neq \tau, t_1 \neq t_2;$
- $(\mathbb{T}, t_1, \mathbb{d}, \Sigma) \xrightarrow[\Delta_1]{\text{e-syn}(E)}^n \widehat{W}_1, \widehat{W}_1 \xrightarrow[\Delta'_1]{\tau}^* \widehat{W}'_1;$
- $(\mathbb{T}, t_2, \mathbb{d}, \Sigma) \xrightarrow[\Delta_2]{\text{e-syn}(E)}^m \widehat{W}_2, \widehat{W}_2 \xrightarrow[\Delta'_2]{\tau}^* \widehat{W}'_2;$
- $(\Delta_1 \cup \Delta'_1) \frown (\Delta_2 \cup \Delta'_2);$
- $\neg(\exists \widehat{W}. \widehat{W}_0 \Rightarrow^* \widehat{W} \wedge (\widehat{W}, E) \Longrightarrow \text{WA});$

then $\exists \widehat{W}. \widehat{W}_0 \Rightarrow^* \widehat{W} \wedge \widehat{W} \Longrightarrow \text{Race}$.

PROOF. We prove it by induction on n and apply Lemma 83. (* on paper proof *) □

Lemma 85 (Reachclosed step). If $(\widehat{W}, \delta_0) \leq_{\mu}^{i,E} (\widehat{W}, \Delta_0)$, $\text{Safe}(\widehat{W})$, $\widehat{W} \xrightarrow[\delta]{\tau} \widehat{W}'$, then $(\delta_0 \cup \delta) \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$.

PROOF. Proof by induction on the index i . □

Lemma 86 (Reachclosed steps). If $(\widehat{W}, \delta_0) \leq_{\mu}^{i,E} (\widehat{W}, \Delta_0)$, $\text{Safe}(\widehat{W})$, $\widehat{W} \xrightarrow[\delta]{\tau}^n \widehat{W}'$, then $\delta \subseteq (\text{curF}(\widehat{W}) \cup \mu.S)$.

PROOF. Proof by induction on the number of steps and applying Lemma 85.

□

F ANALYSIS CORRECTNESS PRESERVATION

PROOF. We first do intro and unfold AC, we get the following assumptions:

$$\hat{P} \leqslant_E \hat{\mathbb{P}} \quad (\text{F.1})$$

$$(\hat{P}, E) \Longrightarrow \text{WA} \quad (\text{F.2})$$

And we need to prove the following goal:

$$(\hat{\mathbb{P}}, E) \Longrightarrow \text{WA}$$

We do inversion on (F.2) and get:

$$\hat{P} \xrightarrow{\text{load}} \hat{W} \quad (\text{F.3})$$

$$\hat{W} \Rightarrow^* \hat{W}' \quad (\text{F.4})$$

$$(\hat{W}', E) \Longrightarrow \text{WA} \quad (\text{F.5})$$

By applying Lemma 79 on (F.1), (F.3) and (F.4), we get there exists $i, \mu, \hat{\mathbb{W}}, \hat{\mathbb{W}}', \Delta_0, \delta_0$ such that:

$$\hat{\mathbb{P}} \xrightarrow{\text{load}} \hat{\mathbb{W}} \quad (\text{F.6})$$

$$\hat{\mathbb{W}} \Rightarrow^* \hat{\mathbb{W}}' \quad (\text{F.7})$$

$$(\hat{W}', \delta_0) \leqslant_{\mu}^{i, E} (\hat{\mathbb{W}}', \Delta_0) \quad (\text{F.8})$$

Then, we do inversion on (F.5) and get:

$$(\hat{W}', E) \Longrightarrow \text{WA}_0 \vee (\hat{W}', E) \Longrightarrow \text{WA}_1 \quad (\text{F.9})$$

We destruct (F.9) and discuss each case repectively:

- By the definition of $(\hat{W}', E) \Longrightarrow \text{WA}_0$, we get the following:

$$\hat{W}' \xrightarrow[\text{emp}]{o} (T, \mathbf{t}, \mathbb{d}, \sigma), o \neq \tau \quad (\text{F.10})$$

$$\mathbf{t}_1 \neq \mathbf{t}_2 \quad (\text{F.11})$$

$$\delta_1 \frown \delta_2 \quad (\text{F.12})$$

$$(\text{id}_1, _) \in E \quad (\text{F.13})$$

$$T(\mathbf{t}_1) = (tl_1, F_1, \kappa_1), F_1 \vdash (\kappa_1, \sigma) \xrightarrow[\delta_1]{\tau}^* (\kappa'_1, \sigma'_1), \mathbb{d}(\mathbf{t}_1) = \text{id}_1 \quad (\text{F.14})$$

$$T(\mathbf{t}_2) = (tl_2, F_2, \kappa_2), F_2 \vdash (\kappa_2, \sigma) \xrightarrow[\delta_2]{\tau}^* (\kappa'_2, \sigma'_2), \mathbb{d}(\mathbf{t}_2) = d_2 \quad (\text{F.15})$$

And we get the following holds:

$$(T, \mathbf{t}_1, \mathbb{d}, \sigma) \xrightarrow[\delta_1]{\tau}^* (T\{\mathbf{t}_1 \rightsquigarrow (tl_1, F_1, \kappa_1)\}, \mathbf{t}_1, \mathbb{d}, \sigma'_1) \quad (\text{F.16})$$

$$(T, \mathbf{t}_2, \mathbb{d}, \sigma) \xrightarrow[\delta_2]{\tau}^* (T\{\mathbf{t}_2 \rightsquigarrow (tl_2, F_2, \kappa_2)\}, \mathbf{t}_2, \mathbb{d}, \sigma'_2) \quad (\text{F.17})$$

From (F.10) and (F.8), we get $\exists \widehat{W}'', \mathbb{T}, \Sigma, \Delta, j$, for any $t'' \in \text{dom}(T)$, the following hold:

$$\widehat{W}' \Rightarrow^* \widehat{W}'' \quad (\text{F.18})$$

$$\widehat{W}'' \xrightarrow[\text{emp}]{o} (\mathbb{T}, t'', \text{dl}, \Sigma) \quad (\text{F.19})$$

$$((T, t'', \text{dl}, \sigma), \text{emp}) \leq_{\mu}^{j, E} ((\mathbb{T}, t'', \text{dl}, \Sigma), \text{emp}) \quad (\text{F.20})$$

According to Lemma 80, (F.16), (F.17), we discuss each case respectively:

– $\exists \widehat{W}'_1, \delta'_1, \widehat{W}'_1, \Delta'_1$, such that

$$* (T, t_1, \text{dl}, \sigma) \xrightarrow[\delta'_1]{\tau}^* \widehat{W}'_1, \delta_1 \subseteq \delta'_1;$$

$$* (\mathbb{T}, t_2, \text{dl}, \Sigma) \xrightarrow[\Delta'_1]{\tau}^* \widehat{W}'_1, \text{FPmatch}(\mu, \Delta'_1, \delta'_1);$$

and $\widehat{W}'_2, \delta'_2, \widehat{W}'_2, \Delta'_2$, such that

$$* (T, t_2, \text{dl}, \sigma) \xrightarrow[\delta'_2]{\tau}^* \widehat{W}'_2, \delta_2 \subseteq \delta'_2;$$

$$* (\mathbb{T}, t_2, \text{dl}, \Sigma) \xrightarrow[\Delta'_2]{\tau}^* \widehat{W}'_2, \text{FPmatch}(\mu, \Delta'_2, \delta'_2);$$

According to Lemma 86, we get there exists $l \in \mu.S$:

$$(l \in \delta'_1.ws \wedge l \in \delta'_2) \vee (l \in \delta'_2.ws \wedge l \in \delta'_1)$$

According to the definition of FPmatch , we get there exists $l' \in \mu.S$:

$$(l' \in \Delta'_1.ws \wedge l' \in \Delta'_2) \vee (l' \in \Delta'_2.ws \wedge l' \in \Delta'_1)$$

Then, we get: $(\widehat{W}'', E) \Longrightarrow \text{WA}_0$, and we finish the proof of this case.

– $\exists \widehat{W}'_1, \delta'_1, \widehat{W}'_1, \Delta'_1$, such that

$$* (T, t_1, \text{dl}, \sigma) \xrightarrow[\delta'_1]{\tau}^* \widehat{W}'_1, \delta_1 \subseteq \delta'_1;$$

$$* (\mathbb{T}, t_1, \text{dl}, \Sigma) \xrightarrow[\Delta'_1]{\tau}^* \widehat{W}'_1, \text{FPmatch}(\mu, \Delta'_1, \delta'_1);$$

and $\exists \widehat{W}'_2, \delta'_2, \widehat{W}'_2, \Delta'_2$, such that

$$* (T, t_2, \text{dl}, \sigma) \xrightarrow[\delta'_2]{\tau}^* \widehat{W}'_2, \delta_2 \subseteq \delta'_2, \text{dl}(t_2) = 0;$$

$$* (\mathbb{T}, t_2, \text{dl}, \Sigma) \xrightarrow[\Delta_2]{e\text{-syn}(E)}^+ \widehat{W}_2, \widehat{W}_2 \xrightarrow[\Delta'_2]{\tau}^* \widehat{W}'_2, \text{FPmatch}(\mu, \Delta_2 \cup \Delta'_2, \delta_2);$$

According to Lemma 86, we get there exists $l \in \mu.S$:

$$(l \in \delta'_1.ws \wedge l \in \delta'_2) \vee (l \in \delta'_2.ws \wedge l \in \delta'_1)$$

According to the definition of FPmatch , we get there exists $l' \in \mu.S$:

$$(l' \in \Delta'_1.ws \wedge l' \in (\Delta_2 \cup \Delta'_2)) \vee (l' \in (\Delta_2 \cup \Delta'_2).ws \wedge l' \in \Delta'_1)$$

Then, we finish the proof of this case by applying Lemma 87.

– $\exists \widehat{W}'_1, \delta'_1, \widehat{W}_1, \Delta_1, \Delta'_1$, such that

$$* (T, t_1, \text{dl}, \sigma) \xrightarrow[\delta'_1]{\tau}^* \widehat{W}'_1, \delta_1 \subseteq \delta'_1, \text{dl}(t_1) = 0;$$

$$* (\mathbb{T}, t_1, \text{dl}, \Sigma) \xrightarrow[\Delta_1]{e\text{-syn}(E)}^+ \widehat{W}_1, \widehat{W}_1 \xrightarrow[\Delta'_1]{\tau}^* \widehat{W}'_1, \text{FPmatch}(\mu, \Delta_1 \cup \Delta'_1, \delta'_1);$$

and this is contradicted with $\text{dl}(t) = \text{id}_1$, according to (F.14).

- The proof of this case is similar with the previous one.
- By the definition of $(\widehat{W}', E) \vdash \text{WA}_1$, we get the following:

$$\widehat{W}' \Rightarrow^* \widehat{W}_1^t \quad (\text{F.21})$$

$$\widehat{W}_1^t \xrightarrow[\text{emp}]{\text{id}' \cdot \text{ExtA}} (T, t', \text{dl}, \sigma) \quad (\text{F.22})$$

$$\widehat{W}_1 \cdot \text{dl}(t) = \text{id} \quad (\text{F.23})$$

$$((\text{id}, \text{id}_1) \in E \wedge \text{id}_1 \neq \text{id}') \vee ((\text{id}_2, \text{id}') \in E \wedge \text{id}_2 \neq \text{id}) \quad (\text{F.24})$$

We finish the proof by applying Lemma 88.

□

Lemma 87 (Constructing wrong analysis - 0).

For any $\widehat{W}_0, o, T, t, \text{dl}, \Sigma, t_1, \widehat{W}_1, \Delta_1, t_2, \widehat{W}_2, \widehat{W}'_2, \Delta_2, \Delta'_2, n, \text{id}$ and E , if

- $\widehat{W}_0 \xrightarrow[\text{emp}]{o} (T, t, \text{dl}, \Sigma), o \neq \tau, t_1 \neq t_2;$
- $(T, t_1, \text{dl}, \Sigma) \xrightarrow[\Delta_1]{\tau}^* \widehat{W}_1;$
- $(T, t_2, \text{dl}, \Sigma) \xrightarrow[\Delta_2]{\text{e-syn}(E)}^n \widehat{W}_2, \widehat{W}_2 \xrightarrow[\Delta'_2]{\tau}^* \widehat{W}'_2;$
- $\Delta_1 \frown (\Delta_2 \cup \Delta'_2);$
- $\text{dl}(t_1) = \text{id}, (\text{id}, _) \in E;$

then $\exists \widehat{W}. \widehat{W}_0 \Rightarrow^* \widehat{W} \wedge (\widehat{W}, E) \vdash \text{WA}_0$.

PROOF. Prove by induction on n . (* on paper proof *)

□

Lemma 88 (Constructing wrong analysis - 1).

For any $\widehat{W}, \widehat{W}_1, t, t', \text{id}, \text{id}', \text{dl}, \sigma, \widehat{W}, \delta_0, \Delta_0, n, i, E$ and μ , if

- $\widehat{W} \Rightarrow^n \widehat{W}_1^t, \widehat{W}_1^t \xrightarrow[\text{emp}]{\text{id}' \cdot \text{ExtA}} (T, t, \text{dl}, \sigma);$
- $\widehat{W}_1 \cdot \text{dl}(t) = \text{id};$
- $((\text{id}, \text{id}_1) \in E \wedge \text{id}_1 \neq \text{id}') \vee ((\text{id}_2, \text{id}') \in E \wedge \text{id}_2 \neq \text{id});$
- $(\widehat{W}, \delta_0) \leq_{\mu}^{i, E} (\widehat{W}, \Delta_0);$

then there exists \widehat{W}_1, T and Σ , such that:

- $\widehat{W} \Rightarrow^* \widehat{W}_1^t, \widehat{W}_1^t \xrightarrow[\text{emp}]{\text{id}' \cdot \text{ExtA}} (T, t, \text{dl}, \Sigma);$
- $\widehat{W}_1 \cdot \text{dl}(t) = \text{id};$
- $((\text{id}, \text{id}_1) \in E \wedge \text{id}_1 \neq \text{id}') \vee ((\text{id}_2, \text{id}') \in E \wedge \text{id}_2 \neq \text{id}).$

PROOF. Prove by induction on n .

□

