

The Name of the Title is Hope

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In this document, we show how to define transformation rules to prove the correctness of the compiler optimizations.

1 LANGUAGES

The syntax of the language.

$$\begin{aligned}
 (\text{Addr}) \quad x &\in \dots & (\text{Reg}) \quad r &\in \dots & (\text{Lab}) \quad l, f &\in \dots \\
 (\text{Expre}) \quad e &::= r \mid v \mid e + e \mid e - e \mid e * e \\
 (\text{Instr}) \quad i &::= \langle r := e \rangle_l \mid \langle r := [e] \rangle_l \mid \langle [e_1] := e_2 \rangle_l \mid \langle \text{skip} \rangle_l \mid \langle \text{print } r \rangle_l \\
 &\quad \mid \langle \text{jmp} \rangle_l \mid \langle \text{be } e \rangle_{l_1, l_2} \\
 (\text{Code}) \quad C &::= \{l \rightsquigarrow i\}^* \quad \text{where } l \in \text{Lab} \\
 (\text{GE}) \quad ge &::= \{x \rightsquigarrow v\}^* \\
 (\text{Init}) \quad \text{Init} &\in GE \rightarrow \text{Mem} \times \text{Reg}
 \end{aligned}$$

The program state.

$$\begin{aligned}
 (\text{Mem}) \quad M &::= \{x \rightsquigarrow v\}^* \quad \text{where } x \in \text{Addr} \\
 (\text{RegFile}) \quad R &::= \{r \rightsquigarrow v\}^* \quad \text{where } r \in \text{Reg} \\
 (\text{State}) \quad S &::= (M, R)
 \end{aligned}$$

The semantics of the language.

$$\begin{aligned}
 &\frac{C(\text{pc}) = i \quad i \vdash ((M, R), \text{pc}) \rightarrow ((M', R'), \text{pc}')}{C \vdash ((M, R), \text{pc}) \rightarrow ((M', R'), \text{pc}')} \\
 &\frac{\llbracket e \rrbracket_R = v}{\langle r := e \rangle_l \vdash ((M, R), \text{pc}) \rightarrow ((M, R\{r \rightsquigarrow v\}), l)} \quad \frac{\llbracket e \rrbracket_R = x \quad M(x) = v}{\langle r := [e] \rangle_l \vdash ((M, R), \text{pc}) \rightarrow ((M, R\{r \rightsquigarrow v\}), l)} \\
 &\frac{\llbracket e_1 \rrbracket_R = x \quad \llbracket e_2 \rrbracket_R = v}{\langle [e_1] := e_2 \rangle_l \vdash ((M, R), \text{pc}) \rightarrow ((M\{x \rightsquigarrow v\}, R), l)} \quad \frac{}{\langle \text{skip} \rangle_l \vdash ((M, R), \text{pc}) \rightarrow ((M, R), l)} \\
 &\frac{\llbracket e \rrbracket_R = v \quad (l = l_1 \wedge v = 0) \vee (l = l_2 \wedge v = 1)}{\langle \text{be } e \rangle_{l_1, l_2} \vdash ((M, R), \text{pc}) \rightarrow ((M, R), l)} \quad \frac{R(r) = v}{\langle \text{print } r \rangle_l \vdash ((M, R), \text{pc}) \rightarrow ((M, R), l)}
 \end{aligned}$$

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2 FINAL THEOREM

We show the final theorem in this section. Our goal is to prove the correctness of the compiler optimizations. A compiler optimization includes an analyzer and a translator.

$$\begin{aligned} (\text{Analyzer}) \quad \text{Analyzer} &\in \text{Code} \times \text{GE} \times \text{Lab} \rightarrow \text{AnalyRes} \\ (\text{Translator}) \quad \text{Translator} &\in \text{Code} \times \text{AnalyRes} \rightarrow \text{Code} \end{aligned}$$

The result of the analyses *AnalyRes* will be defined in the following.

A compiler optimization is correct, if the target code generated refines the source code. We define the refinement relation below.

Definition 2.1 (Refinement).

$$\begin{aligned} C' \subseteq_{ge,f} C \quad ::= \quad &\forall M, R. (\text{Init}(ge) = (M, R) \wedge \text{Safe}(C, (M, R), f)) \\ &\implies (C', (M, R), f) \subseteq (C, (M, R), f) \end{aligned}$$

$$\text{where } \text{Safe}(C, (M, R), f) ::= \neg(C \vdash (M, R, f) \rightarrow^* \mathbf{abort}).$$

We give the final theorem below.

THEOREM 2.2 (FINAL THEOREM). For any C, C', f, ge and A , if $\text{Analyzer}(C, ge, f) = A$ and $\text{Translator}(C, A) = C'$, then $C' \subseteq_{ge,f} C$.

The correctness proof of the Theorem. 2.2 can be divided into the proof of the two lemmas shown below.

LEMMA 2.3 (OPTIMIZATION CORRECTNESS). For any C, C', f, ge and A , if $\text{Analyzer}(C, ge, f) = A$ and $\text{Translator}(C, A) = C'$, then there exists I such that $I \vdash_{ge,f} (C, A) \sim C'$.

LEMMA 2.4 (SOUNDNESS OF TRANSFORMATION RULE).

$$I \vdash_{ge,f} (C, A) \sim C' \implies C' \subseteq_{ge,f} C$$

In Lemma. 2.3, we prove the correctness of the optimization by proving whether the source code, the target code and the analyses result can pass the checking of the transformation rules.

The transformation rule in the form of " $I \vdash_{ge,f} (C, A) \sim C'$ " will be defined in Sec. 5.

The Lemma. 2.4 shows that the soundness of the transformation rule can ensure that the target code refines the source code.

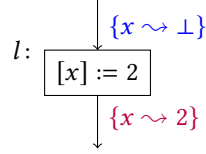
3 THE DEFINITION OF THE ANALYSIS RESULT

We define the result of the analysis (*AnalyRes*) mentioned previously in this section. In this work, we focus on the dataflow analysis mainly.

$$\begin{aligned} (AI) \quad L &::= \dots \\ (AIM) \quad \mathbb{L} &::= \{l_1 \rightsquigarrow L_1, \dots, l_2 \rightsquigarrow L_n\} \\ (\text{AnalyRes}) \quad A &::= (\mathbb{L}_i, \mathbb{L}_o) \end{aligned}$$

The result of the dataflow analysis gives each node an abstract interpretation. We show the abstract interpretation in the value analysis, the liveness analysis and the common subexpression elimination below.

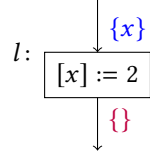
Value Analysis :



$$(AVal) \quad \hat{v} \in Val \cup \{\perp, \top\}$$

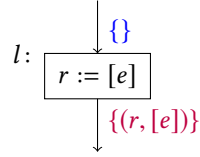
$$a_v \in \mathcal{P}((Addr \rightarrow AVal) \cup (Reg \rightarrow AVal))$$

Liveness Analysis :



$$a_l \in \mathcal{P}(Addr \cup Reg)$$

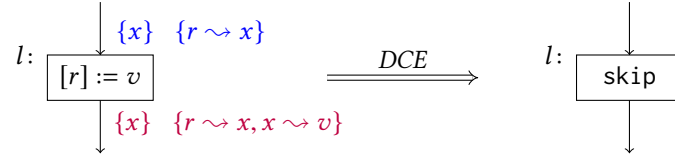
Available Expression Analysis :



$$(VExp) \quad ve ::= (r, e) \mid (r, [e])$$

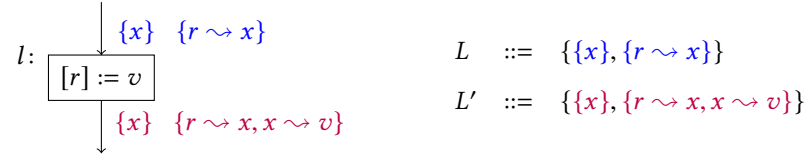
$$a_e \in \mathcal{P}(VExp)$$

An analyzer in a compiler optimization may do more than one analysis. For example, the *dead code elimination* optimization in CompCert does both the value analysis and the liveness analysis as shown below.



The instruction " $[r] := v$ " is a dead code, since " $\{r \rightsquigarrow x\}$ " says that the register r points to the memory location x , and " $\{x : \circ\}$ " says that the memory location x will not be read before the next write.

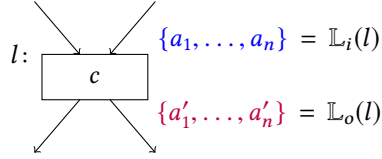
Thus, we define the set of abstract interpretation L ($AISet$), as the collection of abstract interpretations of each node. We give an example of the specific L below.



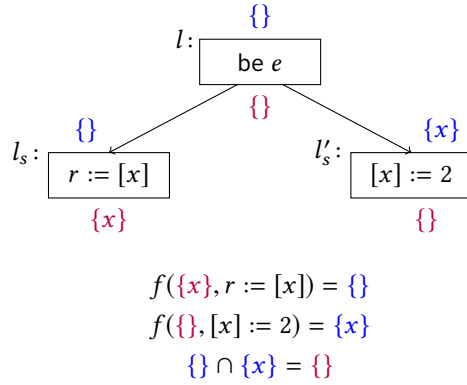
Then, we define the mapping of the set of abstract interpretations \mathbb{L} ($AISetM$), which is a partial mapping from the label to the set of the abstract interpretations.

Finally, we define the result of the analysis below, which is a pair of $AISetM$. Here, the \mathbb{L}_i maps the label of each node to the set of abstract interpretations, which describe the program state before the execution of the instruction of the node, and the \mathbb{L}_o maps the label of each node to the set of abstract interpretations, which

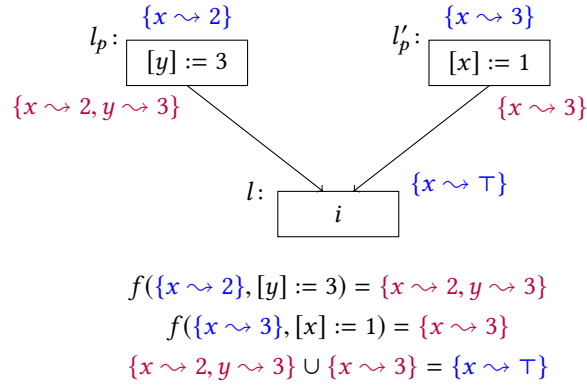
describe the program state after the execution of the instruction of the node. We show the meaning of \mathbb{L}_i and \mathbb{L}_o below.



The abstract interpretation, which describes the program state after the execution of the node labelled l , may be different from the abstract interpretation, which describes the program state before the execution of the node l 's successor node l_s , as the example shown below.



Similarly, The abstract interpretation, which describes the program state before the execution of the node labelled l , may be different from the abstract interpretation, which describes the program state after the execution of the node l 's predecessor node l_p , as the example shown below.



4 THE ASSERTION LANGUAGE

As we have introduced, we want to define a set of transformation rules to check the correctness of the compiler optimization. The transformation rule checks whether the transformation is legal according to the result of the analysis. Thus, the rules that check the correctness of transforming an individual instruction from the source

code to the corresponding instruction in the target code may be in the following form.

$$\frac{\text{some side conditions} \dots}{\vdash \{L\} i \{L'\} \sim i'}$$
$$A = (\mathbb{L}_i, \mathbb{L}_o)$$

$$\mathbb{L}_i(l) = L \quad \mathbb{L}_o(l) = L'$$

We use a code transformation in constant propagation as an example.

$$\frac{L = \{\{x \rightsquigarrow 2\}\}}{\vdash \{L\} \langle r := [x] \rangle_{l_1} \{L'\} \sim \langle r := 2 \rangle_{l_1}}$$
$$A = (\mathbb{L}_i, \mathbb{L}_o)$$

$$\mathbb{L}_i(l) = L = \{\{x \rightsquigarrow 2\}\} \quad \mathbb{L}_o(l) = L = \{\{x \rightsquigarrow 2, r \rightsquigarrow 2\}\}$$

However, the transformation rule in such form is not general, since different analyses will produce different forms of analysis results.

So, we define a general assertion language to replace the specific analysis result to describe the program state. The basic idea is show below. I is a converter that converts a specific form of the abstract interpretation to the general assertion.

$$\frac{I(L) \Rightarrow (x \mapsto 2)}{\vdash \{I(L)\} \langle r := [x] \rangle_{l_1} \{I(L')\} \sim \langle r := 2 \rangle_{l_1}}$$
$$A = (\mathbb{L}_i, \mathbb{L}_o)$$

$$\mathbb{L}_i(l) = L = \{\{x \rightsquigarrow 2\}\} \quad \mathbb{L}_o(l) = L = \{\{x \rightsquigarrow 2, r \rightsquigarrow 2\}\}$$

$$I(L) = (x \mapsto 2) \quad I(L') = (x \mapsto 2) \wedge (r = 2)$$

We define the syntax of the assertion language in Fig. 1.

We define the semantics of the assertion language in Fig. 2. Here, the is is the set of identifiers, which include registers and memory locations.

$$(IdSet) \quad is \in \mathcal{P}(Addr \cup Reg)$$

$$\begin{aligned}
 (\text{Asrt}) \quad p ::= & \quad e \mapsto e' \mid e = e' \mid \text{liveE}(e) \mid \text{liveX}(e) \mid p[e/r] \\
 & \mid p_1 * p_2 \mid p_1 \wedge p_2 \mid p_1 \vee p_2 \mid p_1 \multimap p_2 \mid \exists v.p \mid \forall v.p
 \end{aligned}$$

Fig. 1. The syntax of the assertion language

$$\begin{aligned}
 (M, R, is) \models e \mapsto e' & ::= \exists x, v. \llbracket e \rrbracket_R = x \wedge \llbracket e' \rrbracket_R = v \wedge M(x) = v \\
 (M, R, is) \models e = e' & ::= \llbracket e \rrbracket_R = \llbracket e' \rrbracket_R \\
 (M, R, is) \models \text{liveE}(e) & ::= \text{fv}(e) \cap is = \emptyset \\
 (M, R, is) \models \text{liveX}(e) & ::= \exists x. \llbracket e \rrbracket_R = x \wedge x \notin is \\
 (M, R, is) \models p[e/r] & ::= \exists v. \llbracket e \rrbracket_R = v \wedge (M, R\{r \leadsto v\}, is) \models p \\
 (M, R, is) \models p_1 * p_2 & ::= \exists M_1, M_2. \text{dom}(M_1) \cap \text{dom}(M_2) = \emptyset \wedge M = M_1 \cap M_2 \\
 & \quad \wedge (M_1, R, is) \models p_1 \wedge (M_2, R, is) \models p_2 \\
 (M, R, is) \models p_1 \wedge p_2 & ::= (M, R, is) \models p_1 \wedge (M, R, is) \models p_2 \\
 (M, R, is) \models p_1 \vee p_2 & ::= (M, R, is) \models p_1 \vee (M, R, is) \models p_2 \\
 (M, R, is) \models p_1 \multimap p_2 & ::= \forall M'. ((M', R, is) \models p_1 \wedge \text{dom}(M') \cap \text{dom}(M) = \emptyset) \\
 & \quad \implies (M \cup M', R, is) \models p_2 \\
 (M, R, is) \models \exists v.p & ::= \exists v. (M, R, is) \models p \\
 (M, R, is) \models \forall v.p & ::= \forall v. (M, R, is) \models p
 \end{aligned}$$

Fig. 2. The semantics of the assertion language

We use the " $\text{fv}(e)$ " to collect the registers used in the expression e .

$$\text{fv}(e) ::= \begin{cases} \{r\} & \text{if } e = r \\ \emptyset & \text{if } e = n \\ \text{fv}(e_1) \cup \text{fv}(e_2) & \text{if } e = e_1 + e_2 \\ \text{fv}(e_1) \cup \text{fv}(e_2) & \text{if } e = e_1 - e_2 \end{cases}$$

We define an invariant I to describe the relation between the specific analysis and the assertion.

$$(AInv) \quad I \in AI \rightarrow Asrt$$

The invariant I ($AInv$) will be instantiated when meeting different analysis results. Below, we give some examples.

Auxiliary definition for value analysis:

$$I'_v(a_v) ::= (\forall r \in \text{dom}(a_v). (\exists v. r = v \wedge a_v(r) = v) \vee (r = - \wedge a_v(r) \in \{\perp, \top\})) \\ \wedge (\forall x \in \text{dom}(a_v). (\exists v. x \mapsto v \wedge a_v(x) = v) \vee (x \mapsto - \wedge a_v(x) \in \{\perp, \top\}))$$

Constant propagation (Value analysis a_v):

$$I_c(a_v) ::= I'_v(a_v) \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x))$$

Dead code elimination (Value analysis a_v + Liveness analysis a_l):

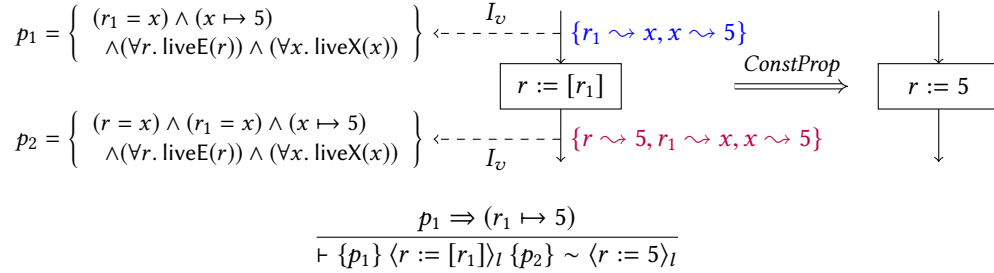
$$I_d(\{a_v, a_l\}) ::= I'_v(\{a_v\}) \\ \wedge (\forall r \notin a_l. \text{liveE}(r)) \wedge (\forall r \in a_l. \neg \text{liveE}(r)) \\ \wedge (\forall x \notin a_l. \text{liveX}(x) \wedge (\forall x \in a_l. \neg \text{liveX}(x)))$$

Common subexpression elimination (Value analysis a_v + Available expression analysis a_e):

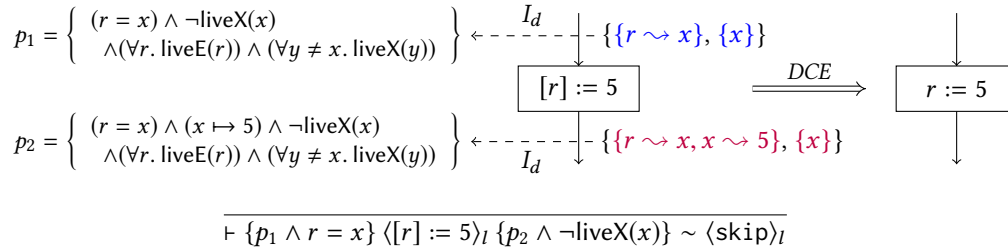
$$I_e(\{a_v, a_e\}) ::= I'_v(\{a_v\}) \\ \wedge (\forall (r, [e]) \in a_e. e \mapsto r) \wedge (\forall (r, e) \in a_e. e = r) \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x))$$

We use some digrams to illustrate the use of the invariant I ($AInv$) and the assertion p ($Asrt$) in defining the transformation rules.

Constant propagation.



Dead code elimination.



$$\begin{aligned}
\text{succ}(c) &::= \begin{cases} \{l\} & \text{if } c \in \{\langle r := e \rangle_l, \langle r := [e] \rangle_l, \langle [e_1] := e_2 \rangle_l, \\ & \langle \text{jmp} \rangle_l, \langle \text{skip} \rangle_l\} \\ \{l_1, l_2\} & \text{if } c = \langle \text{be } e \rangle_{l_1, l_2} \end{cases} \\
\text{pred}(C, l) &::= \{l_p \mid l \in \text{succ}(C(l_p))\} \\
\text{liveE}(e_1, \dots, e_n) &::= \text{liveE}(e_1) \wedge \dots \wedge \text{liveE}(e_n) \\
p \Rightarrow p' &::= \forall M, R, is. (M, R, is) \models p \implies (M, R, is) \models p' \\
p \Rrightarrow p' &::= \forall M, R, is. (M, R, is) \models p \implies \exists is'. is \subseteq is' \wedge (M, R, is') \models p' \\
p_1 \oplus p_2 &::= (p_1 \wedge p_2) \vee (p_1 \multimap p_2) \\
\text{ALStaE}(p, e) &::= \forall M, R, is. (M, R, is) \models p \implies (M, R, is \setminus \text{fv}(e)) \models p \\
\text{ALStaX}(p, e) &::= \forall M, R, is, x. ((M, R, is) \models p \wedge \llbracket e \rrbracket_R = x) \implies (M, R, is \setminus x) \models p
\end{aligned}$$

Fig. 3. Some auxiliary definitions

Common subexpression elimination.

$$\begin{array}{ccc}
p_1 = \left\{ \begin{array}{l} (r = 5) \wedge (r_1 \mapsto r_2) \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} & \xleftarrow{I_e} \downarrow \{\{r \rightsquigarrow 5\}, \{(r_2, [r_1])\}\} & \\
& \boxed{r := [r_1]} & \xRightarrow{\text{CSE}} \boxed{r := r_2} \\
p_2 = \left\{ \begin{array}{l} (r = -) \wedge (r_1 \mapsto r_2) \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} & \xleftarrow{I_e} \downarrow \{\{r \rightsquigarrow \perp\}, \{(r_2, [r_1])\}\} & \\
& & \downarrow
\end{array}$$

$$\frac{p_1 \Rightarrow (r_1 \mapsto r_2 \wedge \text{liveE}(r_2))}{\vdash \{p_1\} \langle r := [r_1] \rangle_l \{p_2\} \sim \langle r := r_2 \rangle}$$

5 TRANSFORMATION RULES

We define the transformation rules in Fig. 4. Some auxiliary definitions are defined in Fig. 3. We introduce each rule in details below.

Well-formed Optimization. The Wf-Opt rule is the top rule. It divides the correctness proof of the optimization into two parts: the *Well-formed Analysis*, which checks the correctness of the results of the analysis, and the *Well-formed Translater*, which checks the correctness of the code transformation if the relied analysis is correct.

Well-formed Analysis. The wf-Analysis is the top rule of *Well-formed Analysis* check. The result of the analysis A includes two parts as we have introduced: \mathbb{L}_i records the set of abstract interpretations describing the states before the execution of each node, and \mathbb{L}_o records the set of abstract interpretations describing the state after the execution of each node. *First*, the abstract interpretations for describing the initial states should be correct (" $\exists is. (\text{Init}(ge), is) \models I(\mathbb{L}_i(f))$ "); *Second*, we check that the abstract interpretations of each node is correct. We use

Well-formed Optimization:

$$\frac{I, C \vdash_{ge, f} A \quad I, A \vdash C \sim C'}{I \vdash_{ge, f} (C, A) \sim C'} \quad (\text{wf-Opt})$$

Well-formed Analysis:

$$\begin{array}{c} A = (\mathbb{L}_i, \mathbb{L}_o) \quad \exists is. (\text{Init}(ge), is) \models I(\mathbb{L}_i(f)) \\ \text{for all } l \in \text{dom}(C) : \\ \quad I(\mathbb{L}_o(l)) \Rightarrow I(\mathbb{L}_i(l_s)), \text{ for all } l_s \in \text{succ}(C(l)) \\ \quad \vdash \{I(\mathbb{L}_i(l))\} C(l) \{I(\mathbb{L}_o(l))\} \\ \hline I, C \vdash_{ge, f} A \end{array} \quad (\text{wf-Analysis})$$

$$\frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow p'}{\vdash \{p[e/r] \wedge \text{liveE}(e)\} \langle r := e \rangle_l \{p'\}} \quad (\text{Asgn-Ac})$$

$$\frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow p'}{\vdash \{((e \mapsto e') \oplus p[e'/r]) \wedge \text{liveE}(e) \wedge \text{liveX}(e)\} \langle r := [e] \rangle_l \{p'\}} \quad (\text{Ld-Ac})$$

$$\frac{\text{ALStaX}((e_1 \mapsto - * p), e_1) \quad (e_1 \mapsto e_2 * p) \wedge \text{liveX}(e_1) \Rightarrow p'}{\vdash \{((e_1 \mapsto -) \oplus (e_1 \mapsto - * p)) \wedge \text{liveE}(e_1, e_2)\} \langle [e_1] := e_2 \rangle_l \{p'\}} \quad (\text{St-Ac})$$

$$\frac{p_1 \Rightarrow p'_1 \quad \vdash \{p'_1\} i \{p'_2\} \quad p'_2 \Rightarrow p_2}{\vdash \{p_1\} i \{p_2\}} \quad (\text{Consq-Ac})$$

Well-formed Translator:

$$\frac{I, A \vdash C_1 \sim C'_1 \quad I, A \vdash C_2 \sim C'_2}{I, A \vdash (C_1 \uplus C_2) \sim (C'_1 \uplus C'_2)} \quad (\text{Link}) \quad \frac{A = (\mathbb{L}_i, \mathbb{L}_o) \quad I(\mathbb{L}_i(l)), I(\mathbb{L}_o(l)) \vdash i \sim i'}{I, A \vdash \{l \rightsquigarrow i\} \sim \{l \rightsquigarrow i'\}} \quad (\text{Ins-Trans})$$

$$\frac{p \Rightarrow (e = e') \wedge \text{liveE}(e')}{p, p' \vdash \langle r := e \rangle_l \sim \langle r := e' \rangle_l} \quad (\text{Asgn}) \quad \frac{p' \Rightarrow \neg \text{liveE}(r)}{p, p' \vdash \langle r := e \rangle_l \sim \langle \text{skip} \rangle_l} \quad (\text{Asgn-Elim})$$

$$\frac{p \Rightarrow (e = e') \wedge \text{liveE}(e') \wedge \text{liveX}(e')}{p, p' \vdash \langle r := [e] \rangle_l \sim \langle r := [e'] \rangle_l} \quad (\text{Ld}) \quad \frac{p' \Rightarrow \neg \text{liveE}(r)}{p, p' \vdash \langle r := [e] \rangle_l \sim \langle \text{skip} \rangle_l} \quad (\text{Ld-Elim})$$

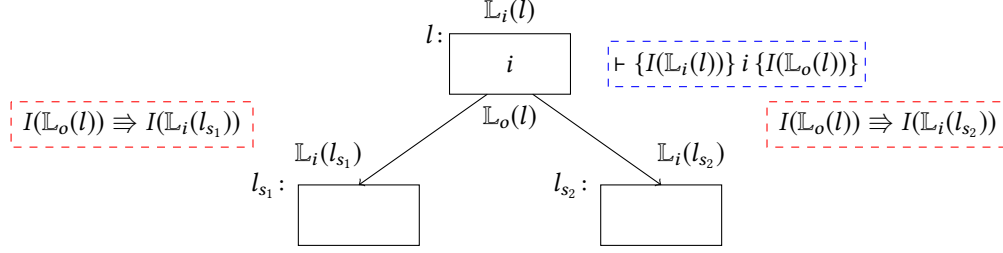
$$\frac{p \Rightarrow (e \mapsto e') \wedge \text{liveE}(e')}{p, p' \vdash \langle r := [e] \rangle_l \sim \langle r := e' \rangle_l} \quad (\text{Ld-Elim2})$$

$$\frac{p \Rightarrow (e_1 = e'_1) \wedge (e_2 = e'_2) \wedge \text{liveE}(e'_1, e'_2)}{p, p' \vdash \langle [e_1] := e_2 \rangle_l \sim \langle [e'_1] := e'_2 \rangle_{l'}} \quad (\text{St}) \quad \frac{p' \Rightarrow \neg \text{liveX}(e_1)}{p, p' \vdash \langle [e_1] := e_2 \rangle_l \sim \langle \text{skip} \rangle_l} \quad (\text{St-Elim})$$

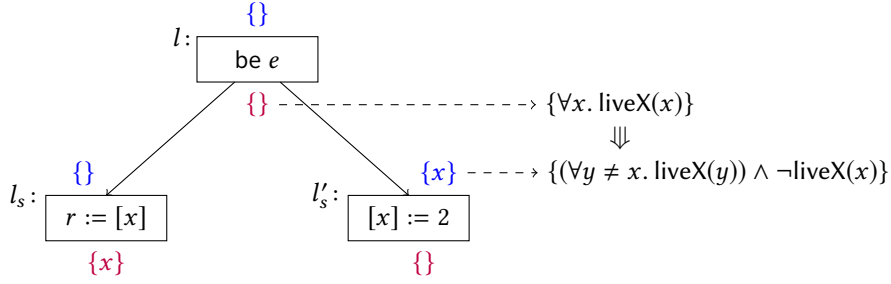
$$\frac{p \Rightarrow \text{liveE}(r)}{p, p' \vdash \langle \text{print } r \rangle_l \sim \langle \text{print } r \rangle_l} \quad (\text{Print})$$

Fig. 4. Transformation rules

the following digram to show the correctness checking of each node.



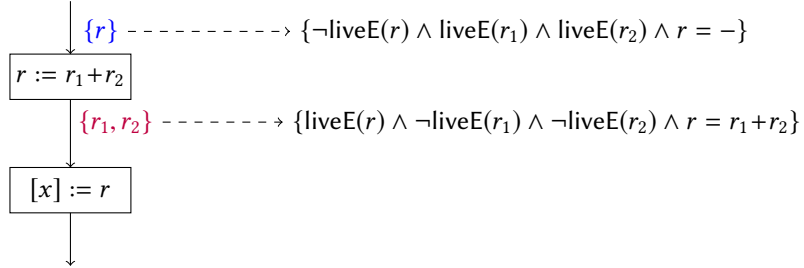
As for the implication of the assertion, we define two types of the implication in Fig. 3 shown as " $p \Rightarrow p'$ " and " $p' \Rightarrow p'$ " respectively. The example shown below explains why we define " $p' \Rightarrow p'$ "



By the meaning of the " $p \Rightarrow p'$ ", we have

$$\text{liveX}(e) \Rightarrow \neg \text{liveX}(e) \quad \text{liveE}(e) \Rightarrow \neg \text{liveE}(e)$$

The Asgn-Ac rule checks whether the analysis results for the assignment instruction is correct. ALStaE(p, r) says that the assertion p is stable if we remove the register r from the is , which records the identifiers that are not live. It is because after $r := e$ is executed, the register r may become live after being written as shown below.



Let $p = \{\text{liveE}(r_1) \wedge \text{liveE}(r_2) \wedge r = r_1 + r_2\}$, and $p' = \{\neg \text{liveE}(r) \wedge \text{liveE}(r_1) \wedge \text{liveE}(r_2) \wedge r = r_1 + r_2\}$

We can prove that $\text{ALStaE}(p, r)$ and $(p \wedge \text{liveE}(r)) \Rightarrow p'$

Thus, we have $\vdash \{p[(r_1 + r_2)/r] \wedge \text{liveE}(r_1 + r_2)\} \langle r := r_1 + r_2 \rangle \{p'\}$

For the example shown above, the registers r_1 and r_2 may become not live after the execution of " $r := r_1 + r_2$ ", since they will not be read before the next updating in the following execution. We give an example below to

show the use of the Asgn-Ac rule.

$$\begin{array}{c}
 \left\{ \begin{array}{l} r_1 = 1 \wedge r_2 = 2 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xrightarrow{I_c} \left\{ \begin{array}{l} r_1 \rightsquigarrow 1, r_2 \rightsquigarrow 2 \end{array} \right\} \\
 \left\{ \begin{array}{l} r = 3 \wedge r_1 = 1 \wedge r_2 = 2 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xrightarrow{I_c} \left\{ \begin{array}{l} r \rightsquigarrow 3, r_1 \rightsquigarrow 1, r_2 \rightsquigarrow 2 \end{array} \right\}
 \end{array}$$

$$\text{let } p = p' = \left\{ \begin{array}{l} r = 3 \wedge r_1 = 1 \wedge r_2 = 2 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\}$$

Thus, we can prove $\vdash \{p[(r_1 + r_2)/r]\} \langle r := r_1 + r_2 \rangle_l \{p'\}$

Handling the liveness analysis. Below the definition of the *liveness* taken from "Data Flow Analysis: Theory and Practice" [?].

A pointer is live at a program point u if the address that it holds at u is read along some path starting at u .

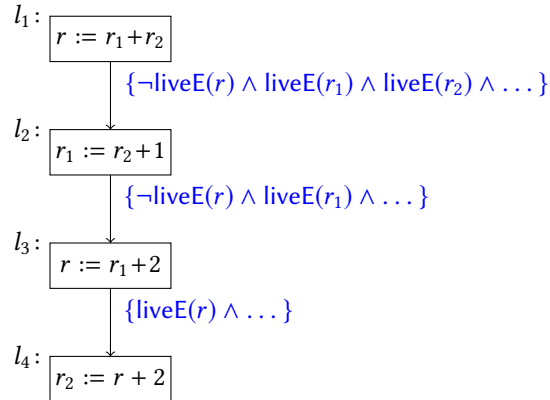
Similarly, in our work, a register or a memory location is live at a program point u , if it is read along some path starting at u .

An important use of the *liveness analysis* is in the *dead code elimination* optimization. The *liveness analysis* first calculates whether a register or a memory location may be live at some program point. Then, the code transformation in the *dead code elimination* eliminates the redundant assignment if its target is not live.

The *dead code elimination* optimization that we hope to prove uses the information that a register or a memory location is not live. So, in our work, as we have introduced, we define is representing the set of registers and memory locations that are not live. So, the registers and memory locations, which are recorded in is , are entirely unused in the following execution of the program before the next writes to them.

$$(IdSet) \quad is \in \mathcal{P}(Addr \cup Reg)$$

We give an example below to show how we describe that a register is not live after the execution of the instruction in the node labelled l_1 .



We mark the register r is *not live* after the execution of the node l_1 , shown as " $\neg \text{liveE}(r)$ ". The assertion " $\neg \text{liveE}(r)$ " says that the register r is in the *not live* set is .

$$\forall M, R, is. (M, R, is) \models \neg \text{liveE}(r) \implies r \in is.$$

The instruction $r_1 := r_2 + 1$ in the node labelled l_2 should not read the register r , according to the meaning the set is , which records the set of registers and memory locations that are not live. So, before the execution of the instruction " $r_1 := r_2 + 1$ " in the node labelled l_2 . We need to ensure that the register r_2 that will be read is *live* to ensure that the execution of such instruction will not read the registers in the set is . The register r can be removed from the set is after the execution of the instruction " $r := r_1 + 2$ " in the node labelled l_3 .

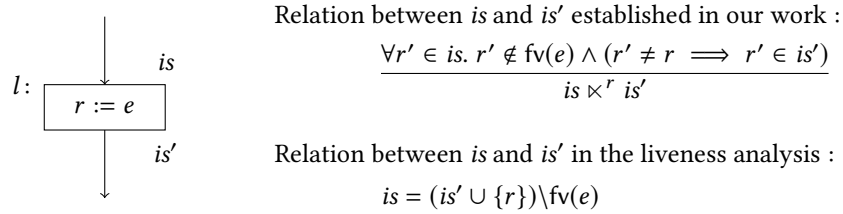


Fig. 5. Handling the liveness analysis

We use Fig. 5 to illustrate how we handle the liveness analysis. The *liveness analysis* is a backward analysis, which calculates is , the set of not liveness identifiers, according to is' , the set of liveness identifiers. It calculates the set of not liveness identifiers before the execution of the instruction " $r := e$ " based on three facts.

- (1) the register r written by the instruction " $r := e$ " is not live before its execution, if r is not read by this instruction;
- (2) the registers read by the instruction " $r := e$ " should be live before its execution.
- (3) Except for the register r written, the registers that may be live after the execution the instruction " $r := e$ " still may be live before the execution of this instruction, as shown below.

$$\forall r' \notin is' \implies (r' \notin is \vee r = r')$$

It explains why the calculation in the *liveness analysis* can only add the register r in the not live set.

We explain that how we present the three facts shown above in our (Asgn-Ac) rule below.

- (1) We relax the fact of the item (1) shown above. we do not care that whether the register r is live or not before the execution of the instruction $r := e$, if r is not read by this instruction. It can be find from the precondition " $p[e/r] \wedge \text{liveE}(e)$ " and the requirement " $\text{ALStaE}(p, r)$ " (defined in Fig. 3) in our (Asgn-Ac) rule.
- (2) As the for the item (2) shown above, the requirement that the register read by the instruction " $r := e$ " is live before its execution is shown in the pre-condition " $\text{liveE}(e)$ ".
- (3) As for the item (3) shown above, we present it in its converse-negative form. Except for the register r written, the registers that are not live before the execution of the instruction " $r := e$ " are still not live after its execution.

$$\forall r' \in (is \setminus \{r\}). r' \in is$$

In our (Asgn-Ac) rule, we show such requirement as " $p \wedge \text{liveE}(r) \Rightarrow p'$ " ($p \Rightarrow p'$ is defined in Fig. 3).

In mathematically, we can find that the relation established between is and is' by the liveness analysis implies the relation between the is and is' ensured by the (Asgn-Ac) rule.

$$is = (is' \cup \{r\}) \setminus \text{fv}(e) \implies is \times^r is'$$

First, we can prove that any register in is is not read by the execution of the instruction " $r := e$ ".

$$\forall r' \in (is' \cup \{r\}) \setminus \text{fv}(e). r' \notin \text{fv}(e)$$

Second, registers that are in is and not written by the instruction " $r := e$ " (not equal to r) are still in is' .

$$\forall r' \in (is' \cup \{r\}) \setminus \text{fv}(e). r \neq r' \implies r' \in is'$$

The Ld-Ac rule checks whether the analysis results for the memory location instruction is correct. The rule for the memory load is a little difficult, since we need to handle two different conditions. *First*, the precondition describes the specific memory location to load. We use the example below to show such condition.

$$\begin{array}{c} \left\{ \begin{array}{l} r_1 = x \wedge x \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xleftarrow{I_c} \left\{ \begin{array}{l} r_1 \rightsquigarrow x, x \rightsquigarrow 3 \end{array} \right\} \\ \downarrow \boxed{r := [r_1]} \\ \left\{ \begin{array}{l} r = 3 \wedge r_1 = x \wedge x \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xleftarrow{I_c} \left\{ \begin{array}{l} r \rightsquigarrow 3, r_1 \rightsquigarrow x, x \rightsquigarrow 3 \end{array} \right\} \end{array}$$

$$\text{let } p = p' = \left\{ \begin{array}{l} r = 3 \wedge r_1 = x \wedge x \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\}$$

Thus, we can prove $\vdash \{((r_1 \mapsto 3) \oplus p[3/r]) \wedge \text{liveE}(r) \wedge \text{liveX}(r_1)\} \langle r := [r_1] \rangle_l \{p'\}$

The other condition is that the precondition does not describe the specific memory location to load. We use the example below to show such condition.

$$\begin{array}{c} \left\{ \begin{array}{l} x \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xleftarrow{I_c} \left\{ \begin{array}{l} x \rightsquigarrow 3 \end{array} \right\} \\ \downarrow \boxed{r := [r_1]} \\ \left\{ \begin{array}{l} r = - \wedge x \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xleftarrow{I_c} \left\{ \begin{array}{l} r \rightsquigarrow \perp, x \rightsquigarrow 3 \end{array} \right\} \end{array}$$

$$\text{let } p = p' = \left\{ \begin{array}{l} r = - \wedge x \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\}$$

Thus, we can prove $\vdash \{((r_1 \mapsto -) \oplus p[-/r]) \wedge \text{liveE}(r) \wedge \text{liveX}(r_1)\} \langle r := [r_1] \rangle_l \{p'\}$

Here, the assertion " $(e_1 \mapsto e_2) \oplus p$ " says that either " $(e_1 \mapsto e_2) \wedge p$ " holds or " $(e_1 \mapsto e_2) \neg p$ " holds. Sometimes, we do not know whether the memory location specified by the expression e_1 is depicted in the precondition. From the semantics of $p_1 \oplus p_2$, we have

$$\forall p, e. p \implies (e \mapsto -) \oplus p.$$

The St-Ac rule checks whether the analysis results for the memory store instruction is correct. It needs to handle two different conditions. *First*, the precondition describes the specific memory location to write to.

$$\begin{array}{c}
 \left\{ \begin{array}{l} r = x \wedge x \mapsto 2 \wedge y \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xleftarrow{I_c} \left\{ \begin{array}{l} r \rightsquigarrow x, x \rightsquigarrow 2, y \rightsquigarrow 3 \end{array} \right\} \\
 \downarrow [r] := 1 \\
 \left\{ \begin{array}{l} r = x \wedge x \mapsto 1 \wedge y \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xleftarrow{I_c} \left\{ \begin{array}{l} r \rightsquigarrow x, x \rightsquigarrow 1, y \rightsquigarrow 3 \end{array} \right\}
 \end{array}$$

$$\text{let } p = \left\{ \begin{array}{l} r = x \wedge y \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\}, \quad p' = \left\{ \begin{array}{l} r = x \wedge x \mapsto 1 \wedge y \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\}$$

Thus, we can prove $\vdash \{((r \mapsto -) \oplus (r \mapsto - * p)) \wedge \text{liveE}(r)\} \langle [r] := 1 \rangle_l \{p'\}$

Second, the precondition does not describe the specific memory location to write to.

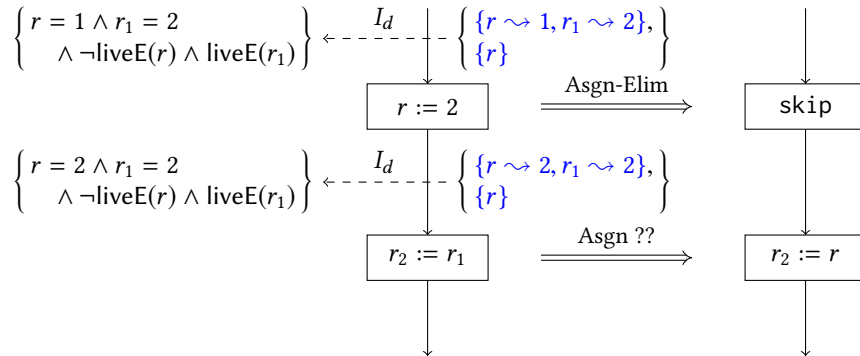
$$\begin{array}{c}
 \left\{ \begin{array}{l} x \mapsto 2 \wedge y \mapsto 3 \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xleftarrow{I_c} \left\{ \begin{array}{l} x \rightsquigarrow 2, y \rightsquigarrow 3 \end{array} \right\} \\
 \downarrow [r] := r_1 \\
 \left\{ \begin{array}{l} \text{true} \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\} \xleftarrow{I_c} \left\{ \begin{array}{l} \{\} \end{array} \right\}
 \end{array}$$

$$\text{let } p = p' = \left\{ \begin{array}{l} \text{true} \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x)) \end{array} \right\}$$

Thus, we can prove $\vdash \{((r \mapsto -) \oplus (r \mapsto - * p)) \wedge \text{liveE}(r)\} \langle [r] := r_1 \rangle_l \{p'\}$

Well-formed Translator. We can use the Link rule to divide the correctness proof of the code transformation to two disjoint parts, and use the Ins-Trans rule to check the correctness of tranfering an instruction in the source code to the target code.

The Asgn rule is used to simplify the expression e . The reason, why we require that the expression e' is live ($\text{liveE}(e')$), is that registers marked not live may have different values in the source and target programs. Because the elimination rules (Asgn-Elim, Ld-Elim) will eliminate the write operations to registers, which is not live after the modification. We use an example below to show such condition.



Assuming that before the source executing " $r := 2$ " and the target executing " $r_2 := r_1$ ", the same registers in the source and target programs have the same values. Since the register r is not live after " $r := 2$ ", we can use the Asgn-Elim rule to eliminate " $r := 2$ " and cause r in the source and target programs having different values. Although we know that " $r = r_1$ " holds in the source program before executing " $r_2 := r_1$ ", we can not replace " $r_2 := r_1$ " with $r_2 := r$.

The other transformation rules are similar with the Asgn rule and the Asgn-Elim rule. The Ld-Elim2 rule is used to handle the elimination of the redundant reads in the *constant propagation* and the *common subexpression elimination*. The Ld-Elim rule and the St-Elim rule are used to handle the elimination of the redundant reads and the redundant writes respectively in the *dead code elimination*.

5.1 Soundness of the Transformation Rules

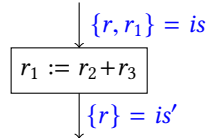
Definition 5.1 (Semantics of the well-formed analysis).

$$\begin{aligned}
 I, C \models_{ge, f} A &::= (\forall M, R, pc, M', R', pc', is. \\
 &\quad (M, R, is) \models I(\mathbb{L}_i(pc)) \wedge C \vdash (M, R, pc) \rightarrow (M', R', pc') \\
 &\quad \implies \exists is'. is \bowtie_R^{C(pc)} is' \wedge (M', R', is') \models I(\mathbb{L}_i(pc')) \\
 &\quad \wedge (\exists is. \text{Init}(ge), is \models I(\mathbb{L}_i(f))) \\
 &\quad \text{where } A(pc) = (\mathbb{L}_i, \mathbb{L}_o)
 \end{aligned}$$

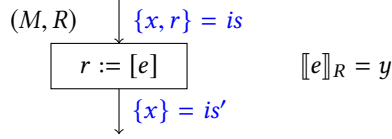
Here, " $is \bowtie_R^i is'$ " describes how the live identifier set is changes. We give its formal definition below.

$$\begin{aligned}
 &\forall r \in is. r \notin \text{usedR}(i) \wedge (r \notin \text{updR}(i) \implies r \in is') \\
 &\forall x \in is. x \notin \text{ldM}(i, R) \wedge (x \notin \text{stM}(i, R) \implies x \in is') \\
 \hline
 &is \bowtie_R^i is' \\
 \\
 \text{usedR}(i) &::= \begin{cases} \text{fv}(e) & \text{if } i \in \{\langle r := e \rangle_I, \langle r := [e] \rangle_I, \langle \text{be } e \rangle_{I_1, I_2} \} \\ \text{fv}(e_1) \cup \text{fv}(e_2) & \text{if } i = \langle [e_1] := e_2 \rangle_I \\ \{r\} & \text{if } i = \langle \text{print } r \rangle \\ \emptyset & \text{otherwise} \end{cases} \\
 \\
 \text{updR}(i) &::= \begin{cases} \{r\} & \text{if } i \in \{\langle r := e \rangle_I, \langle r := [e] \rangle_I\} \\ \emptyset & \text{otherwise} \end{cases} \\
 \\
 \text{ldM}(i, R) &::= \begin{cases} \{\llbracket e \rrbracket_R\} & \text{if } i = \langle r := [e] \rangle_I \\ \emptyset & \text{otherwise} \end{cases} \\
 \\
 \text{stM}(i, R) &::= \begin{cases} \{\llbracket e_1 \rrbracket_R\} & \text{if } i = \langle [e_1] := e_2 \rangle_I \\ \emptyset & \text{otherwise} \end{cases}
 \end{aligned}$$

We use the following example to illustrate the meaning of the " $is \bowtie_R^i is'$ ". Here, the is is the subset of the registers and memory locations that are not live.



As the example shown above, if the register r is in is before the execution of the instruction " $r_1 := r_1 + r_2$ ", the execution of the instruction " $r_1 := r_1 + r_2$ " does not read r and it is still not live after the execution of such instruction.



Similarly, if a memory location x is in is before the execution of the instruction " $r := [e]$ ", the execution of the instruction " $r := [e]$ " does not read the memory location x and it is still not live after the execution of such instruction.

Then, we define the semantics of the well-formed translator.

Definition 5.2 (Semantics of the well-formed translator).

$$I, A \models C \sim C' ::= \forall g, e, f. I, C \models_{g, e, f} A \implies C' \preceq^{A, I} C$$

We define the simulation relation " $C' \preceq^{A, I} C$ " below. Before we give the simulation relation, we first define the state relation between the source and target program states.

Definition 5.3 (State relation).

$$p, is \models (M, R) \sim (M', R') ::= (M, R, is) \models p \wedge (\forall r \notin is. R(r) = R'(r)) \wedge (\forall x \notin is. M(x) = M'(x))$$

Definition 5.4 (Simulation relation). $C' \preceq^{A, I} C$ holds, where $A = (\mathbb{L}_i, \mathbb{L}_o)$, if and only if the followings are true: for all $M, R, pc, M_1, R_1, M'_1, R'_1, pc'$ and is , if $I(\mathbb{L}_i(pc)), is \models (M_1, R_1) \sim (M, R)$ and $C' \vdash (M_1, R_1, pc) \rightarrow (M'_1, R'_1, pc')$, then

- either, there exists M', R' and is' , such that

$$C \vdash (M, R, pc) \rightarrow (M', R', pc') \text{ and } I(\mathbb{L}_i(pc')), is' \models (M', R') \sim (M'_1, R'_1);$$

- or, $C \vdash (M, R, pc) \rightarrow \mathbf{abort}$.

LEMMA 5.5 (SOUNDNESS OF THE WELL-FORMED ANALYSIS).

$$I, C \vdash_{g, e, f} A \implies I, C \models_{g, e, f} A$$

LEMMA 5.6 (SOUNDNESS OF THE WELL-FORMED TRANSLATOR).

$$I, A \vdash C \sim C' \implies I, A \models C \sim C'$$

PROOF. Prove by induction on " $I, A \vdash C \sim C'$ ".

- If $C = \{l \rightsquigarrow i\}$ and $C' = \{l \rightsquigarrow i'\}$, we have

$$I, A \vdash \{l \rightsquigarrow i\} \sim \{l \rightsquigarrow i'\} \tag{a1}$$

Let $A = (\mathbb{L}_i, \mathbb{L}_o)$ and we unfold (a1). We have

$$I(\mathbb{L}_i(l)), I(\mathbb{L}_o(l)) \vdash i \sim i' \tag{a1.1}$$

We do inversion on (a1.1) and discuss each case. Here, we show that proof of the two cases: (1) $i = \langle r := e \rangle_l$ and $i' = \langle r := e' \rangle_l$; (2) $i = \langle r := e \rangle_l$ and $i' = \langle \text{skip} \rangle_l$.

- (1) We have " $p, p' \vdash \langle r := e \rangle_I \sim \langle r := e' \rangle_I$ ", where $p = I(\mathbb{L}_i(I))$ and $p' = I(\mathbb{L}_o(I))$. From the (Asgn) rule, we have

$$p \Rightarrow (e = e') \wedge \text{liveE}(e') \quad (\text{a1.2})$$

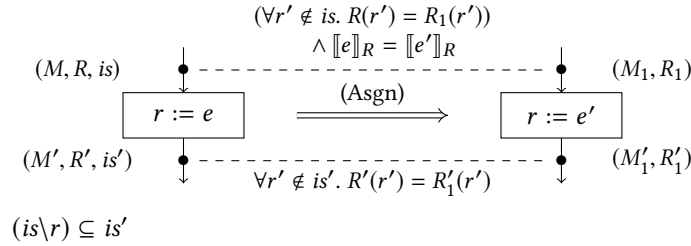
And we need to prove the following holds

$$I, (\mathbb{L}_i, \mathbb{L}_o) \models \{l \rightsquigarrow i\} \sim \{l \rightsquigarrow i'\} \quad (\text{g1.1})$$

We unfold (g1.1) by Def. 5.2. We need to prove the following holds:

$$I, \{l \rightsquigarrow i\} \models (\mathbb{L}_i, \mathbb{L}_o) \Longrightarrow \{l \rightsquigarrow i'\} \preceq^{(\mathbb{L}_i, \mathbb{L}_o), I} \{l \rightsquigarrow i\} \quad (\text{g1.2})$$

We can prove that (g1.2) holds, since the results of the expression e' in source and target program states equal, and, except for the register r assigned, the registers that are in is are also in is' . We use the following figure to illustrate the fact that the values of the register r assigned equal between the source and target program states, and, except the register r , the other registers that do not equal between the source and target program states before execution also do not equal after execution.



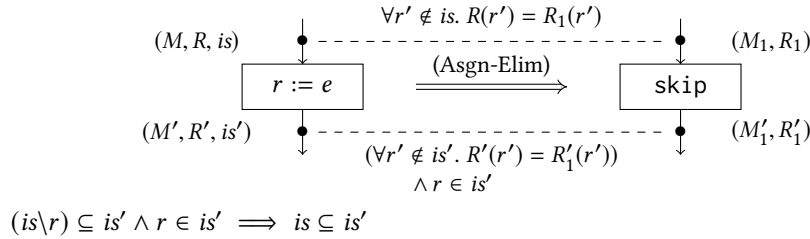
- (2) We have " $p, p' \vdash \langle r := e \rangle_I \sim \langle \text{skip} \rangle_I$ ", where $p = I(\mathbb{L}_i(I))$ and $p' = I(\mathbb{L}_o(I))$. From the (Asgn-Elim) rule, we have

$$p' \Rightarrow \neg \text{liveE}(r) \quad (\text{a1.3})$$

We need to prove the following holds:

$$I, \{l \rightsquigarrow i\} \models (\mathbb{L}_i, \mathbb{L}_o) \Longrightarrow \{l \rightsquigarrow i'\} \preceq^{(\mathbb{L}_i, \mathbb{L}_o), I} \{l \rightsquigarrow i\} \quad (\text{g1.3})$$

We can prove that (g1.3) holds, since the registers that are in is are also in is' and the register r assigned in the source program is also in is' .



It means that the registers that do not equal between the source and target program states before the execution also do not equal after execution. The register r also do not require to equal after execution.

- If $C = (C_1 \uplus C_2)$ and $C' = (C'_1 \uplus C'_2)$, we have

$$I, A \vdash (C_1 \uplus C_2) \sim (C'_1 \uplus C'_2) \quad (\text{a2})$$

The below are the inductive hypotheses.

$$I, A \vdash C_1 \sim C'_1 \implies I, A \models C_1 \sim C'_1 \quad (\text{a2.1})$$

$$I, A \vdash C_2 \sim C'_2 \implies I, A \models C_2 \sim C'_2 \quad (\text{a2.2})$$

We unfold (a2) according to the (Link) rule, we have

$$I, A \vdash C_1 \sim C'_1 \quad (\text{a2.3})$$

$$I, A \vdash C_2 \sim C'_2 \quad (\text{a2.4})$$

By applying (a2.1) on (a2.3) and (a2.2) on (a2.4), we have

$$I, A \models C_1 \sim C'_1 \quad (\text{a2.5})$$

$$I, A \models C_2 \sim C'_2 \quad (\text{a2.6})$$

We need to prove

$$I, A \models (C_1 \uplus C'_1) \sim (C_2 \sim C'_2) \quad (\text{g2})$$

Since we have the following lemmas

$$\forall I, C, C', A, ge, f. (I, C \models_{ge, f} A \wedge C' \subseteq C) \implies I, C' \models_{ge, f} A$$

and

$$\forall C_1, C'_1, C_2, C'_2, A, I. (C'_1 \preceq^{A, I} C_1 \wedge C'_2 \preceq^{A, I} C_2) \implies (C'_1 \uplus C'_2) \preceq^{A, I} (C_1 \uplus C_2)$$

hold, we finish the proof of (g2).

□

LEMMA 5.7 (SIMULATION ENSURES REFINEMENT).

$$(I, C \models_{ge, f} A \wedge I, A \models C \sim C') \implies C' \subseteq_{ge, f} C$$

Thus, we can prove the Lemma. 2.4 by applying Lemma. 5.5, Lemma. 5.6 and Lemma. 5.7.

6 PROOF OF THE OPTIMIZATION CORRECTNESS

6.1 Correctness proof: Constant propagation

Analyzer. The implementation of the *value analysis* is shown below.

```

Val_Analyzer(C, f) :
1.   for ( $\forall l \in \text{dom}(C)$ ) :
2.      $\mathbb{L}_o(l) = \emptyset$ 
3.   while (any  $\mathbb{L}_o(l)$  changes occur) :
4.     for ( $\forall l \in \text{dom}(C)$ ) :
5.        $\mathbb{L}_i(l) = \bigcap_{l_p \in \text{pred}(C, l)} \mathbb{L}_o(l_p)$ 
6.        $\mathbb{L}_o(l) = f(\mathbb{L}_i(l), C(l))$ 
7.   return ( $\mathbb{L}_i, \mathbb{L}_o$ )

```

$\text{Analyzer}(C, ge, f) = \text{Val_Analyzer}(C, f)$

Below, we give the implementation of the transfer function in the value analysis.

$$\begin{aligned}
 f(\langle r := e \rangle_l, a_v) &::= a_v \{ r \rightsquigarrow \llbracket e \rrbracket_{a_v} \} \\
 f(\langle r := [e] \rangle_l, a_v) &::= \begin{cases} a_v \{ r \rightsquigarrow \perp \} & \text{if } \llbracket e \rrbracket_{a_v} = \perp \\ a_v \{ r \rightsquigarrow a_v(x) \} & \text{if } \llbracket e \rrbracket_{a_v} = x \\ a_v \{ r \rightsquigarrow \top \} & \text{otherwise} \end{cases} \\
 f(\langle [e] := e' \rangle_l, a_v) &::= \begin{cases} a_v \{ x \rightsquigarrow \llbracket e' \rrbracket_{a_v} \} & \text{if } \llbracket e \rrbracket_{a_v} = x \\ a_v \setminus \text{Addr} & \text{otherwise} \end{cases}
 \end{aligned}$$

Then, we define the merging of two abstract interpretations in the value analysis.

$$\begin{aligned}
 a_v \cap a'_v &::= \{ x \rightsquigarrow \hat{v} \mid x \in (\text{dom}(a_v) \cap \text{dom}(a'_v)) \wedge \hat{v} = (a_v(x) \cap a'_v(x)) \} \\
 &\quad \cup \{ r \rightsquigarrow \hat{v} \mid r \in (\text{dom}(a_v) \cap \text{dom}(a'_v)) \wedge \hat{v} = (a_v(r) \cap a'_v(r)) \} \\
 \hat{v}_1 \cap \hat{v}_2 &::= \begin{cases} \hat{v}_1 & \text{if } \hat{v}_1 = \hat{v}_2 \\ \top & \text{otherwise} \end{cases}
 \end{aligned}$$

Translator. Below, we give an implementation of the translator in *constant propagation* optimizer.

$$\text{Translator}(C, A) ::= \begin{cases} \{l_1 \rightsquigarrow \langle r := v \rangle_{l_2}\} \uplus \text{Translator}(C', A) & \text{if } C = \{l_1 \rightsquigarrow \langle r := e \rangle_{l_2}\} \uplus C', \llbracket e \rrbracket_{\mathbb{L}_i(l_1)} = v \\ \{l_1 \rightsquigarrow \langle r := v \rangle_{l_2}\} \uplus \text{Translator}(C', A) & \text{elif } C = \{l_1 \rightsquigarrow \langle r := [e] \rangle_{l_2}\} \uplus C', \\ & \llbracket e \rrbracket_{\mathbb{L}_i(l_1)} = x, \mathbb{L}_i(l_1)(x) = v \\ \{l \rightsquigarrow i\} \uplus \text{Translator}(C', A) & \text{elif } C = \{l \rightsquigarrow i\} \uplus C' \\ \emptyset & \text{elif } C = \emptyset \\ \text{undef} & \text{otherwise} \end{cases}$$

where $A = (\mathbb{L}_i, \mathbb{L}_o)$

Correctness proof. First, we define a converter I_c to convert the result of the value analysis to the assertion form.

$$I_c(a_v) ::= (\forall r \in \text{dom}(a_v). (\exists v. r = v \wedge a_v(r) = v) \vee (r = - \wedge a_v(r) \in \{\perp, \top\})) \\ \wedge (\forall x \in \text{dom}(a_v). (\exists v. x \mapsto v \wedge a_v(x) = v) \vee (x \mapsto - \wedge a_v(x) \in \{\perp, \top\})) \\ \wedge (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x))$$

Then, we prove the correctness of the Lemma 6.1.

LEMMA 6.1 (OPTIMIZER CORRECTNESS: CONSTANT PROPAGATION).

$$\forall C, ge, f, A, C'. (\text{Analyzer}(C, ge, f) = A \wedge \text{Translater}(C, A) = C') \implies I_c \vdash_{ge, f} (C, A) \sim C'$$

We divide the correctness proof the Lemma 6.1 into two lemmas, shown as Lemma 6.2 and Lemma 6.3, respectively.

LEMMA 6.2. $\forall C, ge, f. \text{Analyzer}(C, ge, f) = A \implies I_c, C \vdash_{ge, f} A.$

PROOF. Let $A = (\mathbb{L}_i, \mathbb{L}_o)$ and we apply the (wf-Analysis) rule on the proof goal. Then, we need to prove the following subgoals.

$$\exists is. (\text{Init}(ge), is) \models I_c(\mathbb{L}_i(f)) \quad (\text{g1})$$

for all $l \in \text{dom}(C)$:

$$I_c(\mathbb{L}_o(l)) \Rightarrow I_c(\mathbb{L}_i(l_s)), \text{ for all } l_s \in \text{succ}(C(l)) \quad (\text{g2}) \\ \vdash \{I_c(\mathbb{L}_i(l))\} C(l) \{I_c(\mathbb{L}_o(l))\}$$

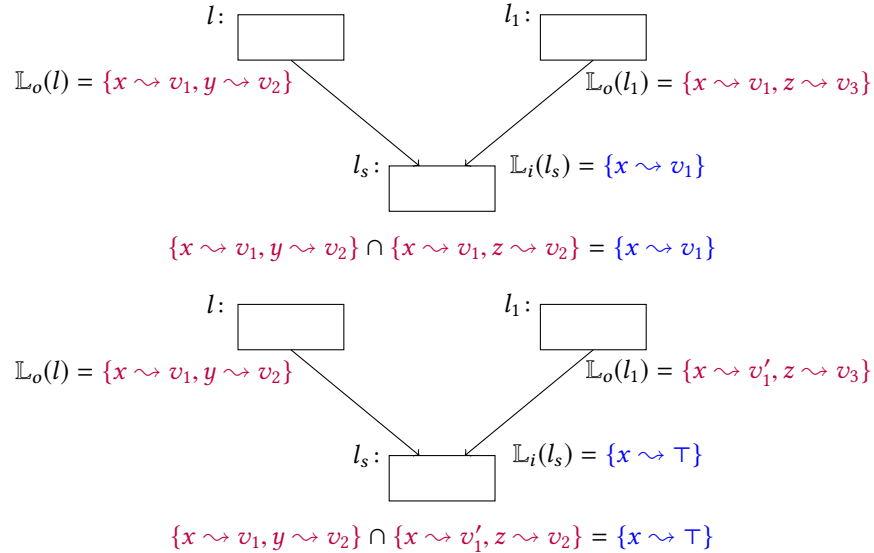
According to $\mathbb{L}_i(f) = \emptyset$, we can prove that $(\text{Init}(ge), \emptyset) \models (\forall r. \text{liveE}(r)) \wedge (\forall x. \text{liveX}(x))$. Thus, we prove the subgoal (g1).

For the subgoal (g2), we need to prove that for all $l \in \text{dom}(C)$, the following subgoals hold:

$$I_c(\mathbb{L}_o(l)) \Rightarrow I_c(\mathbb{L}_i(l_s)), \text{ for all } l_s \in \text{succ}(C(l)) \quad (\text{g2-1})$$

$$\vdash \{I_c(\mathbb{L}_i(l))\} C(l) \{I_c(\mathbb{L}_o(l))\} \quad (\text{g2-2})$$

The correctness of the subgoal (g2-1) is straight-forward. Here, we omit the definition of merging two abstract interpretations. We use the figures shown below to illustrate the meaning of merging two abstract interpretations.



According to the definition of merging two abstract interpretations in the value analysis, we have, where $l_s \in \text{succ}(C(l))$,

$$\forall r \in \text{dom}(\mathbb{L}_i(l_s)). \mathbb{L}_o(l)(r) = \mathbb{L}_i(l_s)(r) \vee \mathbb{L}_i(l_s)(r) = \top$$

$$\text{dom}(\mathbb{L}_i(l_s)) \subseteq \text{dom}(\mathbb{L}_o(l))$$

Thus, we can prove that $I_c(\mathbb{L}_o(l)) \Rightarrow I_c(\mathbb{L}_i(l_s))$ holds, and we finish the proof of the subgoal (g2-1). We prove the subgoal (g2-2) according to the following conclusion.

$$\forall a_v, a'_v, i. f(i, a_v) = a'_v \implies \vdash \{I_c(a_v)\} i \{I_c(a'_v)\}$$

We prove the conclusion shown above by discussing each case of the instruction i :

- $i = \langle r := e \rangle_l$, and $a'_v = a_v \{r \rightsquigarrow \llbracket e \rrbracket_{a_v}\}$. We discuss the result of the $\llbracket e \rrbracket_{a_v}$.
 - there exists v , such that $\llbracket e \rrbracket_{a_v} = v$. We have the following conclusion.

$$\forall v. \llbracket e \rrbracket_{a_v} = v \implies (I_c(a_v) \Rightarrow e = v)$$

Let $p = (I_c(a_v \setminus r) \wedge r = v)$, then we have

$$\frac{I_c(a_v) \Rightarrow p[e/r] \wedge \text{liveE}(e) \quad \frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow I_c(a'_v)}{\vdash \{p[e/r] \wedge \text{liveE}(e)\} \langle r := e \rangle_l \{I_c(a'_v)\}}}{\vdash \{I_c(a_v)\} \langle r := e \rangle_l \{I_c(a'_v)\}}$$

We prove the " $p \wedge \text{liveE}(r) \Rightarrow I_c(a'_v)$ " by the following steps.

$$\begin{aligned} & I_c(a_v \setminus r) \wedge r = e \wedge e = v \wedge \text{liveE}(r) \\ \Rightarrow & I_c(a_v \setminus r) \wedge r = v \wedge \text{liveE}(r) \\ \Rightarrow & I_c(a_v \{r \rightsquigarrow \llbracket e \rrbracket_{a_v}\}) \quad (* \text{ where } \llbracket e \rrbracket_{a_v} = v *) \end{aligned}$$

- $\llbracket e \rrbracket_{a_v} \in \{\perp, \top\}$. Let $p = (I_c(a_v \setminus r) \wedge r = -)$, then we have

$$\frac{I_c(a_v) \Rightarrow p[e/r] \wedge \text{liveE}(e) \quad \frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow I_c(a'_v)}{\{p[e/r] \wedge \text{liveE}(e)\} \langle r := e \rangle_l \{I_c(a'_v)\}}}{\vdash \{I_c(a_v)\} \langle r := e \rangle_l \{I_c(a'_v)\}}$$

- $i = \langle r := [e] \rangle_l$. We discuss the result of the $\llbracket e \rrbracket_{a_v}$.
 - there exists x , such that $\llbracket e \rrbracket_{a_v} = x$. We have $a'_v = a_v \{r \rightsquigarrow a_v(x)\}$. Here, we consider that condition that there exists v such that $a_v(x) = v$. The condition that $a_v(x) \in \{\perp, \top\}$ is similar. We have the following conclusion.

$$\forall x, v. (\llbracket e \rrbracket_{a_v} = x \wedge a_v(x) = v) \implies (I_c(a_v) \Rightarrow e \mapsto v)$$

Let $p = (I_c(a_v \setminus r) \wedge r = v)$, $p_1 = ((e \mapsto v) \oplus p[v/r]) \wedge \text{liveE}(e) \wedge \text{liveX}(e)$, then we have

$$\frac{I_c(a_v) \Rightarrow p_1 \quad \frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow I_c(a'_v)}{\vdash \{p_1\} \langle r := [e] \rangle_l \{I_c(a'_v)\}}}{\vdash \{I_c(a_v)\} \langle r := [e] \rangle_l \{I_c(a'_v)\}}$$

We prove the " $p \wedge \text{liveE}(r) \Rightarrow I_c(a'_v)$ " by the following steps.

$$\begin{aligned} & (I_c(a_v \setminus r) \wedge r = v) \wedge \text{liveE}(r) \\ \Rightarrow & I_c(a'_v) \quad (* \text{ where } a'_v = a_v \{r \rightsquigarrow v\} *) \end{aligned}$$

- $\llbracket e \rrbracket_{a_v} = \top$. We have $a'_v = a_v \{r \rightsquigarrow \top\}$. The condition that $\llbracket e \rrbracket_{a_v} = \perp$ is similar. Let $p = (I_c(a_v \setminus r) \wedge r = -)$, $p_1 = ((e \mapsto -) \oplus p[-/r]) \wedge \text{liveE}(e) \wedge \text{liveX}(e)$, then we have

$$\frac{I_c(a_v) \Rightarrow p_1 \quad \frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow I_c(a'_v)}{\vdash \{p_1\} \langle r := [e] \rangle_l \{I_c(a'_v)\}}}{\vdash \{I_c(a_v)\} \langle r := [e] \rangle_l \{I_c(a'_v)\}}$$

We prove the " $p \wedge \text{liveE}(r) \Rightarrow I_c(a'_v)$ " by the following steps.

$$\begin{aligned} & (I_c(a_v \setminus r) \wedge r = -) \wedge \text{liveE}(r) \\ \Rightarrow & (I_c(a_v \setminus r) \wedge r = -) \wedge \text{liveE}(r) \\ \Rightarrow & I_c(a'_v) \quad (* \text{ where } a'_v = a_v \{r \rightsquigarrow \top\} *) \end{aligned}$$

- $i = \langle [e] := e' \rangle_l$. We discuss the result of the $\llbracket e \rrbracket_{a_v}$.
 - there exists x , such that $\llbracket e \rrbracket_{a_v} = x$. We have $a'_v = a_v \{x \rightsquigarrow \llbracket e' \rrbracket_{a_v}\}$. We consider the condition that there exists v such that $\llbracket e' \rrbracket_{a_v} = v$. The condition that $\llbracket e' \rrbracket_{a_v} \in \{\perp, \top\}$ is similar. Let $p = (I_c(a_v \setminus x) \wedge e = x \wedge e' = v)$, and $p_1 = ((e \mapsto -) \oplus (e \mapsto - * p) \wedge \text{liveE}(e_1, e_2))$. Since we can prove the following conclusion

$$\forall p. p \Rightarrow ((e \mapsto -) \oplus (e \mapsto - * p)),$$

we have

$$\frac{I_c(a_v) \Rightarrow p_1 \quad \frac{\text{ALStaE}(p, e) \quad (e \mapsto e' * p) \wedge \text{liveX}(e) \Rightarrow I_c(a'_v)}{\{p_1\} \langle [e] := e' \rangle_l \{I_c(a'_v)\}}}{\vdash \{I_c(a_v)\} \langle [e] := e' \rangle_l \{I_c(a'_v)\}}$$

We prove " $(e \mapsto e' * p) \wedge \text{liveX}(e) \Rightarrow I_c(a'_v)$ " by the following steps.

$$\begin{aligned} & (e \mapsto e' * (I_c(a_v \setminus x) \wedge e = x \wedge e' = v)) \wedge \text{liveX}(e) \\ \Rightarrow & I_c(a_v \setminus x) \wedge e \mapsto e' \wedge e = x \wedge e' = v \wedge \text{liveX}(e) \\ \Rightarrow & I_c(a_v \setminus x) \wedge x \mapsto v \wedge \text{liveX}(x) \\ \Rightarrow & I_c(a_v) \end{aligned}$$

- $\llbracket e \rrbracket_{a_v} \in \{\perp, \top\}$. We have $a'_v = a_v \setminus \text{Addr}$. Let $p = I_c(a_v \setminus \text{Addr})$, and $p_1 = ((e \mapsto -) \oplus (e \mapsto - * p) \wedge \text{liveE}(e_1, e_2))$. And we have

$$\frac{I_c(a_v) \Rightarrow p_1 \quad \frac{\text{ALStaE}(p, e) \quad (e \mapsto e' * p) \wedge \text{liveX}(e) \Rightarrow I_c(a'_v)}{\{p_1\} \langle [e] := e' \rangle_l \{I_c(a'_v)\}}}{\vdash \{I_c(a_v)\} \langle [e] := e' \rangle_l \{I_c(a'_v)\}}$$

We prove the " $(e \mapsto e' * p) \wedge \text{liveX}(e) \Rightarrow I_c(a'_v)$ " by the following steps.

$$\begin{aligned} & (e \mapsto e' * I_c(a_v \setminus \text{Addr})) \wedge \text{liveX}(e) \\ \Rightarrow & I_c(a_v \setminus \text{Addr}) \\ \Rightarrow & I_c(a'_v) \end{aligned}$$

□

LEMMA 6.3. $\forall C, A, C'. \text{Translator}(C, A) = C' \implies I_c, A \vdash C \sim C'$.

PROOF. We assume that the source code C is not an empty set. We prove this lemma by induction on C .

- If $C = \{l \rightsquigarrow i\}$, we have the following holds.

$$\text{Translater}(\{l \rightsquigarrow i\}, A) = C' \quad (1)$$

We unfold (1) according to the definition of the translater. We discuss each case respectively and assume $A = (\mathbb{L}_i, \mathbb{L}_o)$.

- $i = \langle r := e \rangle_{l_1}$ and there exists v such that $\llbracket e \rrbracket_{\mathbb{L}_i(l)} = v$. We get $C' = \{l \rightsquigarrow \langle r := v \rangle_{l_1}\}$. And we need to prove the following holds.

$$I_c, A \vdash \{l \rightsquigarrow \langle r := e \rangle_{l_1}\} \sim \{l \rightsquigarrow \langle r := v \rangle_{l_1}\} \quad (\text{g3-1})$$

By applying (Ins-Trans) on (g3-1), we get

$$I_c(\mathbb{L}_i(l)), I_c(\mathbb{L}_o(l)) \vdash \langle r := e \rangle_{l_1} \sim \langle r := v \rangle_{l_1} \quad (\text{g3-1.1})$$

By applying (Asgn) rule on (g3-1.1), we need to prove

$$I_c(\mathbb{L}_i(l)) \Rightarrow (e = v) \wedge \text{liveE}(v) \quad (\text{g3-1.2})$$

Since, we have the following conclusion

$$\forall v. \llbracket e \rrbracket_{a_v} = v \implies (I_c(a_v) \Rightarrow e = v),$$

we finish the proof of this case.

- $i = \langle r := [e] \rangle_{l_1}$ and there exists x, v such that $\llbracket e \rrbracket_{\mathbb{L}_i(l)} = x$ and $\mathbb{L}_i(x) = v$. We get $C' = \{l \rightsquigarrow \langle r := v \rangle_{l_1}\}$. And we need to prove the following holds.

$$I_c, A \vdash \{l \rightsquigarrow \langle r := [e] \rangle_{l_1}\} \sim \{l \rightsquigarrow \langle r := v \rangle_{l_1}\} \quad (\text{g3-2})$$

By applying (Ins-Trans) on (g3-2), we get

$$I_c(\mathbb{L}_i(l)), I_c(\mathbb{L}_o(l)) \vdash \langle r := [e] \rangle_{l_1} \sim \langle r := v \rangle_{l_1} \quad (\text{g3-2.1})$$

By applying (Ld-Elim2) rule on (g3-2.1), we need to prove

$$I_c(\mathbb{L}_i(l)) \Rightarrow (e \mapsto v) \wedge \text{liveE}(v) \quad (\text{g3-2.2})$$

Since, we have the following conclusion

$$\forall x, v. (\llbracket e \rrbracket_{a_v} = x \wedge a_v(x) = v) \implies (I_c(a_v) \Rightarrow e \mapsto v),$$

we finish the proof of this case.

- $C' = \{l \rightsquigarrow i\}$. And we need to prove the following holds.

$$I_c, A \vdash \{l \rightsquigarrow i\} \sim \{l \rightsquigarrow i\} \quad (\text{g3-3})$$

The subgoal (g3-3) can be proved by discuss each case of the instruction i .

- If $C = \{l \rightsquigarrow i\} \uplus C_1$, we have the following holds.

$$\text{Translater}(\{l \rightsquigarrow i\} \uplus C_1, A) = C' \quad (2)$$

And the inductive hypothesis is shown below.

$$\forall A, C'_1. \text{Translater}(C_1, A) = C'_1 \implies I_c, A \vdash C_1 \sim C'_1 \quad (3)$$

We unfold (2) according to the definition of the translater. We prove by discussing each case respectively and assume $A = (\mathbb{L}_i, \mathbb{L}_o)$. Here, we only show the proof of the first case. The proof of the other cases are similar to the first one.

- $i = \langle r := e \rangle_{l_i}$ and there exists v such that $\llbracket e \rrbracket_{\mathbb{L}_i(l)} = v$. We get $C' = \{l \rightsquigarrow \langle r := v \rangle_{l_i}\} \uplus \text{Translator}(C_1, A)$. Let $C'_1 = \text{Translator}(C_1, A)$. We need to prove the following holds.

$$I_c, A \vdash (\{l \rightsquigarrow \langle r := e \rangle_{l_i}\} \uplus C_1) \sim (\{l \rightsquigarrow \langle r := v \rangle_{l_i}\} \uplus C'_1) \quad (\text{g4})$$

By applying (Link) rule on (g4), we need to prove

$$I_c, A \vdash \{l \rightsquigarrow \langle r := e \rangle_{l_i}\} \sim \{l \rightsquigarrow \langle r := v \rangle_{l_i}\} \quad (\text{g4-1})$$

$$I_c, A \vdash C_1 \sim C'_1 \quad (\text{g4-2})$$

The subgoal (g4-1) can be proved by applying the (Ins-Trans) rule, and the subgoal (g4-2) can be proved by applying the inductive hypothesis (3).

□

6.2 Correctness proof: Dead code elimination

Analyzer. The implementation of the *liveness analysis* is shown below.

```

Lv_Analyzer(C, ( $\mathbb{L}_{vi}, \mathbb{L}_{vo}$ ), f) :
1.   if ( $\forall l \in \text{dom}(C)$ ) :
2.      $\mathbb{L}_i(l) = \emptyset$ 
3.   while (any  $\mathbb{L}_i(l)$  changes occur) :
4.     for ( $\forall l \in \text{dom}(C)$ ) :
5.        $\mathbb{L}_o(l) = \bigcap_{l_s \in \text{succ}(C(l))} \mathbb{L}_i(l_s)$ 
6.        $\mathbb{L}_i(l) = f_L(\mathbb{L}_{vi}(l), \mathbb{L}_o(l), C(l))$ 
7.   return ( $\mathbb{L}_i, \mathbb{L}_o$ )

```

Below, we give the implementation of the transfer function in the liveness analysis.

$$\begin{aligned}
f_L(\langle r := e \rangle_l, a_v, a_l) &::= (a_l \cup \{r\}) \setminus \text{fv}(e) \\
f_L(\langle r := [e] \rangle_l, a_v, a_l) &::= \begin{cases} (a_l \cup \{r\}) \setminus (\text{fv}(e) \cup \{x\}) & \text{if } \llbracket e \rrbracket_{a_v} = x \\ (a_l \cup \{r\}) \setminus (\text{fv}(e) \cup \text{Addr}) & \text{otherwise} \end{cases} \\
f_L(\langle [e_1] := e_2 \rangle_l, a_v, a_l) &::= \begin{cases} (a_l \cup \{x\}) \setminus (\text{fv}(e_1) \cup \text{fv}(e_2)) & \text{if } \llbracket e_1 \rrbracket_{a_v} = x \\ a_l \setminus (\text{fv}(e_1) \cup \text{fv}(e_2)) & \text{otherwise} \end{cases}
\end{aligned}$$

The merging of two abstract interpretations in the liveness analysis is just the intersection of two the sets.

$$a_v \cap a'_v ::= \{x \mid x \in a_v \wedge x \in a'_v\} \cup \{r \mid r \in a_v \wedge r \in a'_v\}$$

The Analyzer is defined below. It includes the value analysis and the liveness analysis.

$$\begin{aligned}
\text{Analyzer}(C, ge, f) &::= \{l \rightsquigarrow (L_i, L_o) \mid l \in \text{dom}(C) \wedge L_i = (\mathbb{L}_{vi}(l), \mathbb{L}_{li}(l)) \wedge L_o = (\mathbb{L}_{vo}(l), \mathbb{L}_{lo}(l))\} \\
&\text{where } (L_{vi}, L_{vo}) = \text{Val_Analyzer}(C, f), (L_{li}, L_{lo}) = \text{Lv_Analyzer}(C, (L_{vi}, L_{vo}), f)
\end{aligned}$$

Translator. Below, we give the implementation of the translator in the *dead code elimination* optimizer.

$$\text{Translator}(C, A) ::= \begin{cases} \{l_1 \rightsquigarrow \langle \text{skip} \rangle_{l_2}\} \uplus \text{Translator}(C', A) & \text{if } C = \{l_1 \rightsquigarrow \langle r := e \rangle_{l_2}\} \uplus C', r \in a'_l, \\ & \mathbb{L}_o(l) = (a'_v, a'_l) \\ \{l_1 \rightsquigarrow \langle \text{skip} \rangle_{l_2}\} \uplus \text{Translator}(C', A) & \text{elif } C = \{l_1 \rightsquigarrow \langle r := [e] \rangle_{l_2}\} \uplus C', r \in a'_l, \\ & \mathbb{L}_o(l) = (a'_v, a'_l) \\ \{l_1 \rightsquigarrow \langle \text{skip} \rangle_{l_2}\} \uplus \text{Translator}(C', A) & \text{elif } C = \{l_1 \rightsquigarrow \langle [e_1] := e_2 \rangle_{l_2}\} \uplus C', x \in a'_l, \\ & \llbracket e_1 \rrbracket_{a_v} = x, \mathbb{L}_i = (a_v, a_l), \mathbb{L}_o = (a'_v, a'_l) \\ \{l \rightsquigarrow i\} \uplus \text{Translator}(C', A) & \text{elif } C = \{l_1 \rightsquigarrow i\} \uplus C' \\ \emptyset & \text{elif } C = \emptyset \\ \text{undef} & \text{otherwise} \end{cases}$$

where $A = (\mathbb{L}_i, \mathbb{L}_o)$

Correctness proof. First, we define a converter I_l to convert the result of the analysis to the assertion form.

$$\begin{aligned} I'_v(a_v) &::= (\forall r \in \text{dom}(a_v). (\exists v. r = v \wedge a_v(r) = v) \wedge (r = - \wedge a_v(r) \in \{\perp, \top\})) \\ &\quad \wedge (\forall x \in \text{dom}(a_v). (\exists x. x \mapsto v \wedge a_v(x) = v) \wedge (x \mapsto - \wedge a_v(x) \in \{\perp, \top\})) \\ I'_l(a_l) &::= (\forall r \notin a_l. \text{liveE}(r)) \wedge (\forall r \in a_l. \neg \text{liveE}(r)) \\ &\quad \wedge (\forall x \notin a_l. \text{liveX}(x)) \wedge (\forall x \in a_l. \neg \text{liveX}(x)) \\ I_d(\{a_v, a_l\}) &::= I'_v(a_v) \wedge I'_l(a_l) \end{aligned}$$

Then, we prove the correctness of the Lemma 6.4.

LEMMA 6.4 (OPTIMIZER CORRECTNESS: DEAD CODE ELIMINATION).

$$\forall C, ge, f, A. (\text{Analyzer}(C, ge, f) = A \wedge \text{Translator}(C, A) = C') \implies I_d \vdash_{ge, f} (C, A) \sim C'$$

We divide the correctness proof of Lemma. 6.4 into two lemmas, shown as Lemma. 6.5 and Lemma. 6.6 respectively.

LEMMA 6.5. $\forall C, ge, f. \text{Analyzer}(C, ge, f) = A \implies I_d, C \vdash_{ge, f} A.$

PROOF. Let $A = (\mathbb{L}_i, \mathbb{L}_o)$ and we apply the (wf-Analysis) rule on the proof goal. Then, we need to prove the following subgoals.

$$\exists is. (\text{Init}(ge), is) \models I_d(\mathbb{L}_i(f)) \tag{g5}$$

for all $l \in \text{dom}(C)$:

$$\begin{aligned} I_d(\mathbb{L}_o(l)) &\Rightarrow I_d(\mathbb{L}_i(l_s)), \text{ for all } l_s \in \text{succ}(C(l)) \\ &\vdash \{I_d(\mathbb{L}_i(l))\} C(l) \{I_d(\mathbb{L}_o(l))\} \end{aligned} \tag{g6}$$

Let $\mathbb{L}_i(f) = \{a_{v_1}, a_{l_1}\}$. Since $a_{v_1} = \emptyset$, we can prove that $(\text{Init}(ge), a_{l_1}) \models I'_v(\emptyset) \wedge I'_l(a_{l_1})$ holds. Thus, we prove the subgoal (g5).

For the subgoal (g6), we need to prove that for all $l \in \text{dom}(C)$, the following subgoals hold:

$$I_d(\mathbb{L}_o(l)) \Rightarrow I_d(\mathbb{L}_i(l_s)), \text{ for all } l_s \in \text{succ}(C(l)) \tag{g6-1}$$

$$\vdash \{I_d(\mathbb{L}_i(l))\} C(l) \{I_d(\mathbb{L}_o(l))\} \tag{g6-2}$$

Let $\mathbb{L}_o(l) = (a_v, a_l)$ and $\mathbb{L}_i(l_s) = (a'_v, a'_l)$. We first prove that subgoal (g6-1). Since we can prove that " $I'_v(a_v) \Rightarrow I'_v(a'_v)$ " and $I'_l(a_l) \Rightarrow I'_l(a'_l)$ hold, we have

$$\begin{aligned} I_d(\{a_v, a_l\}) &\Rightarrow I'_v(a_v) \wedge I'_l(a_l) \\ &\Rightarrow I'_v(a'_v) \wedge I'_l(a'_l) \\ &\Rightarrow I_d(\{a'_v, a'_l\}), \end{aligned}$$

and we finish the proof of the subgoal (g6-1).

We define

$$\begin{aligned} I'_{l-r}(a_l, r') &::= (\forall r \notin (a_l \cup \{r'\}). \text{liveE}(r)) \wedge (\forall r \in (a_l \setminus r'). \neg \text{liveE}(r)) \\ &\quad \wedge (\forall x \notin a_l. \text{liveX}(x)) \wedge (\forall x \in a_l. \neg \text{liveX}(x)) \\ I'_{l-x}(a_l, x') &::= (\forall r \notin a_l. \text{liveE}(r)) \wedge (\forall r \in a_l. \neg \text{liveE}(r)) \\ &\quad \wedge (\forall x \notin (a_l \cup \{x'\}). \text{liveX}(x)) \wedge (\forall x \in (a_l \setminus x'). \neg \text{liveX}(x)) \\ I'_{l-nlA}(a_l) &::= (\forall r \notin a_l. \text{liveE}(r)) \wedge (\forall r \in a_l. \neg \text{liveE}(r)) \\ &\quad \wedge (\forall x \notin a_l. \text{liveX}(x)) \end{aligned}$$

as the auxiliary definitions, and prove the subgoal (g6-2) according to the following conclusion.

$$\begin{aligned} &\forall a_v, a'_v, a_l, a'_l, i. \\ &(f(i, a_v) = a'_v \wedge f_L(i, a_v, a'_l) = a_l) \implies \vdash \{I_l(\{a_v, a_l\})\} i \{I_l(\{a'_v, a'_l\})\} \end{aligned}$$

We prove the conclusion shown above by discussing each case of the instruction i :

- $i = \langle r := e \rangle_l$, $a'_v = a_v \{r \rightsquigarrow \llbracket e \rrbracket_{a_v}\}$ and $a_l = (a'_l \setminus \text{fv}(e)) \cup \{r\}$. We discuss the result of the $\llbracket e \rrbracket_{a_v}$.
 - there exists v , such that $\llbracket e \rrbracket_{a_v} = v$. We have the following conclusions.

$$\begin{aligned} \forall v, e, a_v. \llbracket e \rrbracket_{a_v} = v &\implies (I'_v(a_v) \Rightarrow e = v) \\ \forall e, a_l. (\text{fv}(e) \cap a_l = \emptyset) &\implies (I'_v(a_l) \Rightarrow \text{liveE}(e)) \\ \forall a_l, a'_l. (a_l \subseteq a'_l) &\implies (I'_l(a_l) \Rightarrow I'_l(a'_l)) \end{aligned}$$

Let $p = (I'_v(a_v \setminus r) \wedge I'_{l-r}(a_l, r) \wedge r = v)$, then we have

$$\frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow I_d(\{a'_v, a'_l\})}{I_d(\{a_v, a_l\}) \Rightarrow p[e/r] \wedge \text{liveE}(e) \quad \vdash \{p[e/r] \wedge \text{liveE}(e)\} \langle r := e \rangle_l \{I_d(\{a'_v, a'_l\})\}} \vdash \{I_d(\{a_l, a_v\})\} \langle r := e \rangle_l \{I_d(\{a'_l, a'_v\})\}$$

We prove " $p \wedge \text{liveE}(r) \Rightarrow I_l(\{a'_v, a'_l\})$ " by the following steps.

$$\begin{aligned} &I'_v(a_v \setminus r) \wedge I'_{l-r}(a_l, r) \wedge r = v \wedge \text{liveE}(r) \\ &\Rightarrow (I'_v(a_v \setminus r) \wedge r = v) \wedge (I'_{l-r}(a_l, r) \wedge \text{liveE}(r)) \\ &\Rightarrow I'_v(a'_v) \wedge (I'_{l-r}(a_l, r) \wedge \text{liveE}(r)) \quad (* \text{ where } a'_v = a_v \{r \rightsquigarrow v\} *) \\ &\Rightarrow I'_v(a'_v) \wedge I'_l(a_l \setminus r) \\ &\Rightarrow I'_v(a'_v) \wedge I'_l(a'_l) \quad (* \text{ Since } (a_l \setminus r) \subseteq a'_l *) \\ &\Rightarrow I_l(\{a'_v, a'_l\}) \end{aligned}$$

- $\llbracket e \rrbracket_{a_v} \in \{\perp, \top\}$. Let $p = (I_v(a_v \setminus r) \wedge I'_{l-r}(a_l, r) \wedge r = -)$, then we have

$$\frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow I_l(\{a'_v, a'_l\})}{I_l(\{a_v, a_l\}) \Rightarrow p[e/r] \wedge \text{liveE}(e) \quad \vdash \{p[e/r] \wedge \text{liveE}(e)\} \langle r := e \rangle_l \{I_l(\{a'_v, a'_l\})\}} \vdash \{I_l(\{a_l, a_v\})\} \langle r := e \rangle_l \{I_l(\{a'_l, a'_v\})\}$$

- $i = \langle r := [e] \rangle_l$. We discuss the result of the $\llbracket e \rrbracket_{a_v}$.
 - there exists x , such that $\llbracket e \rrbracket_{a_v} = x$. We have $a'_v = a_v \{x \rightsquigarrow \llbracket e' \rrbracket_{a_v}\}$ and $a_l = (a'_l \cup \{r\}) \setminus (\text{fv}(e) \cup \text{Addr})$. We consider the condition that there exists v such that $\llbracket e' \rrbracket_{a_v} = v$. The condition that $\llbracket e' \rrbracket_{a_v} \in \{\perp, \top\}$ is similar. We have the following conclusion.

$$\forall x, v, a_v. (\llbracket e \rrbracket_{a_v} = x \wedge a_v(x) = v) \implies (I'_v(a_v) \Rightarrow e \mapsto v)$$

Let $p = (I'_v(a_v \setminus r) \wedge I'_{l-r}(a_l, r) \wedge r = v)$, $p_1 = ((e \mapsto v) \oplus p[v/r]) \wedge \text{liveE}(e) \wedge \text{liveX}(e)$, then we have

$$\frac{I_d(\{a_v, a_l\}) \Rightarrow p_1 \quad \frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow I_d(\{a'_v, a'_l\})}{\vdash \{p_1\} \langle r := [e] \rangle_l \{I_d(\{a'_v, a'_l\})\}}}{\vdash \{I_d(\{a_v, a_l\})\} \langle r := [e] \rangle_l \{I_d(\{a'_v, a'_l\})\}}$$

We prove the " $p \wedge \text{liveE}(r) \Rightarrow I_d(\{a'_v, a'_l\})$ " by the following steps.

$$\begin{aligned} & I'_v(a_v \setminus r) \wedge I'_{l-r}(a_l, r) \wedge r = v \wedge \text{liveE}(r) \\ \Rightarrow & (I'_v(a_v \setminus r) \wedge r = v) \wedge (I'_{l-r}(a_l, r) \wedge \text{liveE}(r)) \\ \Rightarrow & I'_v(a'_v) \wedge (I'_{l-r}(a_l, r) \wedge \text{liveE}(r)) \quad (* \text{ where } a'_v = a_v \{r \rightsquigarrow v\} *) \\ \Rightarrow & I'_v(a'_v) \wedge I'_l(a_l \setminus r) \quad (* \text{ where } (a_l \setminus r) \subseteq a'_l *) \\ \Rightarrow & I'_v(a'_v) \wedge I'_l(a'_l) \\ \Rightarrow & I_d(\{a'_v, a'_l\}) \end{aligned}$$

- $\llbracket e \rrbracket_{a_v} = \top$. We have $a'_v = a_v \{r \rightsquigarrow \top\}$ and $a_l = (a'_l \cup r) \setminus (\text{fv}(e) \cup \text{Addr})$. The condition that $\llbracket e \rrbracket_{a_v} = \perp$ is similar.

Let $p = (I'_v(a_v \setminus r) \wedge I'_{l-r}(a_l, r) \wedge r = -)$, $p_1 = ((e \mapsto -) \oplus p[-/r]) \wedge \text{liveE}(e) \wedge \text{liveX}(e)$, then we have

$$\frac{I_d(\{a_v, a_l\}) \Rightarrow p_1 \quad \frac{\text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow I_d(\{a'_v, a'_l\})}{\vdash \{p_1\} \langle r := [e] \rangle_l \{I_d(\{a'_v, a'_l\})\}}}{\vdash \{I_d(\{a_v, a_l\})\} \langle r := [e] \rangle_l \{I_d(\{a'_v, a'_l\})\}}$$

We prove the " $p \wedge \text{liveE}(r) \Rightarrow I_d(\{a'_v, a'_l\})$ " by the following steps.

$$\begin{aligned} & I'_v(a_v \setminus r) \wedge I'_{l-r}(a_l, r) \wedge r = - \wedge \text{liveE}(r) \\ \Rightarrow & (I'_v(a_v \setminus r) \wedge r = -) \wedge I'_{l-r}(a_l, r) \wedge \text{liveE}(r) \\ \Rightarrow & I'_v(a'_v) \wedge I'_{l-r}(a_l, r) \wedge \text{liveE}(r) \quad (* \text{ where } a'_v = a_v \{r \rightsquigarrow \top\} *) \\ \Rightarrow & I'_v(a'_v) \wedge I'_l(a_l \setminus r) \\ \Rightarrow & I'_v(a'_v) \wedge I'_l(a'_l) \quad (* \text{ where } (a_l \setminus r) \subseteq a'_l *) \\ \Rightarrow & I_d(\{a'_v, a'_l\}) \end{aligned}$$

- $i = \langle [e_1] := e_2 \rangle_l$. We discuss the result of the $\llbracket e \rrbracket_{a_v}$.
 - there exists x , such that $\llbracket e_1 \rrbracket_{a_v} = x$. We have $a'_v = a_v \{x \rightsquigarrow \llbracket e_2 \rrbracket_{a_v}\}$ and $a_l = (a'_l \cup \{x\}) \setminus (\text{fv}(e_1) \cup \text{fv}(e_2))$. We consider the condition that there exists v such that $\llbracket e_2 \rrbracket_{a_v} = v$. The condition that $\llbracket e'_2 \rrbracket_{a_v} \in \{\perp, \top\}$ is similar.

Let $p = I'_v(a_v \setminus x) \wedge I'_{l-x}(a_l, x) \wedge e_1 = x \wedge e_2 = v$, and $p_1 = ((e_1 \mapsto -) \oplus (e_1 \mapsto - * p) \wedge \text{liveE}(e_1, e_2))$. Then, we have

$$\frac{I_d(\{a_v, a_l\}) \Rightarrow p_1 \quad \frac{\text{ALStaE}(p, e_1) \quad (e_1 \mapsto e_2 * p) \wedge \text{liveX}(e_1) \Rightarrow I_d(\{a'_v, a'_l\})}{\vdash \{p_1\} \langle [e_1] := e_2 \rangle_l \{I_d(\{a'_v, a'_l\})\}}}{\vdash \{I_d(\{a_v, a_l\})\} \langle [e_1] := e_2 \rangle_l \{I_d(\{a'_v, a'_l\})\}}$$

We prove the " $(e_1 \mapsto e_2 * p) \wedge \text{liveX}(e) \Rightarrow I_d(\{a'_v, a'_l\})$ " by the following steps.

$$\begin{aligned}
& (e_1 \mapsto e_2 * (I'_v(a_v \setminus x) \wedge I'_{l-x}(a_l, x) \wedge e_1 = x \wedge e_2 = v)) \wedge \text{liveX}(e) \\
\Rightarrow & (I'_v(a_v \setminus x) \wedge e_1 \mapsto e_2) \wedge e_1 = x \wedge e_2 = v \wedge I'_{l-x}(a_l, x) \wedge \text{liveX}(e_1) \\
\Rightarrow & I'_v(a'_v) \wedge (e_1 = x \wedge I'_{l-x}(a_l, x)) \wedge \text{liveX}(e_1) \quad (* \text{ where } a'_v = a_v \setminus \{x \rightsquigarrow v\} *) \\
\Rightarrow & I'_v(a'_v) \wedge I'_l(a'_l \setminus x) \\
\Rightarrow & I'_v(a'_v) \wedge I'_l(a'_l) \quad (* \text{ since } (a_l \setminus x) \subseteq a'_l *) \\
\Rightarrow & I_d(\{a'_v, a'_l\})
\end{aligned}$$

– $\llbracket e_1 \rrbracket_{a_v} \in \{\perp, \top\}$. We have $a'_v = a_v \setminus \text{Addr}$ and $a_l = a'_l \setminus (\text{fv}(e_1) \cup \text{fv}(e_2))$.

Let $p = I'_v(a_v \setminus \text{Addr}) \wedge I'_{l-\text{lnA}}(a_l)$ and $p_1 = ((e_1 \mapsto -) \oplus (e_1 \mapsto - * p)) \wedge \text{liveE}(e_1, e_2)$. And we have

$$\frac{I_d(\{a_v, a_l\}) \Rightarrow p_1 \quad \frac{\text{ALStaE}(p, e_1) \quad (e_1 \mapsto e_2 * p) \wedge \text{liveX}(e_1) \Rightarrow I_d(\{a'_v, a'_l\})}{\vdash \{p_1\} \langle [e_1] := e_2 \rangle \{I_d(\{a'_v, a'_l\})\}}}{\vdash \{I_d(\{a_v, a_l\})\} \langle [e_1] := e_2 \rangle \{I_d(\{a'_v, a'_l\})\}}$$

We prove the " $(e_1 \mapsto e_2 * p) \wedge \text{liveX}(e) \Rightarrow I_d(\{a'_v, a'_l\})$ " by the following steps.

$$\begin{aligned}
& ((e_1 \mapsto e_2) * (I'_v(a_v \setminus \text{Addr}) \wedge I'_{l-\text{lnA}}(a_l))) \wedge \text{liveE}(e) \\
\Rightarrow & I'_v(a'_v) \wedge I'_{l-\text{lnA}}(a_l) \quad (* \text{ where } a'_v = a_v \setminus \text{Addr} *) \\
\Rightarrow & I'_r(a'_v) \wedge I'_l(a'_l) \quad (* \text{ Since } I'_{l-\text{lnA}}(a_l) \Rightarrow I'_l(a_l) *) \\
\Rightarrow & I'_r(a'_v) \wedge I'_l(a'_l) \quad (* \text{ Since } a_l \subseteq a'_l *) \\
\Rightarrow & I_d(\{a'_v, a'_l\})
\end{aligned}$$

□

LEMMA 6.6. $\forall C, A, C'. \text{Translater}(C, A) = C' \implies I_d, A \vdash C \sim C'$.

PROOF. We assume that the source code C is not an empty set. We prove this lemma by induction on C .

- If $C = \{l \rightsquigarrow i\}$, we have the following holds.

$$\text{Translater}(\{l \rightsquigarrow i\}, A) = C' \quad (4)$$

We unfold (4) according to the definition of the translater. We discuss each case respectively and assume $A = (\mathbb{L}_i, \mathbb{L}_o)$, $\mathbb{L}_i(l) = (a_v, a_l)$ and $\mathbb{L}_o(i) = (a'_v, a'_l)$.

– $i = \langle r := e \rangle_{l_i}$ and $r \in a'_l$. We get $C' = \{l \rightsquigarrow \langle \text{skip} \rangle_{l_i}\}$. And we need to prove the following holds.

$$I_d, A \vdash \{l \rightsquigarrow \langle r := e \rangle_{l_i}\} \sim \{l \rightsquigarrow \langle \text{skip} \rangle_{l_i}\} \quad (\text{g7-1})$$

By applying (Ins-Trans) rule on (g7-1), we get

$$I_d(\{a_v, a_l\}), I_d(\{a'_v, a'_l\}) \vdash \langle r := e \rangle_{l_i} \sim \langle \text{skip} \rangle_{l_i} \quad (\text{g7-1.1})$$

By applying (Assgn-Elim) on (g7-1.1), we need to prove that

$$I_d(\{a'_v, a'_l\}) \Rightarrow \neg \text{liveE}(r) \quad (\text{g7-1.2})$$

Since $r \in a'_l$, we prove (g7-1.2) according to the definition of I_d .

– $i = \langle r := [e] \rangle_{l_i}$ and $r \in a'_l$. We get $C' = \{l \rightsquigarrow \langle \text{skip} \rangle_{l_i}\}$. And we need to prove the following holds.

$$I_d, A \vdash \{l \rightsquigarrow \langle r := [e] \rangle_{l_i}\} \sim \{l \rightsquigarrow \langle \text{skip} \rangle_{l_i}\} \quad (\text{g7-2})$$

By applying (Ins-Trans) rule on (g7-2), we get

$$I_d(\{a_v, a_l\}), I_d(\{a'_v, a'_l\}) \vdash \langle r := [e] \rangle_{l_i} \sim \langle \text{skip} \rangle_{l_i} \quad (\text{g7-2.1})$$

By applying (Ld-Elim) on (g7-3), we need to prove that

$$I_d(\{a'_v, a'_l\}) \Rightarrow \neg \text{liveE}(r) \quad (\text{g7-2.2})$$

Since $r \in a'_l$, we prove (g7-2.2) according to the definition of I_d .

- $i = \langle [e_1] := e_2 \rangle_{l_1}$, and there exists x and v such that $x \in a'_l$ and $\llbracket e_1 \rrbracket_{a_v} = x$. We get $C' = \{l \rightsquigarrow \langle \text{skip} \rangle_{l_1}\}$. And we need to prove the followig holds.

$$I_d, A \vdash \{l \rightsquigarrow \langle [e_1] := e_2 \rangle_{l_1}\} \sim \{l \rightsquigarrow \langle \text{skip} \rangle_{l_1}\} \quad (\text{g7-3})$$

By applying (Ins-Trans) rule on (g7-3), we get

$$I_d(\{a_v, a_l\}), I_d(\{a'_v, a'_l\}) \vdash \langle [e_1] := e_2 \rangle_{l_1} \sim \langle \text{skip} \rangle_{l_1} \quad (\text{g7-3.1})$$

By applying (St-Elim) rule on (g7-3.1), we need to prove that

$$I_d(\{a'_v, a'_l\}) \Rightarrow \neg \text{liveX}(e_1) \quad (\text{g7-3.2})$$

Since $x \in a'_l$ and $\llbracket e_1 \rrbracket_{a_v} = x$, we get

$$I_d(\{a'_v, a'_l\}) \Rightarrow (e_1 = x) \wedge \neg \text{liveX}(x) \Rightarrow \neg \text{liveX}(e_1).$$

We finish the proof of (g7-3.2).

- $C' = \{l \rightsquigarrow i\}$. And we need to prove the following holds.

$$I_d, A \vdash \{l \rightsquigarrow i\} \sim \{l \rightsquigarrow i\} \quad (\text{g7-4})$$

The subgoal (g7-4) can be proved by discuss each case of the instruction i .

- If $C = \{l \rightsquigarrow i\} \uplus C_1$, we have the following holds.

$$\text{Translator}(\{l \rightsquigarrow i\} \uplus C_1, A) = C' \quad (5)$$

And the inductive hypothesis is shown below.

$$\forall A, C'_1. \text{Translator}(C_1, A) = C'_1 \implies I_d, A \vdash C_1 \sim C'_1 \quad (6)$$

We unfold (5) according to the definition of the translator. We prove by discussing each case respectively and assume $A = (\mathbb{L}_i, \mathbb{L}_o)$, $\mathbb{L}_i(l) = (a_v, a_l)$ and $\mathbb{L}_o(l) = (a'_v, a'_l)$. Here, we only show the proof of the first case. The proof of the other cases are similar to the first one.

- $i = \langle r := e \rangle_{l_1}$ and $r \in a'_l$. We get $C' = \{l \rightsquigarrow \langle \text{skip} \rangle_{l_1}\} \uplus \text{Translator}(C_1, A)$. Let $C'_1 = \text{Translator}(C_1, A)$. We need to prove the following holds.

$$I_d, A \vdash (\{l \rightsquigarrow \langle r := e \rangle_{l_1}\} \uplus C_1) \sim (\{l \rightsquigarrow \langle \text{skip} \rangle_{l_1}\} \uplus C'_1) \quad (\text{g8})$$

By applying (Link) rule on (g4), we need to prove

$$I_d, A \vdash \{l \rightsquigarrow \langle r := e \rangle_{l_1}\} \sim \{l \rightsquigarrow \langle \text{skip} \rangle_{l_1}\} \quad (\text{g8-1})$$

$$I_d, A \vdash C_1 \sim C'_1 \quad (\text{g8-2})$$

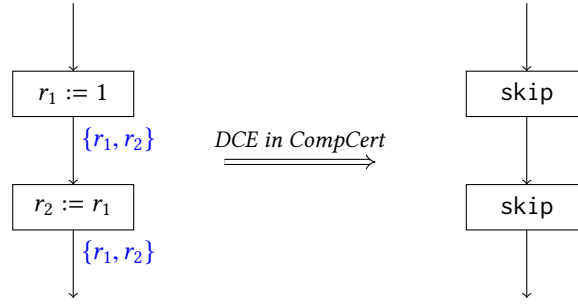
The subgoal (g8-1) can be proved by applying the (Ins-Trans) rule and (Asgn-Elim) rule, and the subgoal (g8-2) can be proved by applying the inductive hypothesis (6).

□

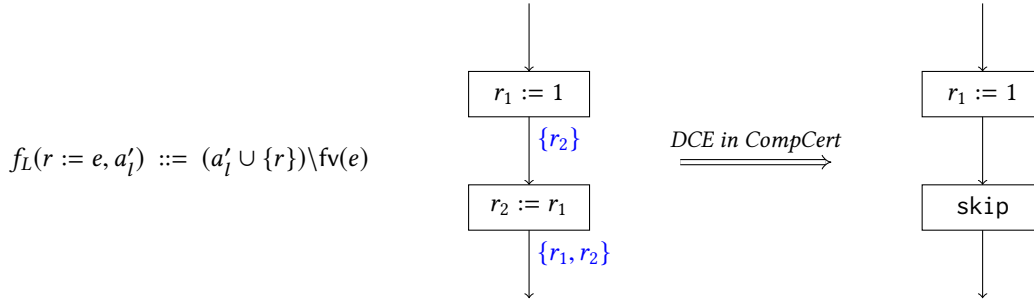
Shortcomings of the current work. The current work can not handle the correctness proof of the *dead code elimination* in CompCert. We consider the condition when meeting the instruction for the assignment ($r := e$). The transfer function (f'_L) for " $r := e$ " in CompCert is:

$$\begin{array}{c}
 \downarrow \\
 \boxed{r := e} \\
 \downarrow a'_l
 \end{array}
 \quad
 f'_L(r := e, a'_l) ::= \begin{cases} a'_l & \text{if } r \in a'_l \\ (a'_l \cup \{r\}) \setminus \text{fv}(e) & \text{otherwise} \end{cases}$$

The *dead code elimination* pass in CompCert can perform the following optimizations shown below.



However, the optimization shown above can not be performed in the *dead code elimination* that we have implemented in this subsection. The transfer function for " $r := e$ " in our work does not deal specially with the situation when $r \in a'_l$. The *dead code elimination* pass that we have implemented can only perform the following optimization.

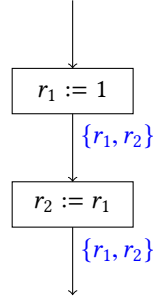


Our current work can not handle the correctness proof of the *dead code elimination* in CompCert, since the following conclusion does not hold (for simplicity, we omit the parameter a_v of I_d).

$$f'_L(\langle r := e \rangle_l, a'_l) = a_l \implies \vdash \{I_d(a_l)\} \langle r := e \rangle_l \{I_d(a'_l)\}$$

The problem here is that the following analysis result is permitted in CompCert's liveness analysis. Our (Asgn-Ac) rule can not handle such condition, since such rule requires that the register r_1 is live when starting to execute

the instruction $r_2 := r_1$.



We try to solve the problem shown above by relaxing our (Asgn-Ac) rule as the following form.

$$\frac{(p[e/r] \Rightarrow \text{liveE}(e)) \vee (p' \Rightarrow \neg \text{liveE}(r)) \quad \text{ALStaE}(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow p'}{\vdash \{p[e/r]\} \langle r := e \rangle_l \{p'\}}$$

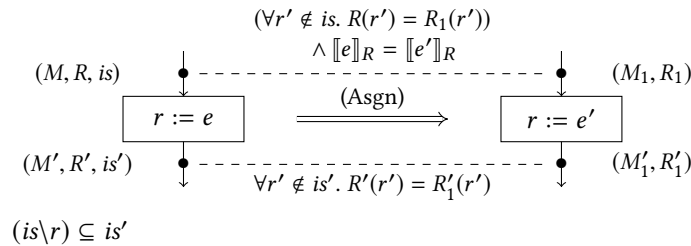
However, the relaxed (Asgn-Ac) rule is not sound. The semantics of the well-formed analysis requires that the registers in is , which is used to interpret the pre-condition, can not be read by the instruction $\langle r := e \rangle_l$. So, the condition "liveE(e)" is essential in the precondition.

The role of the not live set. We give more discussion about why we need the not live set is . The main role of is is to define the simulation relation. We define the state relation between source and target programs in Def. 5.3 and the simulation relation in Def. 5.4. We can find that we permit that the registers or memory locations, which are not in is , do not equal. So, a register r or a memory location x that is describe as "liveE(r)" or liveX(x) in assertion has the same value in the source and target program states. For example, in the (Asgn) rule, it requires that the registers in the expression e' are all live, since the registers that are live have the same values in the source and target program states. It also explains why the (Asgn-Elim) rule permits to eliminate the assignment to the register r , which is not live in the following execution.

When proving Lemma. 5.6, we find that " $is \times_R^i is'$ " can be relaxed. " $is \times_R^i is'$ " includes two restrictions: 1. the instruction i can only read the registers that are live; 2. except the register r assigned, we can not set other registers that are not live to live. We find that we only need the property (2). So, the definition of " $is \times_R^i is'$ " can be relaxed to the following form.

$$\frac{\forall r \in is. r \notin \text{updR}(i) \implies r \in is' \quad \forall x \in is. x \notin \text{stM}(i, R) \implies x \in is'}{is \times_R^i is'}$$

The following figure illustrates that the property (2) is sufficient.



From the precondition, we can prove that the value of the register r has the same value in the source and target program state. And from the property (2), we have " $(is \setminus r) \subseteq is'$ ". We know that except the register r , the registers that are not in is' are also not in is . Thus, we can prove that the after the source program executing " $r := e$ " and the target program exeucting " $r := e'$ ", the registers that are not in is' have the same values in the source and target program states.

After using the new definition of $is \bowtie is'$, we can redefine the (Asgn-Ac) rule, (Ld-Ac) rule and (St-Ac) rule as the following form.

$$\frac{ALStaE(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow p'}{\vdash \{p[e/r]\} \langle r := e \rangle_I \{p'\}} \quad (\text{Asgn-Ac})$$

$$\frac{ALStaE(p, r) \quad p \wedge \text{liveE}(r) \Rightarrow p'}{\vdash \{(e \mapsto e') \oplus p[e'/r]\} \langle r := [e] \rangle_I \{p'\}} \quad (\text{Ld-Ac})$$

$$\frac{ALStaX((e_1 \mapsto - * p), e_1) \quad (e_1 \mapsto e_2 * p) \wedge \text{liveX}(e_1) \Rightarrow p'}{\vdash \{(e_1 \mapsto -) \oplus (e_1 \mapsto - * p)\} \langle [e_1] := e_2 \rangle_I \{p'\}} \quad (\text{St-Ac})$$

Thus, the modified program logic can handle the correctness proof of the *dead code elimination* in CompCert, since we do not require that the registers and memory locations read by the current instruction are live.