## **Homework Set 2 for Module 2**

Due at the Beginning of Class (12:30 pm) on Wednesday, October 17

Name:	Class No:	Lab Div:
Signature:	Score	:/ 100

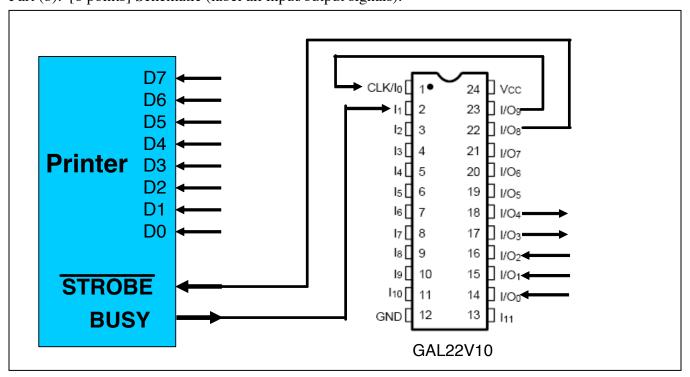
1. [20 points] An interrupt-driven printer interface is desired, but implemented with a GAL22V10 PLD instead of discrete logic. Your job is to: (a) complete the ABEL code for the PLD that implements the interface circuit, and (b) complete the circuit connections between the microcontroller, PLD, and printer. The following declarations and initialization routine are provided:

```
; Port declaration
PTT
           equ
                 $0240
                            ; Port T I/O
                 $0242
DDRT
           equ
                            ; Port T DDR
PTAD
           equ
                 $0270
                            ; Port AD I/O
                 $0272
DDRAD
           equ
                            ; Port AD DDR
; Mask declarations
imask
                 $01
                            ; printer interrupt enable mask
           equ
cmask
                 $02
                            ; device flag clear mask
           equ
smask
           equ
                 $04
                           ; STROBE mask
; Buffer declarations
                            ; buffer size
psize
           equ
                 100t
                            ; printer buffer
pbuf
           rmb
                psize
                           ; printer IN ptr
pin
           rmb
                1
           rmb
pout
                 1
                            ; printer OUT ptr
; Initialization routine
pinit
           clr
                pin
                             ; reset buffer to EMPTY condition
           clr
                pout
           movb #$FF,DDRT
                             ; initialize DDRs
                 #$07,DDRAD
           movb
           clr
                 PTT
                             ; clear all output port pins
           clr
                PTAD
           bset PTAD, cmask ; clear printer device flag
           bclr PTAD, cmask
           bset PTAD, smask ; provide initial STROBE with
           bclr PTAD, smask ; ASCII null character
           cli
                             ; enable IRQ interrupts
           rts
```

## Part (a). [12 points] ABEL file (targeted for a GAL22V10):

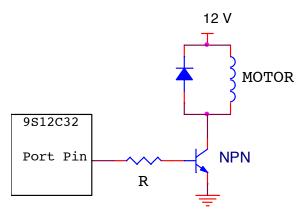
```
MODULE PrtInt
TITLE 'Printer Interrupt PLD'
CLOCK pin 1;
BUSY pin 2;
BUSYBAR pin 23 istype 'com';
STROBEBAR pin 22 istype 'com';
DEVFLAG pin 18 istype 'reg_D';
!IRQ pin 17 istype 'com';
FLAGCLR pin 16;
STROBE pin 15;
PIE pin 14;
EQUATIONS
BUSYBAR = ____;
STROBEBAR = ____;
DEVFLAG.CLK = _____;
DEVFLAG.D = ;
DEVFLAG.AR = _____;
END
```

Part (b). [8 points] Schematic (label all input/output signals):



2. [10 points] Calculate a suitable value for R, given that the motor coil requires 5 A of current to operate, that the NPN switching transistor has a  $V_{BEsat} = 0.6$  V and an  $h_{FE} = 600$ , and that the 9S12C32 port pin is programmed to operate in "full drive" mode (i.e., pin can source up to 10 mA at  $V_{OH} = 4.2$  V or sink up to 10 mA at  $V_{OL} = 0.8$  V). Determine the EIA 5% (E24) resistor value that should be used (show calculations).

Reference: <a href="http://www.daycounter.com/Calculators/Standard-Resistor-Value-Calculator.phtml">http://www.daycounter.com/Calculators/Standard-Resistor-Value-Calculator.phtml</a>



Calculation of R value:

Choice of EIA 5% (E24) resistor value:

3. [10 points] Determine the *interrupt rate* and *error range* (associated with running *slow* vs. running *fast*) for a one-second time base if RTICTL is initialized to \$5A, as well as the value of RTICNT that should be used for each case. Calculate the "wall clock" error (in minutes) that accumulates for each case after 24 hours of operation. Then answer the following questions: (a) Does either case (slow *vs.* fast) represent the *minimum error attainable* for a one-second RTI-based timer? (b) Does either case represent the *maximum error attainable* for a one-second RTI-based timer?

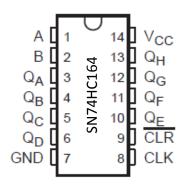
"slow" interrupt rate: ms "slow" RTICNT: "slow" error after 24 hrs: min	1
"fast" interrupt rate: ms "fast" RTICNT: "fast" error after 24 hrs: min	1
Does either case represent the minimum error attainable for a one-second RTI-based timer	?
Does either case represent the maximum error attainable for a one-second RTI-based times	:?

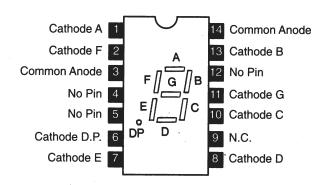
4. [20 points] Show how a 7-segment common anode LED display could be interfaced to your microcontroller module using a 74HC164 8-bit shift register. Calculate the value of current limiting resistor needed for each segment based on the D.C. characteristics of the 74HC164 (see data sheet posted on homework page) – assume the forward voltage of each LED segment is 2.1 V at a (maximum) forward current of 20 mA, and that "maximum brightness" of the display is desired. Determine the EIA 10% (E12) resistor value that should be used and complete the circuit, below.

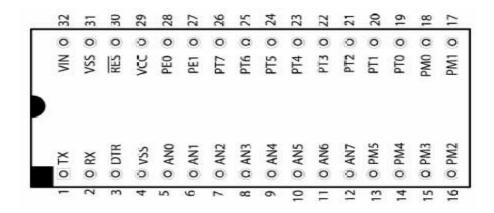
NOTE: A "0" in a shift register bit will turn the corresponding segment "on" (i.e., current must be sunk by a 74HC164 output pin to turn on the corresponding LED segment). Assume data is shifted out "DP" first, followed by segment g, segment f, etc.

Calculation of R value:

Choice of EIA 10% (E12) resistor value:







5. [20 points] Write an initialization routine that configures the microcontroller port pins used

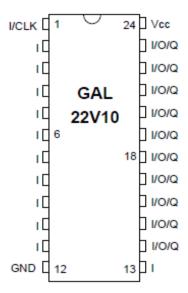
Initialization rout	ine:			
Device driver:				
Timing constrain	ts and total cycle c	count:		

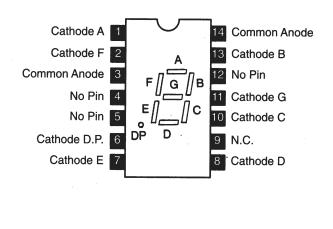
6. [20 points] Repeat Problem 4 using a GAL22V10 (programmed as shown) in place of the 74HC164. Calculate the value of current limiting resistor needed for each LED segment based on the D.C. characteristics of the GAL22V10 (see data sheet posted on homework page) – assume the forward voltage of each LED segment is 2.1 V at a (maximum) forward current of 20 mA, and that "maximum brightness" of the display is desired. Determine the EIA 10% (E12) resistor value that should be used and complete the circuit, below. *Note which of the two designs* (74HC164 vs. GAL22V10) *yields the "brightest" display*.

NOTE: A "1" in a shift register bit will turn the corresponding segment "on" (i.e., current must be sunk by a GAL22V10 output pin to turn on the corresponsing LED segment). Assume data is shifted out "DP" first, followed by segment g, segment f, etc.

Calculation of R value:

Choice of EIA 10% (E12) resistor value:





```
MODULE shiftreg

TITLE '8-bit Shift Register'

DECLARATIONS

clock pin 1;

serial_in pin 2;
!q0..!q7 pin 14..21 istype 'reg';

EQUATIONS

[q1..q7] := [q0..q6];
q0 := serial_in;
[q0..q7].clk = clock;

END
```

```
1 O TX VIN O 32
2 O RX VSS O 31
3 O DTR RES O 30
4 O VSS VCC O 29
5 O ANO PEO O 28
6 O AN1 PE1 O 27
7 O AN2 PT7 O 26
10 O AN5 PT7 O 26
11 O AN6 PT3 O 22
12 O AN7 PT2 O 21
13 O PM5 PT1 O 20
14 O PM4 PT0 O 19
15 O PM3 PM0 O 18
16 O PM2 PM1 O 17
```