start_bit_detected=0$_t$

**IDLE**
sbc_clear = 1'b0
sbc_enable = 1'b0
enable_timer = 1'b0
load_buffer = 1'b0

framing_error=1

**DATA**
sbc_clear = 1'b0
sbc_enable = 1'b0
enable_timer = 1'b0
load_buffer = 1'b1