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1. Look over the FrontPanel online documentation. How many WireIn and WireOut registers do you have at your disposal?

32 32

2. What is the bit depth for each one of these registers?

32 32

3. What is the maximum data bandwidth when using Block-Throttled Pipes?

Write: 321 MB/s (USB 3.0)

Read: 318 MB/s (USB 3.0)

4. What is the memory address range for WireIn and WireOut?

WireIn: 0x00 to 0x1F

WireOut: 0x20 to 0x3F