


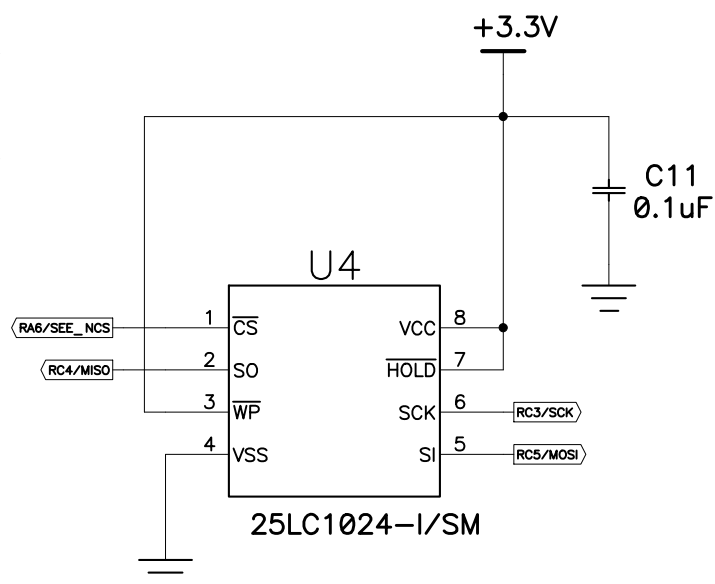
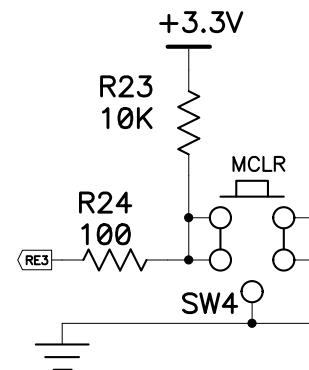
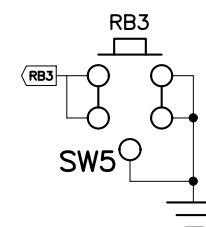
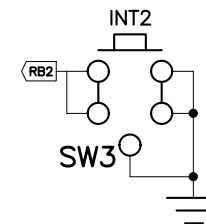
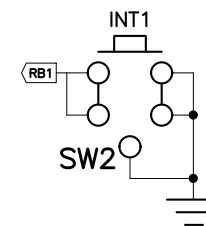
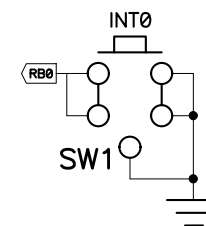
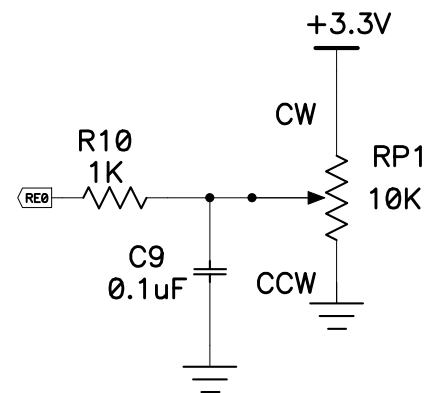
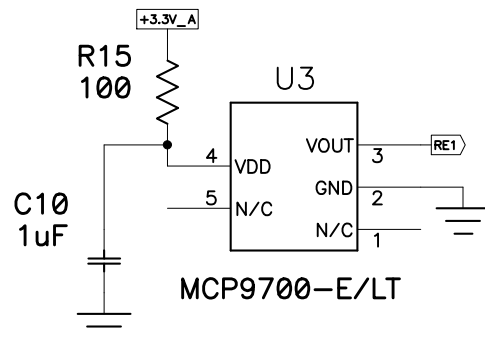
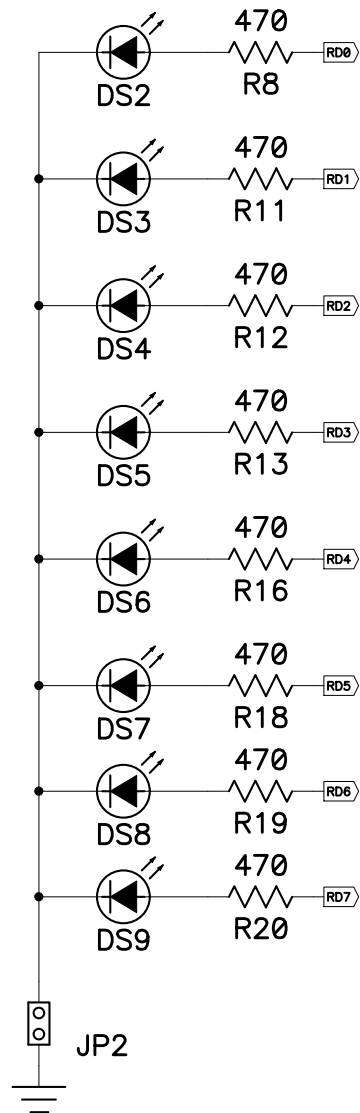
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Size A	 MICROCHIP 03-02227	Rev 1.1
		Engineer Mike Cahill
Date 8-2-2011		Drawn by Jamus Griego
Filename 03-02227r1.SCH		Sheet 1 of 5


A

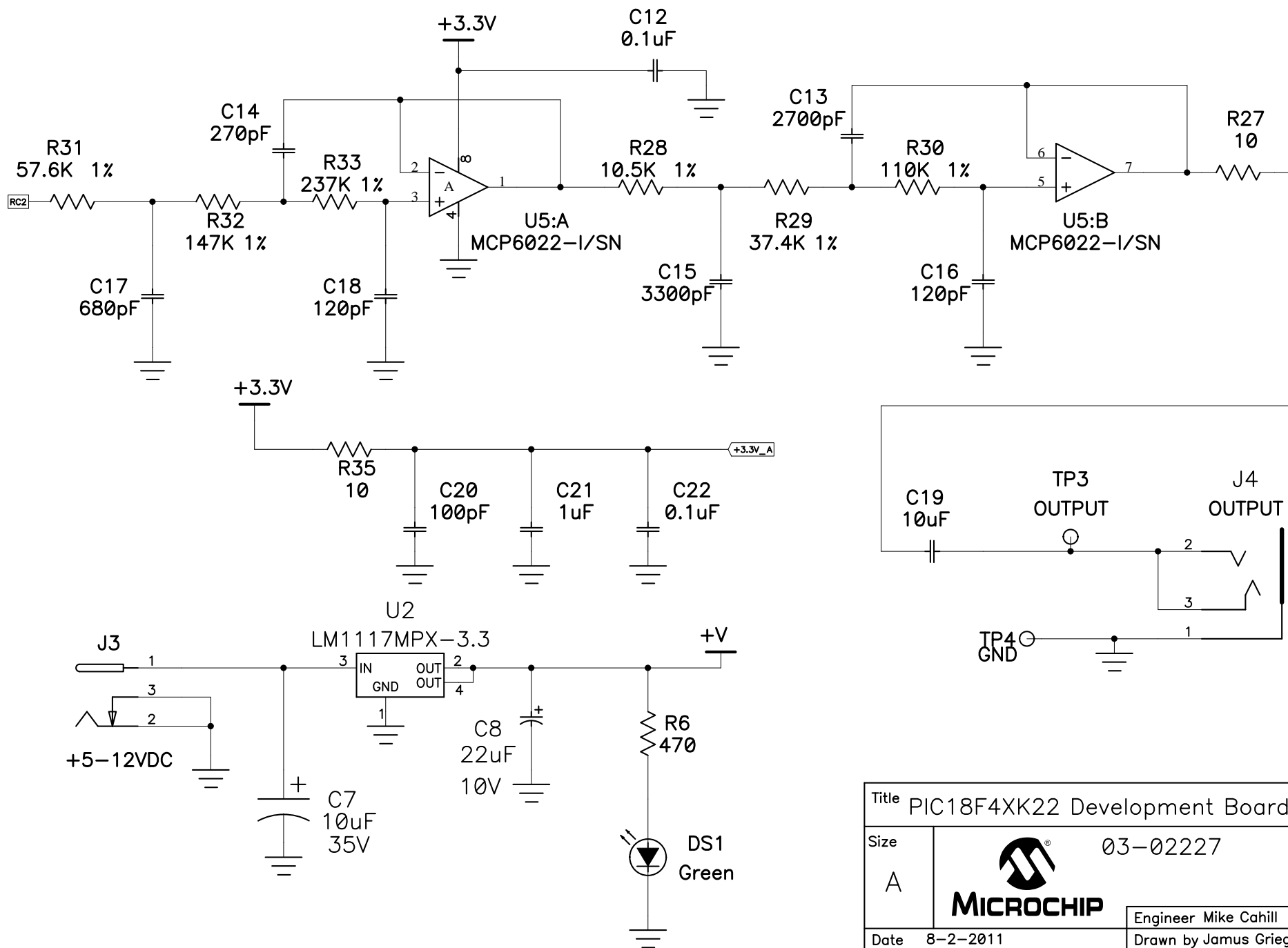
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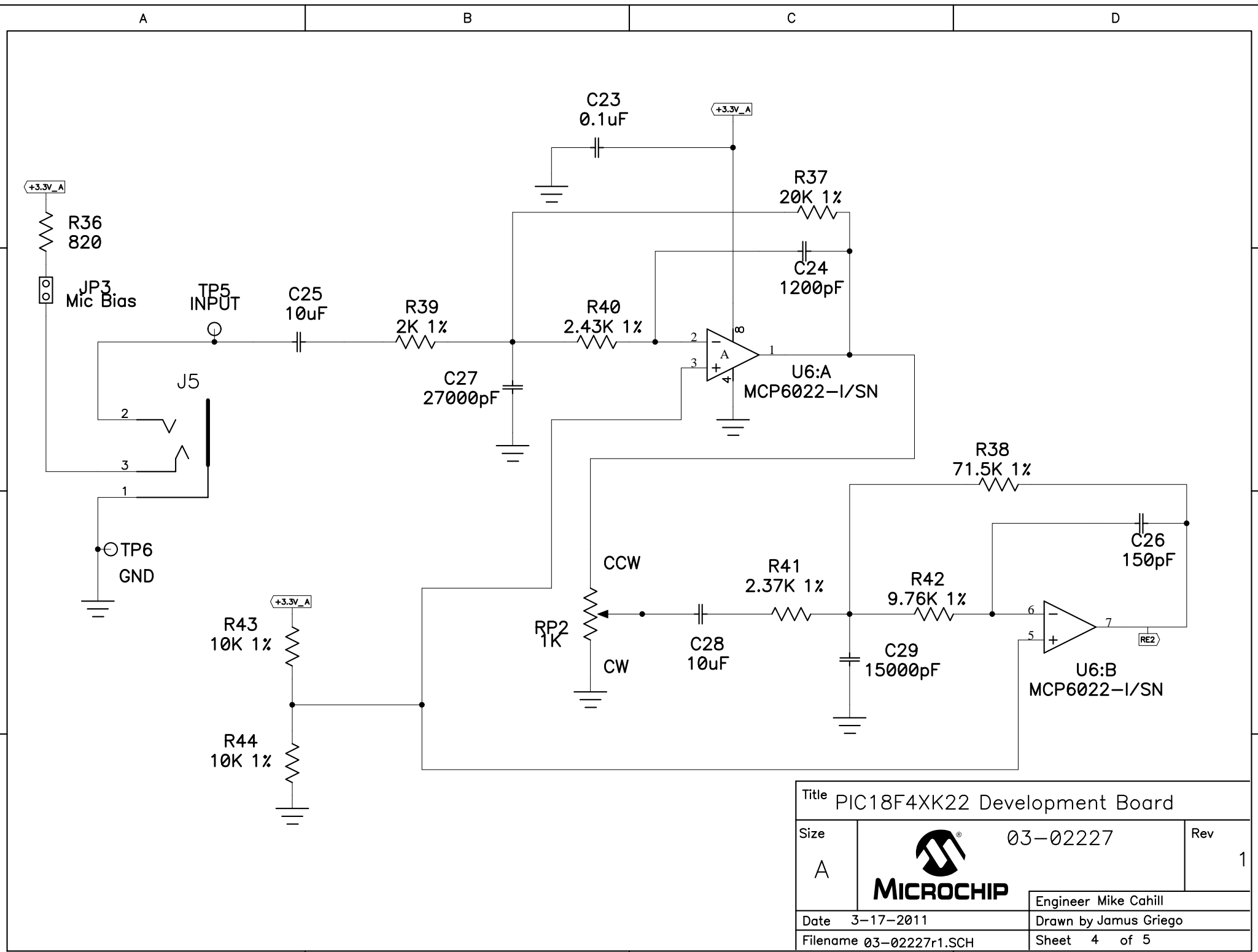
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Title PIC18F4XK22 Development Board		
Size A	 03-02227	Rev 1.1
Date 8-2-2011	Engineer Mike Cahill	
Filename 03-02227r1.SCH	Drawn by Jamus Griego	
	Sheet 2 of 5	



Title PIC18F4XK22 Development Board		
Size A	 03-02227	Rev 1.1
Date 8-2-2011	Engineer Mike Cahill	
Filename 03-02227r1.SCH	Drawn by Jamus Griego	
	Sheet 3 of 5	



Title PIC18F4XK22 Development Board		
Size A	 03-02227	Rev 1
Date 3-17-2011		Engineer Mike Cahill
Filename 03-02227r1.SCH		Drawn by Jamus Griego
		Sheet 4 of 5

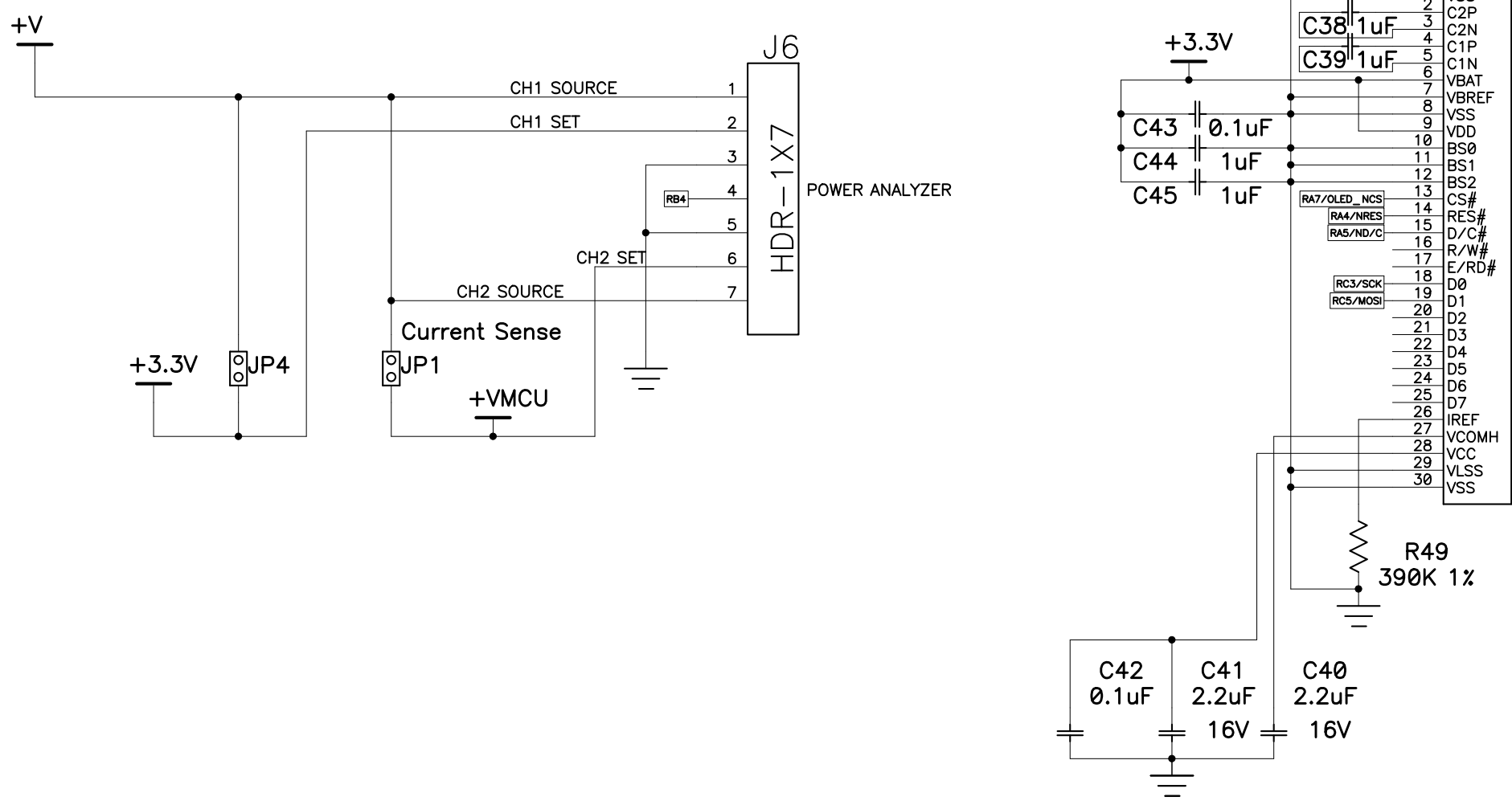
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A B C D



Title PIC18F4XK22 Development Board		
Size A	 03-02227 MICROCHIP	Rev 1.1
Date 8-2-2011		Engineer Mike Cahill
Filename 03-02227r1.SCH		Drawn by Jamus Griego
		Sheet 5 of 5

A B C D