

IN1006 Systems Architecture (PRD1 A 2022/23)

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Started on Thursday, 17 November 2022, 3:55 PM

State Finished

Completed on Thursday, 17 November 2022, 4:09 PM

Time taken 13 mins 46 secs

Grade 10.00 out of 10.00 (100%)

Question 1

Correct

Mark 1.00 out of 1.00

Which of the following equations correctly reflects the truth table shown below? A,B and C are inputs and F is the output.

A	B	C	F
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

Select one:

- ☐ a. Don't know/no answer
- ☒ b. $F = A'BC' + A'BC + AB'C' + ABC' + ABC$
- ☐ c. $F = (A'BC' + A'BC + AB'C' + ABC' + ABC)'$
- ☐ d. $F = AB'C + A'BC' + A'BC' + A'B'C + A'B'C'$
- ☐ e. None of these expressions
- ☐ f. $F = (AB'C + A'BC' + A'BC' + A'B'C + A'B'C')'$



The F output is given as a sum-of-products expression where each product (AND) should correspond to a row where F = 1.

The correct answer is: $F = A'BC' + A'BC + AB'C' + ABC' + ABC$



Mark 1.00 out of 1.00

What is the effect of a bitwise-OR operation on the following 12-bit words: 1000 1010 1101, 0110 1110 0101?

Select one:

- ☐ a. Don't know/no answer
- ☐ b. 1110 0100 1000
- ☒ c. 1110 1110 1101
- ☐ d. 0000 1100 0101
- ☐ e. 1111 0011 1010
- ☐ f. 0001 0001 0010



The OR operation is applied to each of the pairs of bits at the same position in each word, moving from left to right.

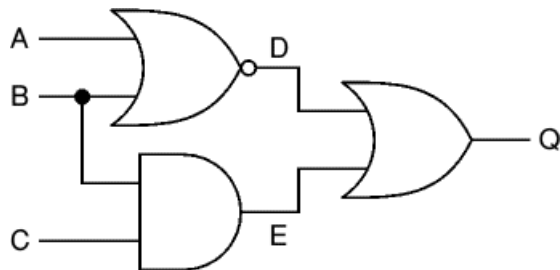
The correct answer is: 1110 1110 1101

Question 3

Correct

Mark 1.00 out of 1.00

Which of the following is the correct Boolean expression for the logic circuit below (with output Q).



Select one:

- ☐ a. Don't know/no answer
- ☐ b. $Q = (AB)' + (B+C)$
- ☒ c. $Q = (A+B)' + (BC)$
- ☐ d. $Q = (A+B)'(BC)$
- ☐ e. $Q = (A+B) + (BC)$



Output Q is OR of a NOR-gate (D) with inputs A, B and an AND-gate (E) with inputs B, C. This gives the expression:

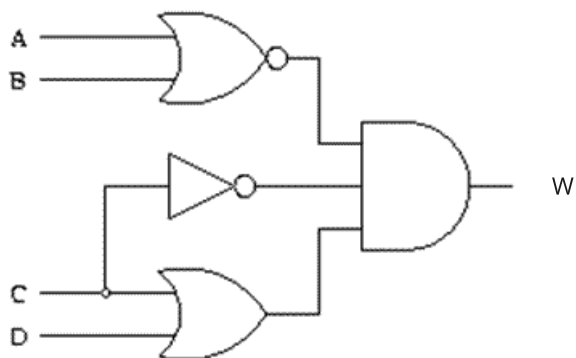
$Q = (A+B)' + (BC)$

The correct answer is: $Q = (A+B)' + (BC)$



Mark 1.00 out of 1.00

Given the logic circuit (with output W) and table below, which line of the table does **not** correspond with the behaviour of the logic circuit?



Row	A	B	C	D	Z
1	0	0	0	0	0
2	0	0	0	1	1
3	0	0	1	0	0
4	0	0	1	1	0
5	0	1	0	0	0
6	0	1	0	1	0
7	0	1	1	0	0
8	0	1	1	1	1
9	1	0	0	0	0
10	1	0	0	1	0
11	1	0	1	0	0
12	1	0	1	1	0
13	1	1	0	0	0
14	1	1	0	1	0
15	1	1	1	0	0
16	1	1	1	1	0

Select one:

- ☐ a. Row 3
- ☐ b. Row 12
- ☐ c. Row 7
- ☐ d. Don't know/no answer
- ☒ e. Row 8
- ☐ f. Row 10
- ☐ g. Row 5
- ☐ h. Row 15
- ☐ i. Row 1



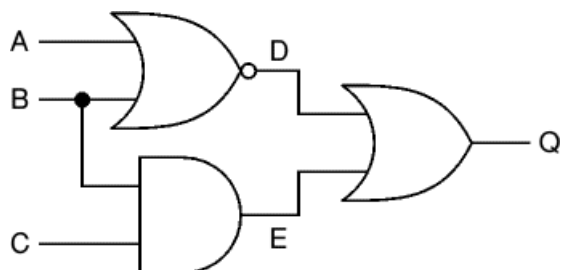
Row 8 is in error as all inputs to the AND gate must be one for W to be one, and this only occurs when the conditions in row two are met.

The correct answer is: Row 8



Mark 1.00 out of 1.00

Given the logic circuit and table below (with output Q), which line of the table does **not** correspond to the behaviour of the logic circuit?



Row	A	B	C	Q
1	0	0	0	1
2	0	0	1	1
3	0	1	0	1
4	0	1	1	1
5	1	0	0	0
6	1	0	1	0
7	1	1	0	0
8	1	1	1	1

Select one:

- ☐ a. All rows are correct
- ☒ b. Row 3
- ☐ c. Row 4
- ☐ d. Row 2
- ☐ e. Row 8
- ☐ f. Row 7
- ☐ g. Row 5
- ☐ h. Don't know/no answer
- ☐ i. Row 1
- ☐ j. Row 6




Row 3 is in error as the output of the NOR-gate (D) and AND-gate (E) are zero, leading to an output of the OR-gate (Q) of zero.
The correct answer is: Row 3

Mark 1.00 out of 1.00

Which of the following statements is *the most accurate* description for the sum-of-products expression below?

$$F = A'BC + ABC' + AB'C'$$

Select one:

- ☐ a. The truth table has four rows where $F = 1$ and no zeros need to be in the inputs to return one.
- ☐ b. The truth table has three rows where $F = 1$ and B must be one to return one.
- ☐ c. The truth table has three rows where $F = 1$ and no zeros need to be in the inputs to return one.
- ☐ d. Don't know/no answer.
- ☒ e. The truth table has three rows where $F = 1$ and at least one zero must be in the inputs to return one. 
- ☐ f. The truth table has two rows where $F = 1$ and no zeros need to be in the inputs to return one.

Your answer is correct.

The number of OR-ed terms above specifies the number of input cases that lead to a true expression (rows of truth table that give $F = 1$). Each of the inverted variables shows where the input needs to be zero for that input case.

The correct answer is: The truth table has three rows where $F = 1$ and at least one zero must be in the inputs to return one.


Question 7

Correct

Mark 1.00 out of 1.00

What is the effect of a bitwise-NAND operation on the following two 12-bit words: 1000 1010 1101, 0110 1110 0101 ?

Select one:

- ☐ a. 1110 0100 1000
- ☐ b. 0000 1100 0101
- ☐ c. Don't know/no answer
- ☐ d. 0001 0001 0010
- ☒ e. 1111 0101 1010 
- ☐ f. 1110 1110 1101

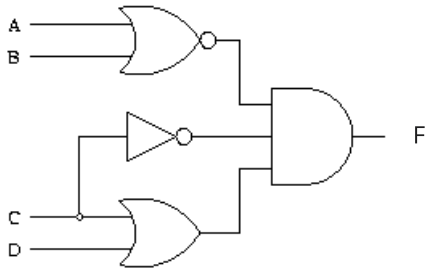
The NAND operation is applied to each of the pairs of bits at the same position in each word, moving from left to right.

The correct answer is: 1111 0101 1010



Mark 1.00 out of 1.00

Which of the following is the correct Boolean expression for the logic circuit below (with output F).



Select one:

- ☒ a. $F = (A+B)'C'(C+D)$
- ☐ b. $F = (A+B)'C'(C+D)'$
- ☐ c. $F = A+B'C'(C+D)$
- ☐ d. $F = (A+B)'C(C+D)$
- ☐ e. Don't know/no answer



The output is one if all three of its inputs are one (AND). The first of these is NOR of inputs A, B. The second NOT C and there third C OR D. This gives the expression: $F = (A+B)'C'(C+D)$

The correct answer is: $F = (A+B)'C'(C+D)$

Mark 1.00 out of 1.00

Which of the following equations correctly reflects the truth table shown below? A, B and C are inputs and F is the output.

A	B	C	F
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Select one:

- ☐ a. Don't know/no answer
- ☐ b. $F = A'B'C' + A'B'C + AB'C' + ABC'$
- ☐ c. $F = (A'B'C + A'BC' + AB'C + A'B'C + ABC)'$
- ☒ d. $F = A'B'C + A'BC' + AB'C + ABC$
- ☐ e. $F = A'B'C' + A'BC + AB'C' + ABC'$
- ☐ f. None of these expressions



The F output is given as a sum-of-products expression where each product (AND) should correspond to a row where F = 1.

The correct answer is: $F = A'B'C + A'BC' + AB'C + ABC$

Question 10

Correct

Mark 1.00 out of 1.00

What is the effect of a bitwise-NOR operation on the following two 12-bit words: 1000 1010 1101, 0110 1110 0101?

Select one:

- ☐ a. 1111 0011 1010
- ☐ b. 0000 1100 0101
- ☒ c. 0001 0001 0010
- ☐ d. 1110 0100 1000
- ☐ e. 1110 1110 1101
- ☐ f. Don't know/no answers



The NOR operation is applied to each of the pairs of bits at the same position in each word, moving from left to right.

The correct answer is: 0001 0001 0010