

IN1006 Systems Architecture (PRD1 A 2022/23)

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Started on Thursday, 10 November 2022, 4:21 PM

State Finished

Completed on Thursday, 10 November 2022, 4:36 PM

Time taken 15 mins

Grade 8.90 out of 10.00 (89%)

Question 1

Correct

Mark 1.00 out of 1.00

In performing a bit-wise addition of the following unsigned binary numbers, how many "carry out" bits will be generated?

0 1 0 0 1 0 1 1

0 0 1 0 1 0 0 1

- ☐ a. 1 "carried out" bits will be produced.
- ☐ b. 4 "carried out" bits will be produced.
- ☐ c. 0 "carried out" bits will be produced.
- ☐ d. 5 "carried out" bits will be produced.
- ☒ e. 3 "carried out" bits will be produced.

✓ Correct. The three carry out bits will be produced when adding first, second and fourth pairs of bits of the given numbers from the right.

Your answer is correct.

The correct answer is:

3 "carried out" bits will be produced.

Question 2

Correct

Mark 1.00 out of 1.00

What is the correct hexadecimal representation for the binary number 01011101? All answers below are given in hexadecimal representation and we omit the (h) subscript.

Select one:

- ☐ a. 5C
- ☐ b. D5
- ☒ c. 5D
- ☐ d. Don't know/no answer
- ☐ e. 5E
- ☐ f. 4D



The most straightforward approach is to consider the binary word four bits at a time as shown in the table.

Binary	0101	1101
Hexadecimal	5	D

The correct answer is: 5D

Question 3

Correct

Mark 1.00 out of 1.00

Which of the following numbers is the octal number representing number 20 in the decimal system (select one answer)?

- ☐ a. 44
- ☐ b. 66
- ☐ c. 10
- ☐ d. 16
- ☒ e. 24

 Correct.

Your answer is correct.

The correct answer is:

24

Question 4

Correct

Mark 1.00 out of 1.00

What are the binary and decimal representations of the hexadecimal number F4?

Select one:

- ☐ a. Binary: 11100100 Decimal: 244
- ☒ b. Binary: 11110100 Decimal: 244
- ☐ c. Don't know/No answer
- ☐ d. Binary: 11110100 Decimal: 240
- ☐ e. Binary: 11110010 Decimal: 244



Your answer is correct.

To convert from base 16, we remember that $F4_h$ means

$$F \times 16^1 + 4 \times 16^0$$

$$15 \times 16 + 4 \times 1$$

$$240 + 4$$

$$244_{10}$$

The correct answer is: Binary: 11110100 Decimal: 244

Question 5

Correct

Mark 1.00 out of 1.00

In performing a bit-wise addition of the following unsigned binary numbers, how many "carry out" bits will be generated?

1 0 0 0 1 0 1 1

0 1 1 1 0 0 0 1

- ☐ a. 1 "carried out" bits will be produced.
- ☒ b. 2 "carried out" bits will be produced.
- ☐ c. 4 "carried out" bits will be produced.
- ☐ d. 3 "carried out" bits will be produced.
- ☐ e. 0 "carried out" bits will be produced.

✓ Correct. The two carry out bits will be produced when adding two right most pairs of bits of the given numbers.

Your answer is correct.

The correct answer is:

2 "carried out" bits will be produced.

Question 6

Correct

Mark 1.00 out of 1.00

What is the numeric range of an 8-bit binary number in 2's complement arithmetic?

Select one:

- ☒ a. -128 ...127
- ☐ b. 0...7
- ☐ c. -128 ... 128
- ☐ d. -127 ... 127
- ☐ e. Don't know/no answer
- ☐ f. 0...255



We represent the negative values in the range -128 through -1 and the positive values in the range 0 through 127 with a single 8-bit byte, so -128 ... 127.

The correct answer is: -128 ...127

Question 7

Correct

Mark 3.00 out of 3.00

Which of the following binary numbers corresponds to the result of the following subtraction of hexadecimal numbers (hint: transform the hexadecimal numbers to binary and perform subtraction as addition of the 2's complement the number to be subtracted):

$$82_{\text{hex}} - 78_{\text{hex}}$$

- ☐ a. 1111 1001
- ☐ b. 0010 1001
- ☐ c. 0000 1001
- ☐ d. 1010 1010
- ☒ e. 0000 1010

✓ Correct answer.

Your answer is correct.

The binary form of 82_{hex} is: 1000 0010

The binary form of 78_{hex} is: 0111 1000

Subtracting 78_{hex} from 82_{hex} can be carried out by adding the 2's complement of 78_{hex} to 82_{hex} .

To find the 2's complement of 78_{hex} we first flip the bits of its binary representation. This gives us: 1000 0111 (flip bits)

And then we add 1, so we get:

1000 0111

+ 1

This gives us:

1000 1000 (i.e., the 2's complement of 78_{hex})

Then we perform the addition:

1000 0010 (82_{hex})

+ 1100 0111 (i.e., the 2's complement of 78_{hex})

The result of this addition is

0000 1010

and as the left most bit is 0 the number is a positive one and therefore it constitutes the answer.

The correct answer is:

0000 1010

Question 8

Incorrect

Mark -0.10 out of 1.00

What is the correct result of the operation below? The initial numbers should be considered as unsigned integers. The result should be given in 2's complement. (Hint: use 2's complement arithmetic to perform the operation.)

00001111 - 00010101

Select one:

- ☐ a. 00000101
- ☐ b. 11101010
- ☒ c. 00000110
- ☐ d. 11101011
- ☐ e. Don't know/no answer
- ☐ f. 11111010



To perform the subtraction we find the negative of the subtrahend:

00010101 (subtrahend)

11101010 (1's complement, flip one bit)

00000001 (add 1)

11101011 (2's complement of the subtrahend)

perform the addition:

00001111

11101011 +

11111010 (this is the result in 2's complement or -6 in decimal)

The correct answer is: 11111010

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