NOTE: Use of internet is permitted **only** to access software website, calculators are permitted and your answers must include worked solutions. If you require extra sheet(s) please write your name and student number at the top of each additional sheet.

<https://logic.ly/>

**Part A**

**Objective**

Understand various logic gates

|  |
| --- |
| 1. Using Logic.ly create a circuit file and label Gate Symbols, Functional Notation and truth table for the gates listed in the table below: |
| |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | | **Inputs** | | **Output(s) Truth Table** | | | | | | **A** | **B** | **AND** | **OR** | **XOR** | **NAND** | **NOR** | | **0** | **0** | 0 | 0 | 0 | 1 | 1 | | **0** | **1** | 0 | 1 | 1 | 1 | 0 | | **1** | **0** | 0 | 1 | 1 | 1 | 0 | | **1** | **1** | 1 | 0 | 0 | 0 | 1 | | **Gate**  **Symbol** | |  |  |  |  |  | | **Functional**  **Notation** | |  |  |  |  |  | |
|  |

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1. Construct the logic circuit below using Lab Logic and detail the functional notation and truth table for a two input **AND** gate.  |  |  |  |  | | --- | --- | --- | --- | |  | Switches | AND Gate | LED Output | |  | | | | |
| Functional Notation is && |

**Part B**

**Objective**

Understand the construction of Logic Gates from various combinations of logic gates

|  |
| --- |
| 1. Using Lab Software prove that combining and **AND** and a **NOT** constructs a **NAND** Gate    **NOTE:** solution must be demonstrated to Lecturer prior to leaving laboratory |
| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | Demonstrated to lecturer | |  |  | | --- | --- | | Yes | No | |  |  | | |

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 1. Prove using Lab Software and a truth table that the circuit below is a XOR Gate. Place an LED on the output of the constructed circuit.   **NOTE:** solution must be demonstrated to Lecturer prior to leaving laboratory   |  |  | | --- | --- | | **Inputs** | | | **A** | **B** | **XOR** | | **0** | **0** | 0 | | **0** | **1** | 1 | | **1** | **0** | 1 | | **1** | **1** | 0 |      |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | Demonstrated to lecturer | |  |  | | --- | --- | | Yes | No | |  |  | | |
| 1. Prove DeMorgan’s Law ¬(A Ù B) = ¬A Ú ¬B using Lab Software |
| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | Demonstrated to lecturer | |  |  | | --- | --- | | Yes | No | |  |  | | |

**Hand up this practical report at the end of session and ensure it has been checked**

|  |  |  |  |
| --- | --- | --- | --- |
| **Student Name** | **Justas Spisak** | **Student Number** | **C00238201** |
| **Date** | **24/11/21** | **Checked** |  |
| **Group** | **A / B** |  |  |