

Flow Status	Successful - Fri Dec 12 16:35:49 2025
Quartus Prime Version	20.1.0 Build 711 06/05/2020 SJ Lite Edition
Revision Name	corelet
Top-level Entity Name	corelet
Family	Cyclone IV GX
Device	EP4CGX150DF31I7AD
Timing Models	Final
Total logic elements	17,112 / 149,760 ( 11 % )
Total combinational functions	11,995 / 149,760 ( 8 % )
Dedicated logic registers	12,098 / 149,760 ( 8 % )
Total registers	12098
Total pins	458 / 508 ( 90 % )
Total virtual pins	0
Total memory bits	0 / 6,635,520 ( 0 % )
Embedded Multiplier 9-bit elements	0 / 720 ( 0 % )
Total GXB Receiver Channel PCS	0 / 8 ( 0 % )
Total GXB Receiver Channel PMA	0 / 8 ( 0 % )
Total GXB Transmitter Channel PCS	0 / 8 ( 0 % )
Total GXB Transmitter Channel PMA	0 / 8 ( 0 % )
Total PLLs	0 / 8 ( 0 % )

	<b>Fmax</b>	<b>Restricted Fmax</b>	<b>Clock Name</b>	<b>Note</b>
1	128.72 MHz	128.72 MHz	clk	

Power Analyzer Status	Successful - Fri Dec 12 16:50:21 2025
Quartus Prime Version	20.1.0 Build 711 06/05/2020 SJ Lite Edition
Revision Name	corelet
Top-level Entity Name	corelet
Family	Cyclone IV GX
Device	EP4CGX150DF31I7AD
Power Models	Final
Total Thermal Power Dissipation	340.72 mW
Core Dynamic Thermal Power Dissipation	35.16 mW
Core Static Thermal Power Dissipation	119.59 mW
I/O Thermal Power Dissipation	185.97 mW
Power Estimation Confidence	Low: user provided insufficient toggle rate data