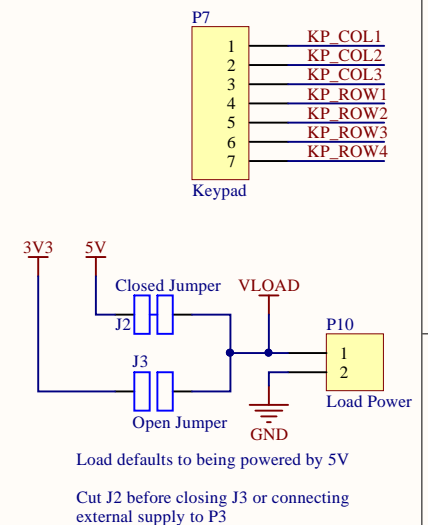
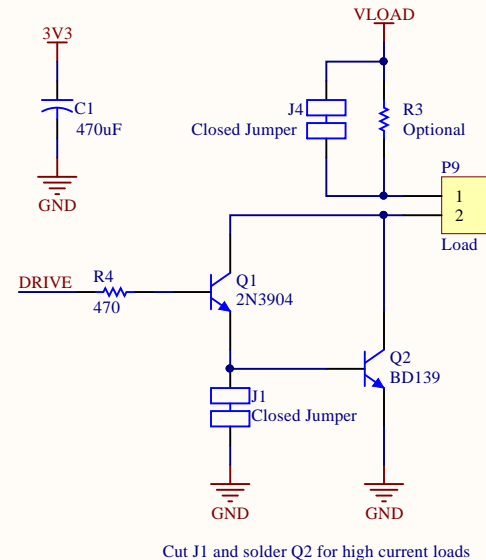
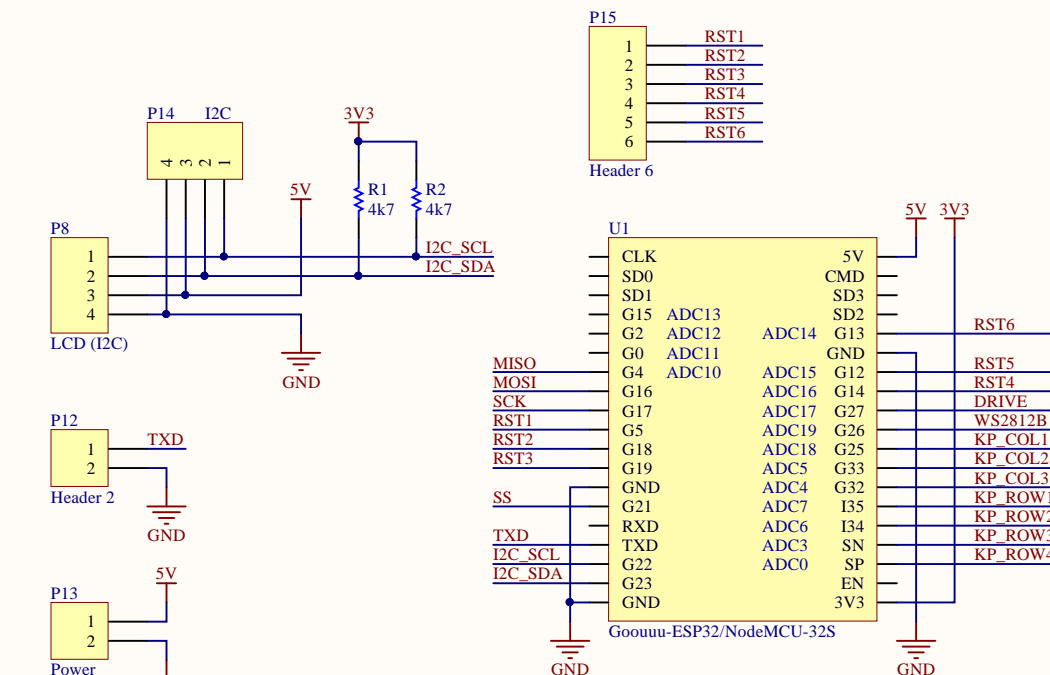
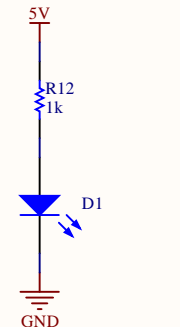


All 6 readers treated as a single SPI device
One reader is activated at a time by managing the RST pins

This is necessary for the counterfeit MFRC522 (version 0x12) which do not behave when multiple are put on the same SPI bus



Title		
Size	Number	Revision
A4		
Date:	5/1/2020	Sheet of
File:	C:\Users\...\multi-rc522.SchDoc	Drawn By:

