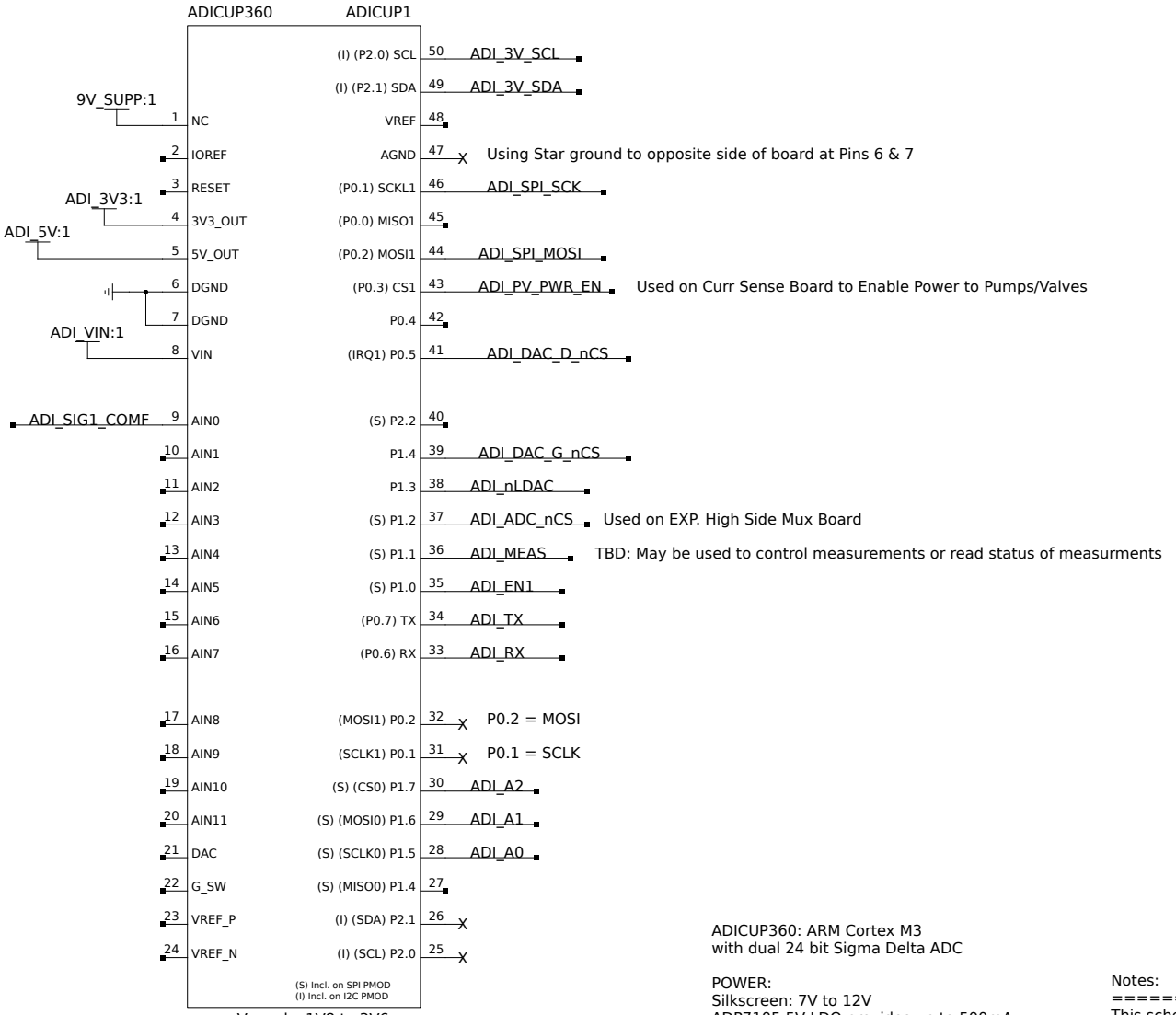
PROJECT ADICUP BREAKOUT PIN MAPPING



I2C ADDRESS SPACE
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20: MCP23017
44: SHT31
70: TCA9548
76: BME680
3C: OLED DISP
A0: EEPROM
C0: PUMP
C2: Valve1
C4: Valve2
C6: Valve3
D1: RTC

ADICUP360: ARM Cortex M3
with dual 24 bit Sigma Delta ADC

POWER:
Silkscreen: 7V to 12V
ADP7105 5V LDO provides up to 500mA
max VIN 20V

Input Voltages must
not exceed {AGND + 0.1V, AVDD - 0.1V},
note this means negative voltages such
as VDCOM/ VGCOM cannot be directly
connected to the ADICUP360.

NOTE 1: 9V_SUPP is derived from ADI_VIN
the spare NC pin on the ADICUP EVAL
board is used to distribute this fused
and reverse polarity protected supply
to other daughter boards. If high current is
needed then use a wired connection.

Notes:
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This schematic page at the project level is referenced
by multiple sub-boards at the design level. The breakout
represents a common PIN mapping at the edges of all
of these stackable boards.

TITLE ADICUP Stackable Common Pin Mapping V0_1

FILE:

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DRAWN BY: NM