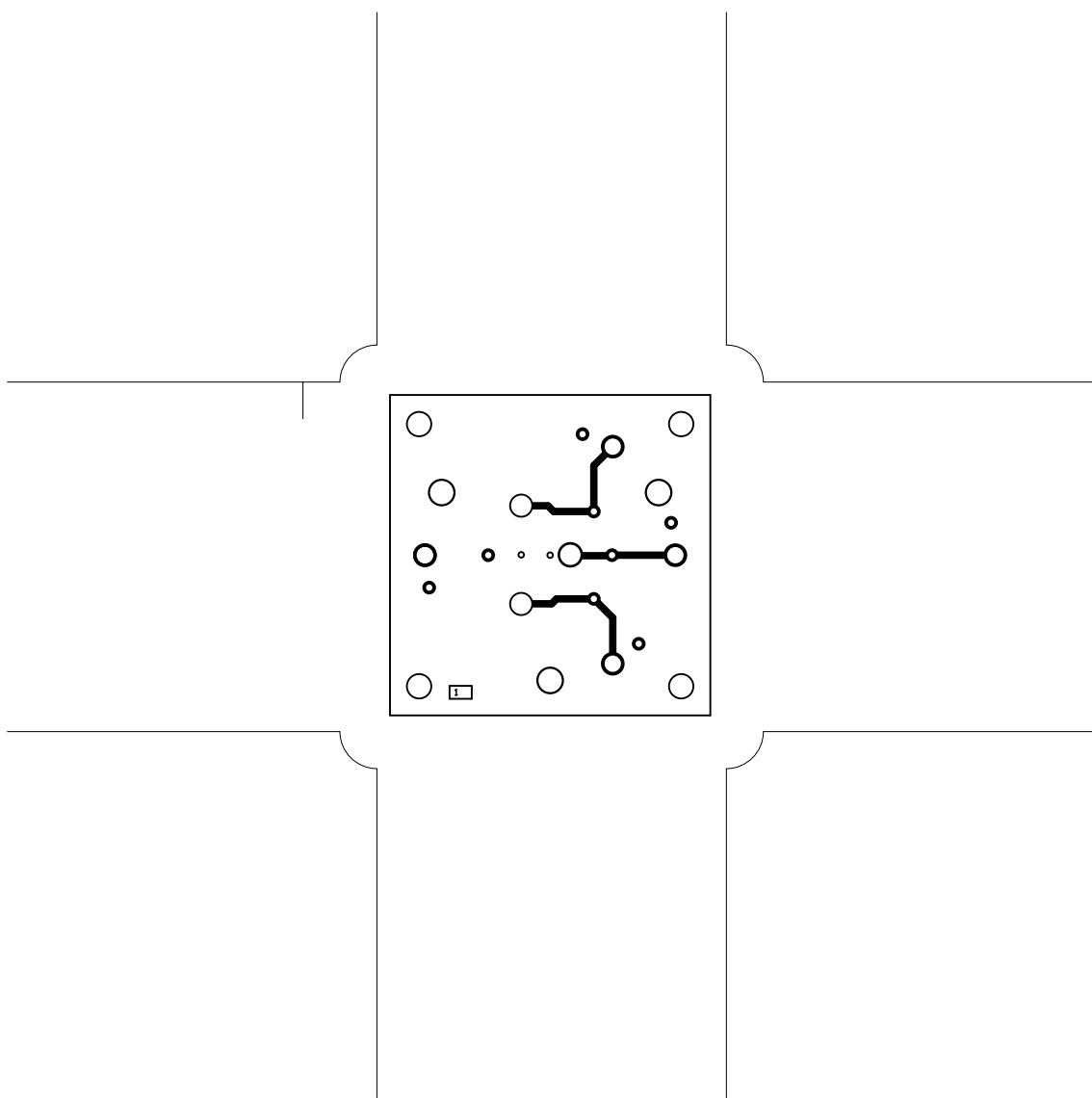
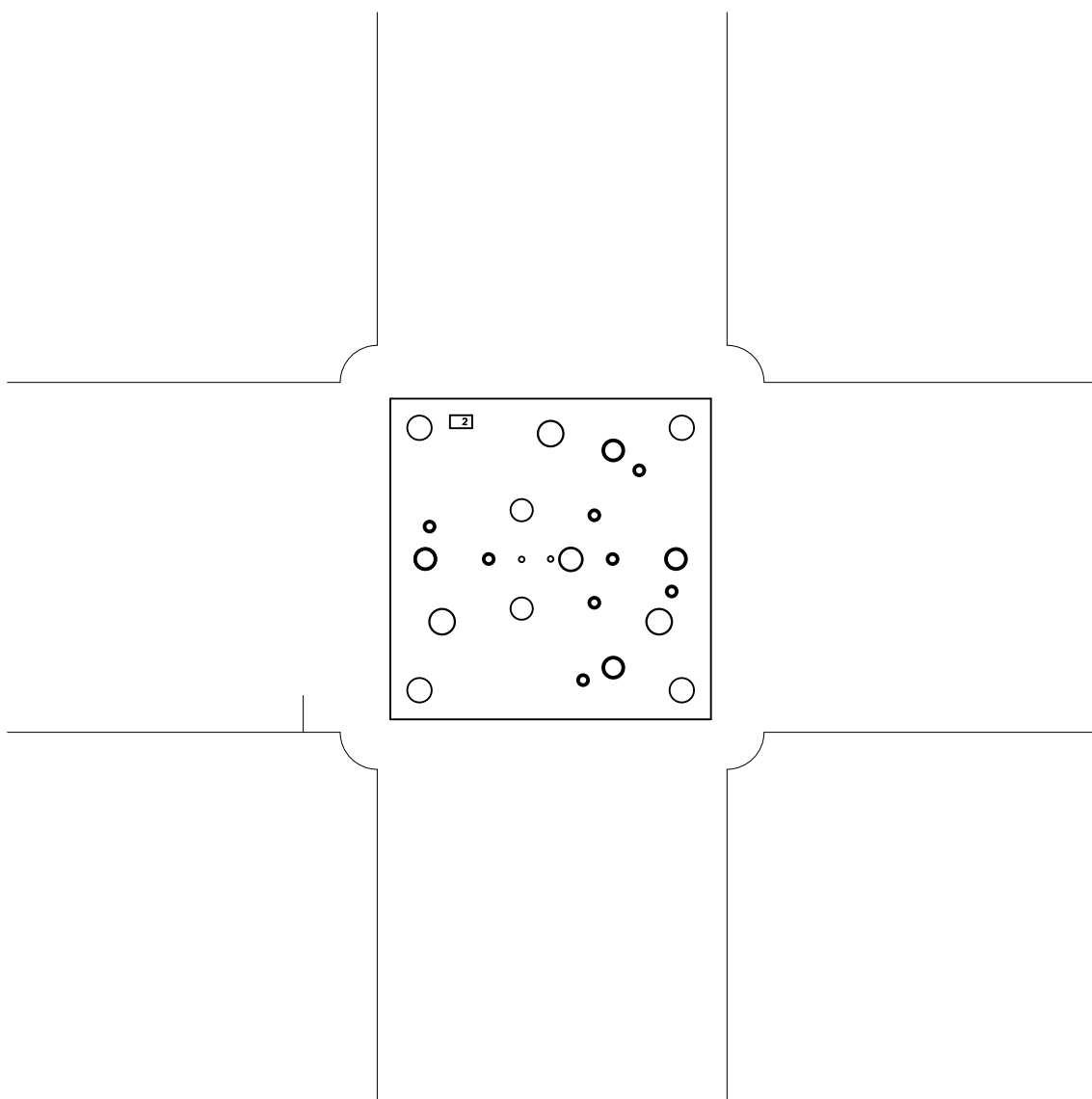


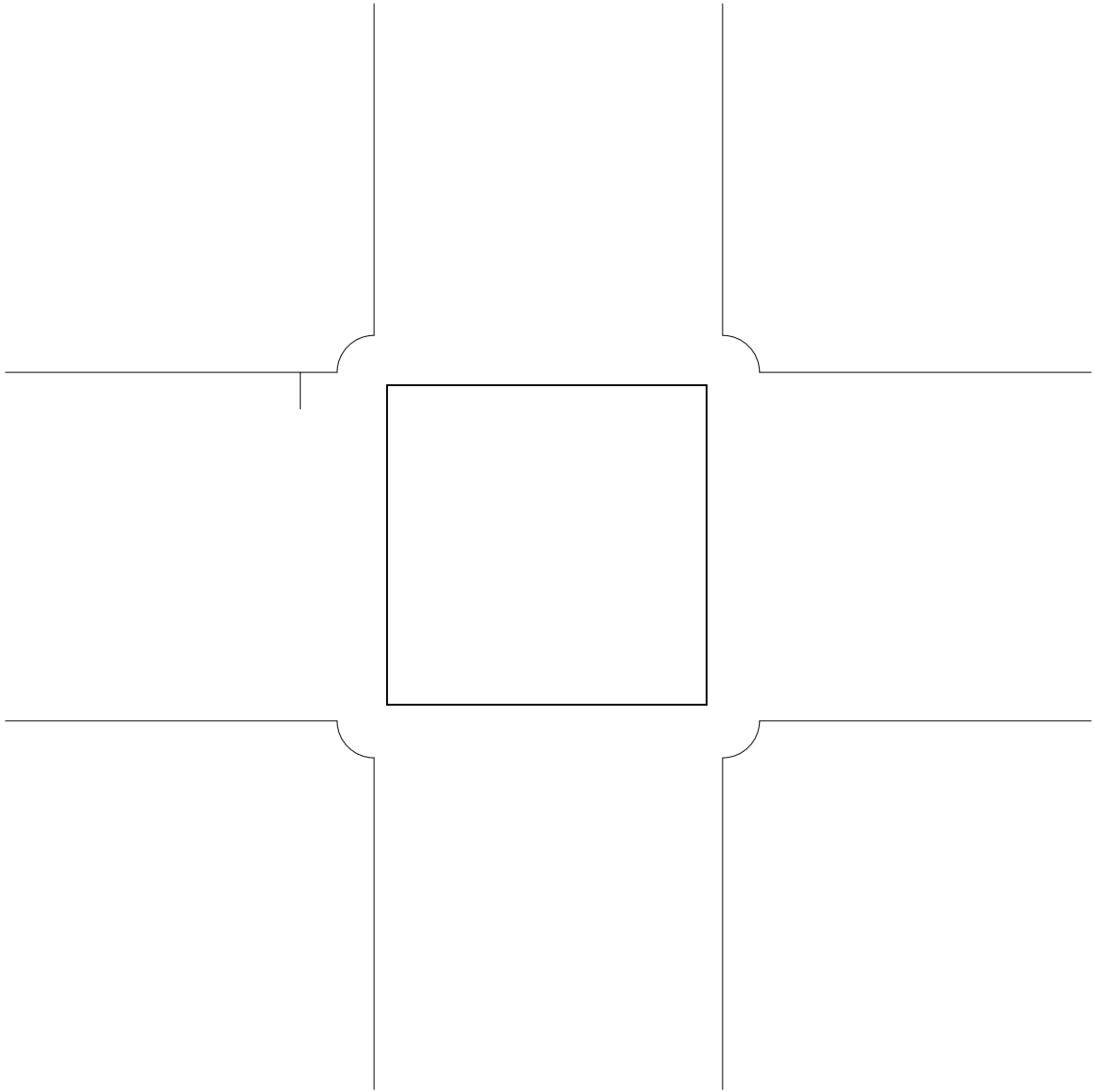
1. Table of Contents (This Page)

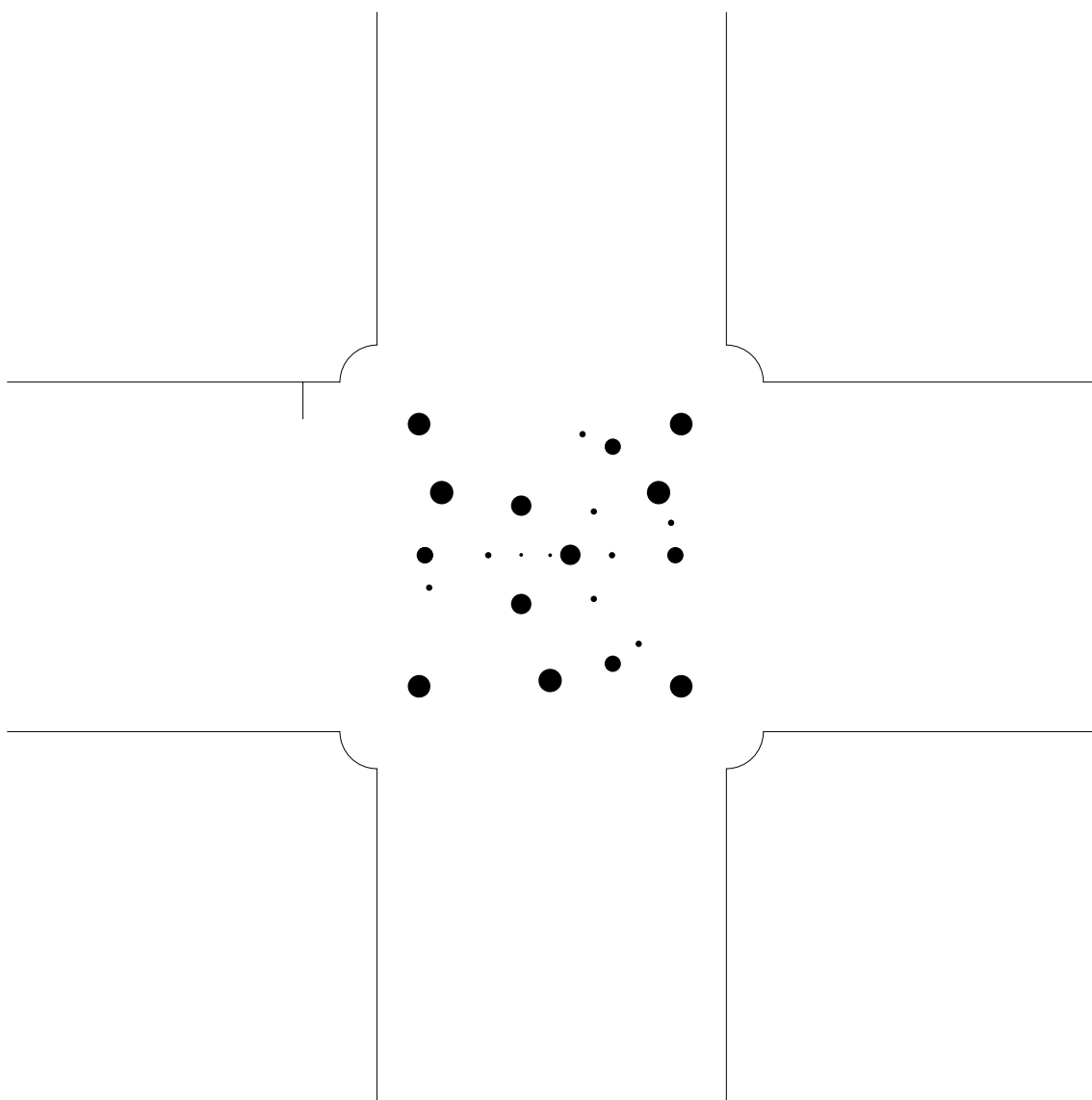
Created on Thu Aug 6 12:51:05 2020

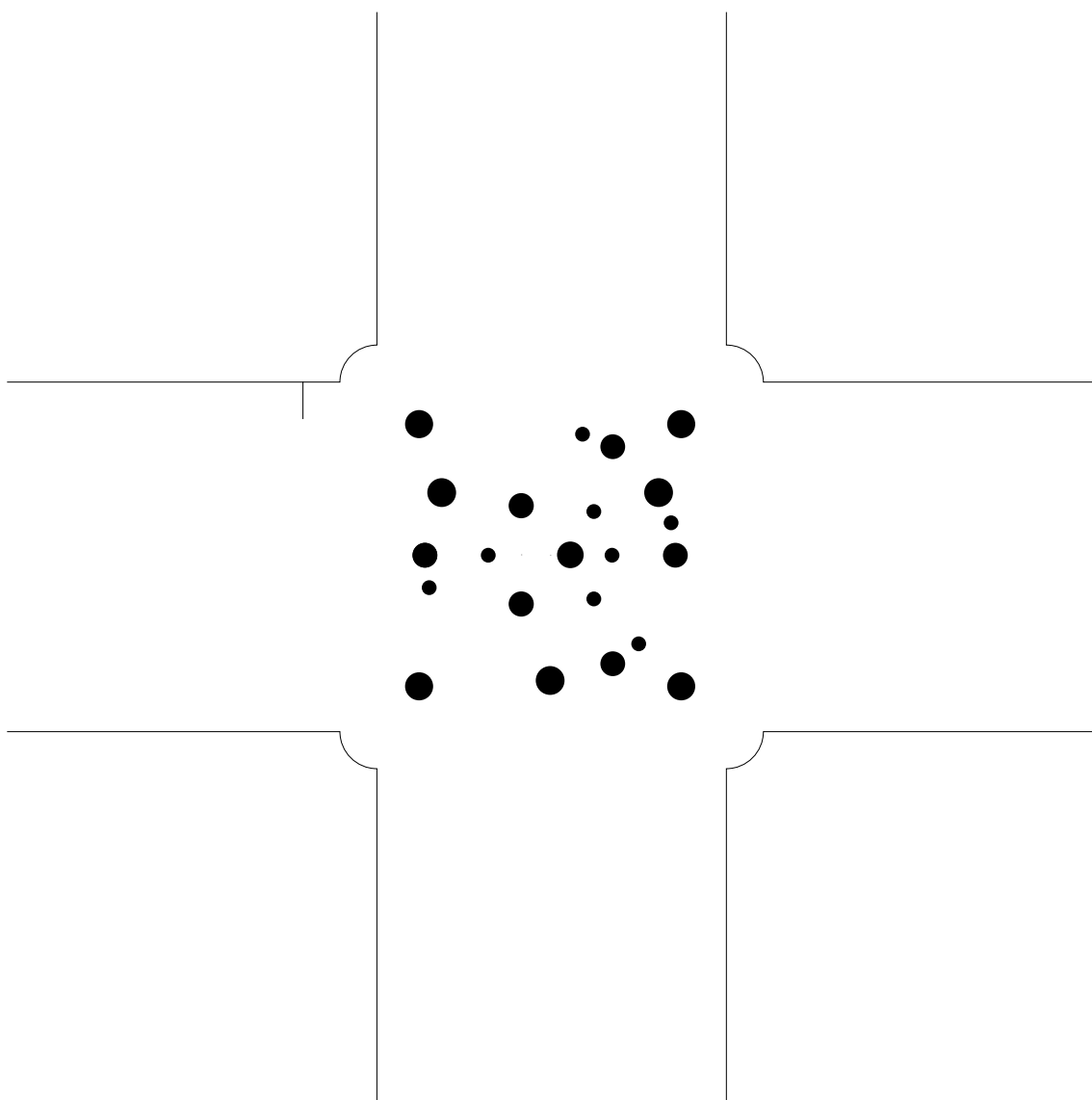
2. component
3. solder
4. outline
5. plated-drill
6. componentmask
7. soldermask
8. topsilk
9. bottomsilk
10. topassembly
11. bottomassembly
12. fab

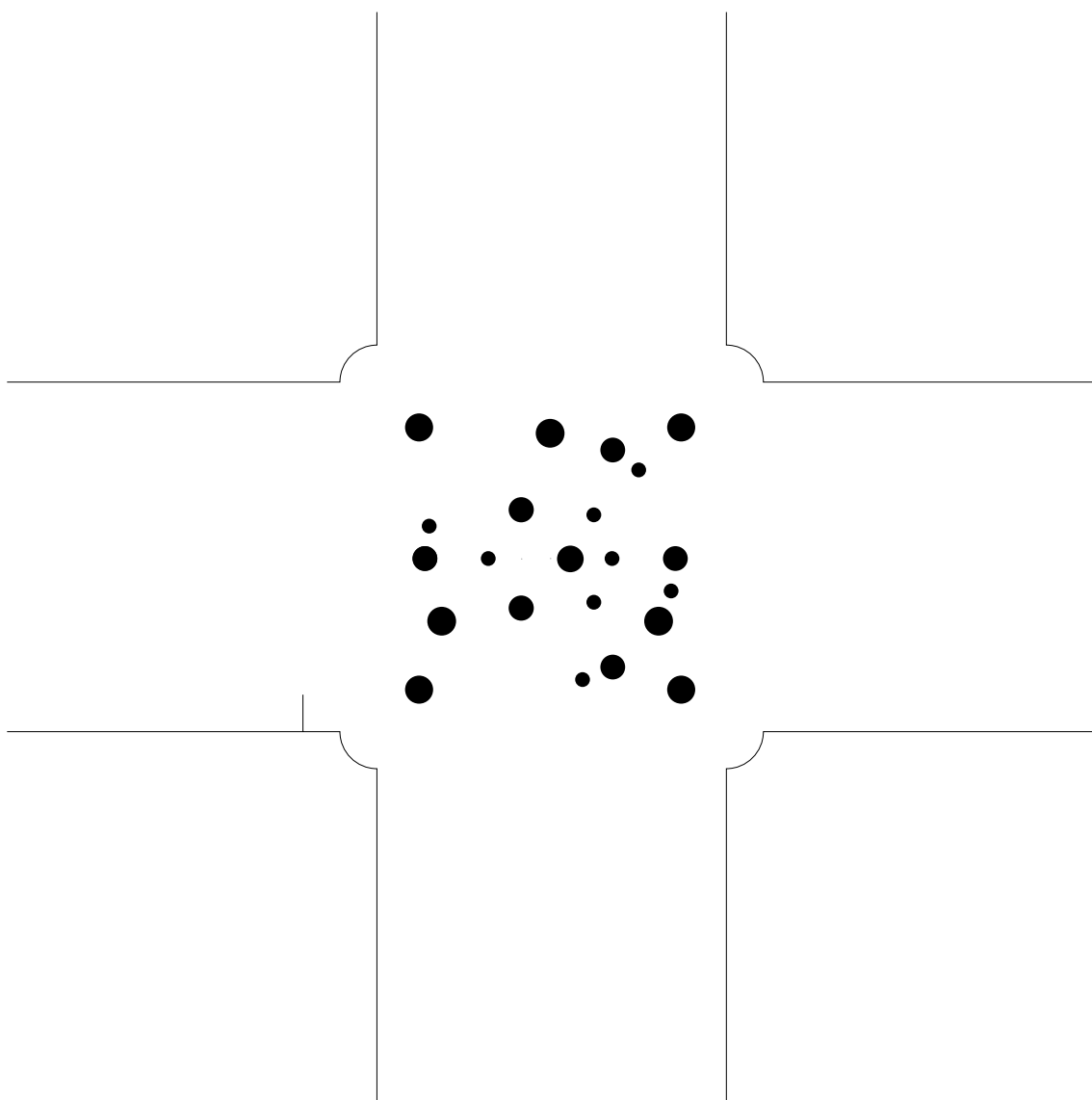


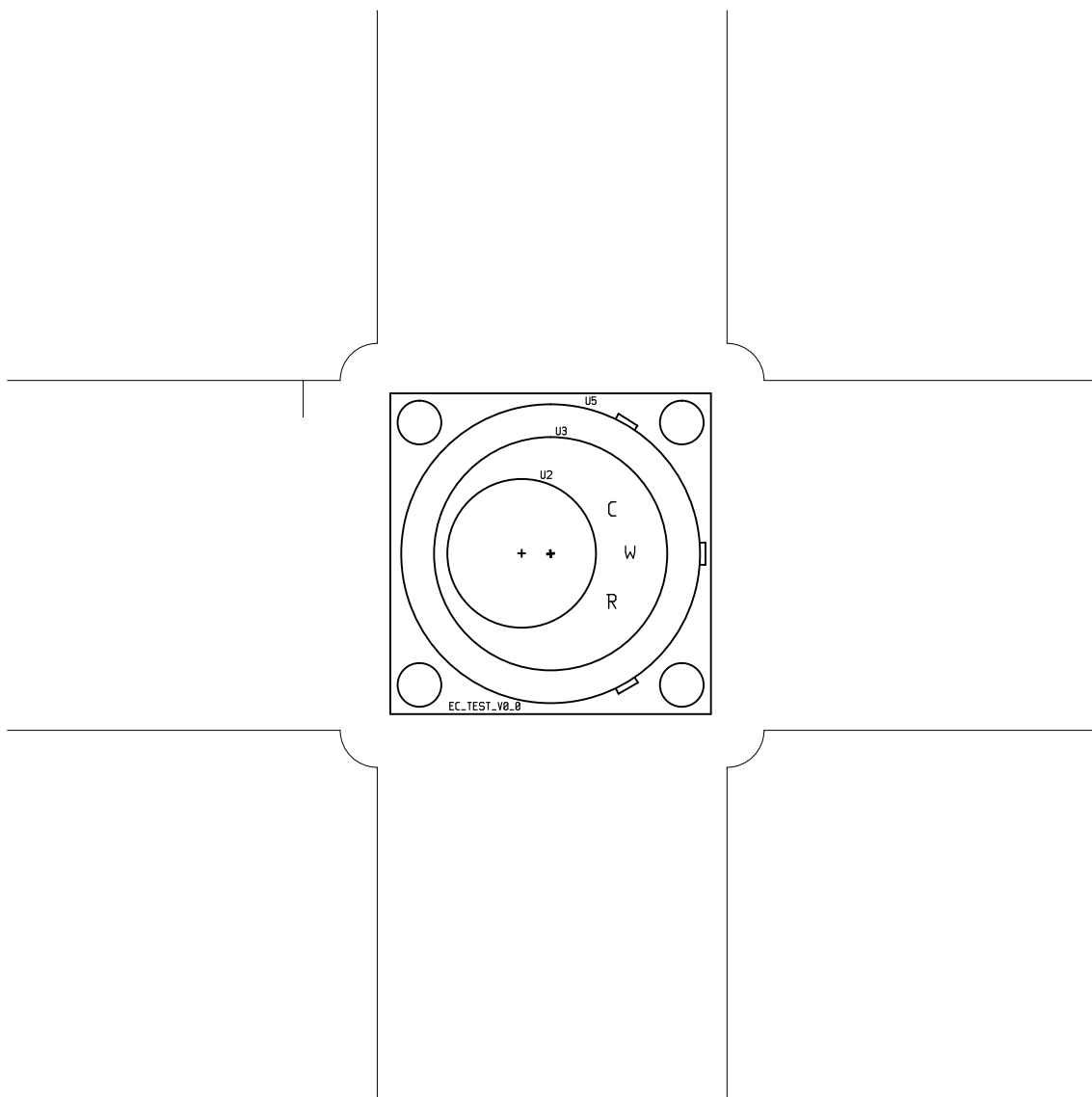


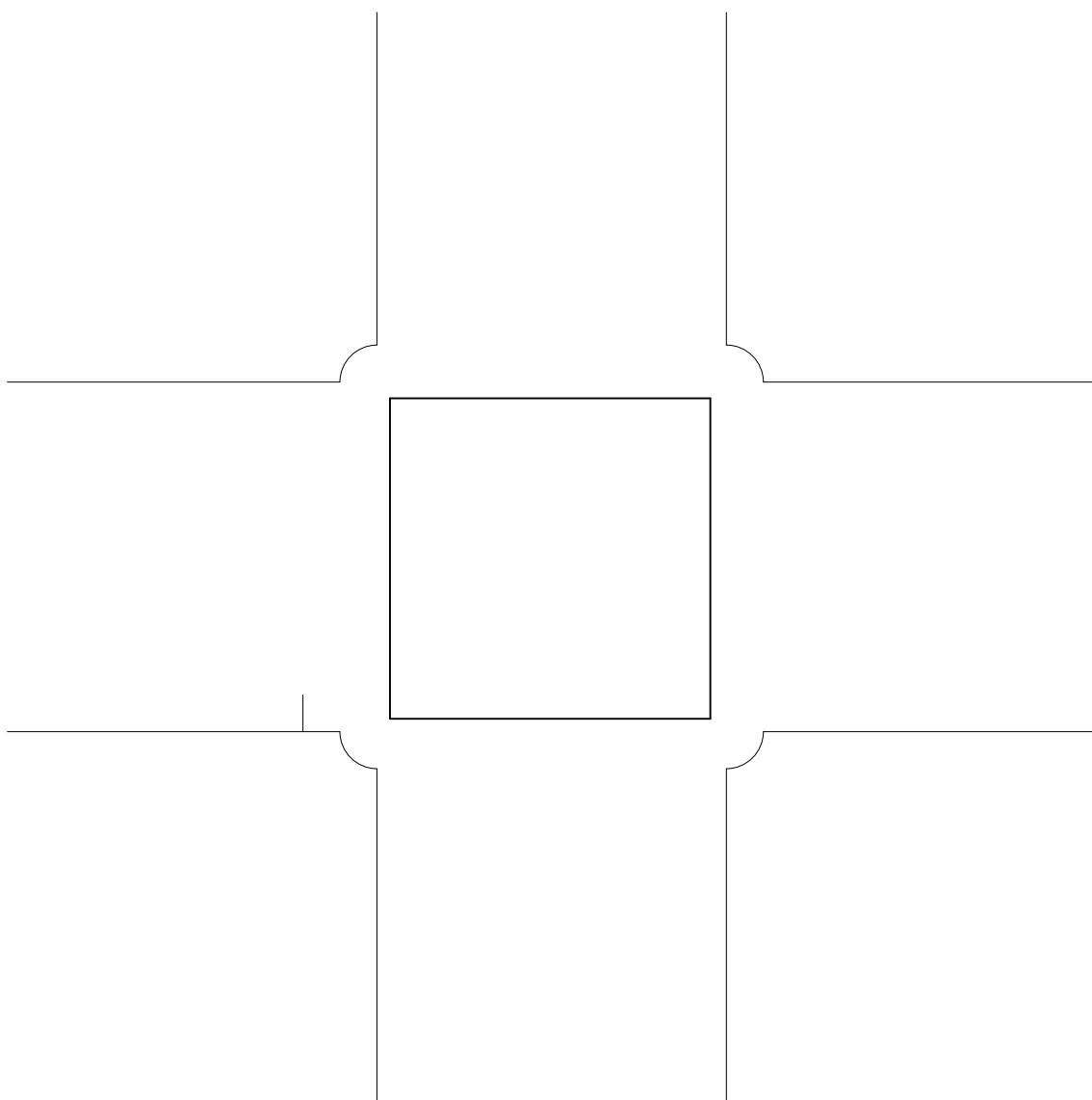


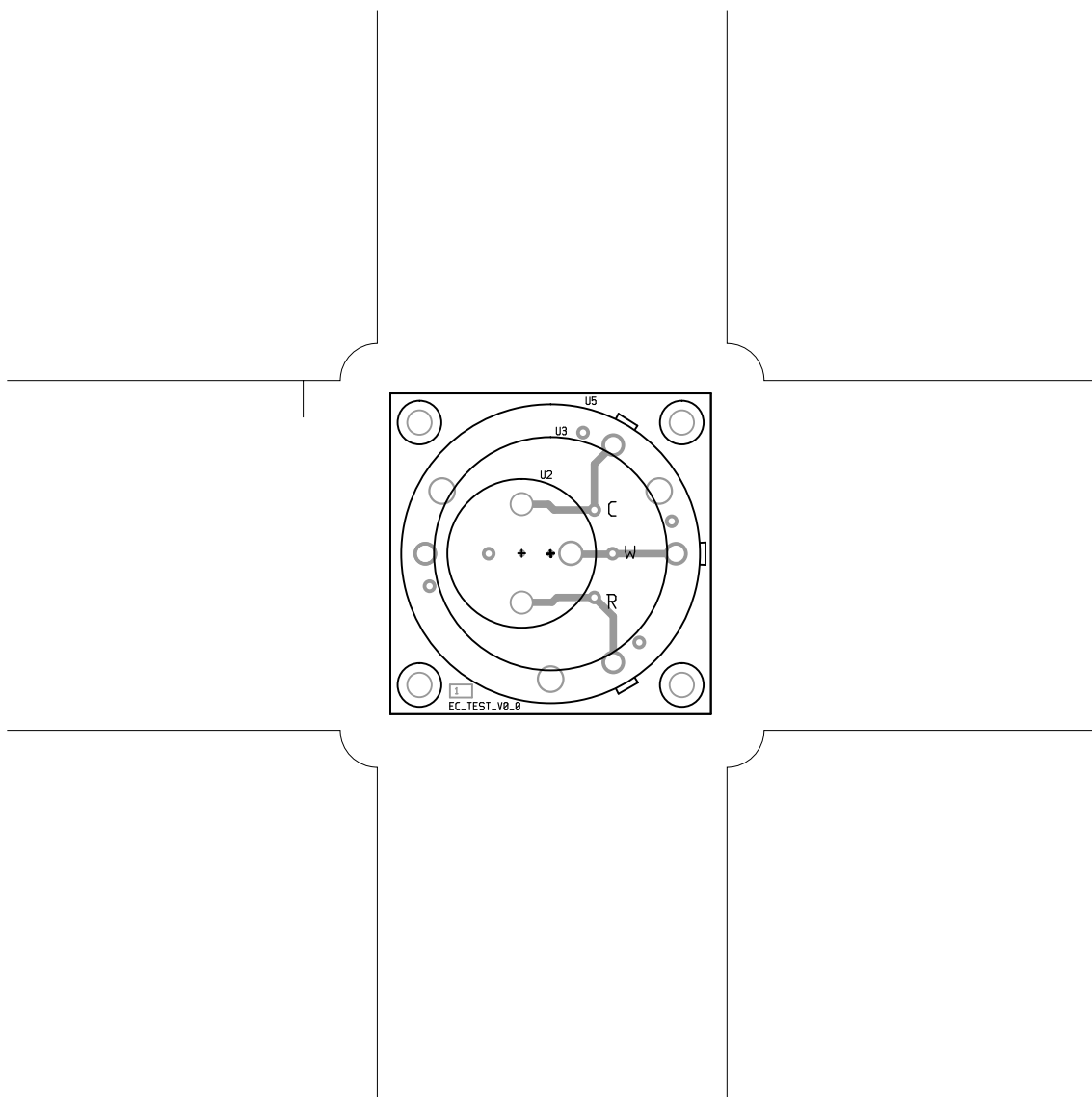


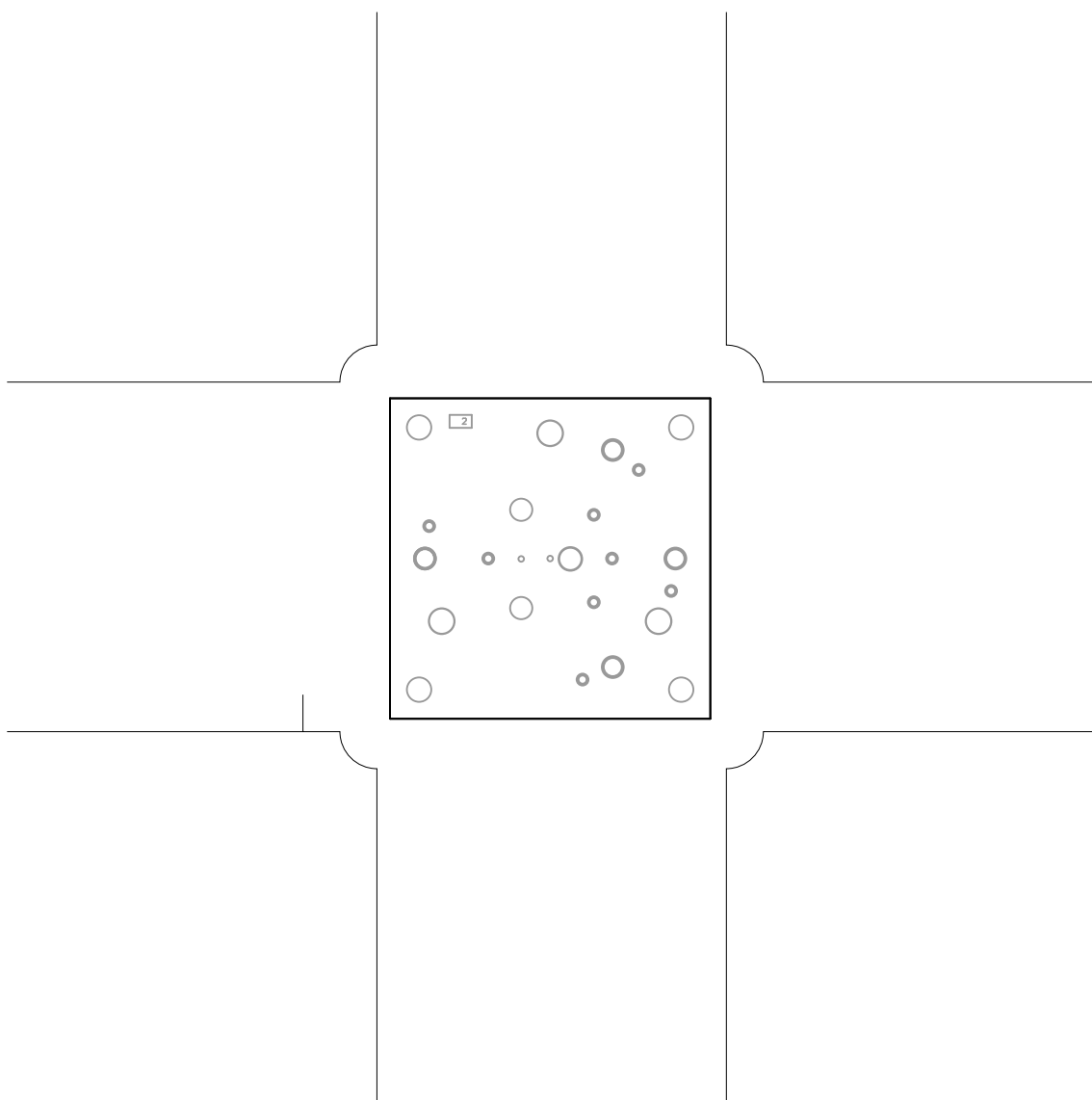








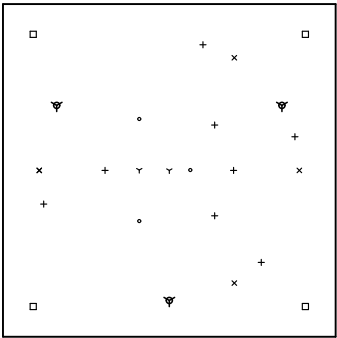




There are 6 different drill sizes used in this layout, 25 holes total

Symbol	Diam. (Inch)	Count	Plated?
⌞	0.020	2	YES
+	0.034	8	YES
×	0.088	5	YES
•	0.110	3	YES
◻	0.122	4	YES
▽	0.126	3	YES

Title: EC_TEST_V0_0 - Fabrication Drawing
Author: niallmac
Date: Thu Aug 6 11:51:05 2020 UTC



Board outline is the centerline of this path