DRAIN CURRENT MEASUREMENT CHS 1-8 C18 [100p (NPO) C36 [100p (NPO) 100p (NPO) | 100p (NPO) TP9 U18 U22 \Diamond D1 D7 **→** тр28 300k 300k 300k R22 1M C19 C25 C31 C37 100n 100n 100n C14 100n 100n 100n TP10 U11 U15 U10 | U14 U18 8 U22 8 LT1167 LT1167 LT1167 LT1167 5k49 5k49 5k49 5k49 LT1464 LT1464 LT1464 LT146 C13 C39 C41 C43 100n 100n 100n C20 C26 C32 C38 -Vcc -Vcc -Vcc -Vcc -Vcc -Vcc -Vcc -Vcc 100n 100n 100n 100n C21 __| 100p (NPO) C15 [100p (NPO) C33 [100p (NPO) 100p (NPO) R20 R26 D2 D4 D6 D8 LT146 300k 300k C16 C34 C22 100n C28 100n 100n 100n U9 U13 R33 R15 R21 R27 LT1167 LT1167 LT1167 LT1167 5k49 5k49 5k49 5k49 C17 C23 C29 C35 -Vcc -Vcc -Vcc -Vcc LT1464: Dual Opamp; unity gain stable with C-Load up to 10nF Testpoints are included on D1..8 to facilitate checking Output 1M: 1V/uA signal. Rsense resistor is inside the feedback loop to compensate Vsense drop. current measurement calibration. VDx 1M: Pulls Drain voltage to zero when IN+ is not being driven. LT1167: Instrumentation amplifier with RG=5k49 gain = 10. MULTIPLEXERS: Gate Bias / Drain Bias/ Current Sense MUX_CTRL U8 U5 U27 A1 16 ADI_A1 A1 16 ADI_A1 C50 A1 16 ADLA1 C49_ DECODE A2 15 ADI_A2 ADI_EN1 2 EN ADI_EN1 2 EN DECODE A2 15 ADLA2 ADI_EN1 2 EN DECODE A2 15 ADI_A2 100n GND 14 GND 14 GND 14 V+ 13 VD1 G1 4 51 SIG1 4 VD2 55 12 VD5 G2 5 52 55 12 G5 SIG2 5 55 12 SIG5 C47 100n VD3 6 53 56 11 VD6 G3 6 53 56 11 G6 SIG3 6 53 SIG6 C46 100n G4 7 54 57 10 VD7 57 10 SIG7 VD4 7 54 57 10 G7 SIG4 7 54 VG_COM 8 D VD_COM 8 58 9 VD8 58 9 G8 _ SIG1_COM 8 [58 9 SIG8 G2 G5 G7 G8 G3 G4 G6 Gate pull-downs These set the gate bias to zero CDT CONFIDENTIAL FOR INTERNAL USE ONLY

TITLE Current Sense - V0_1

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