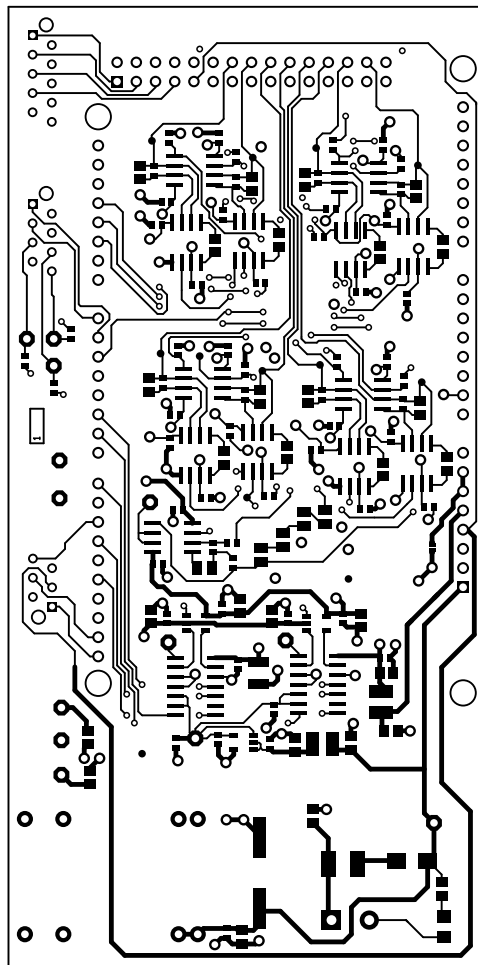
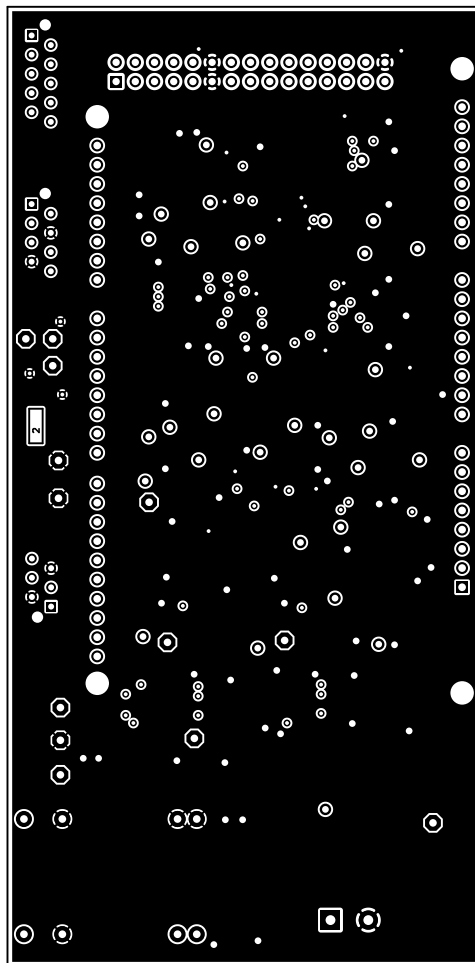


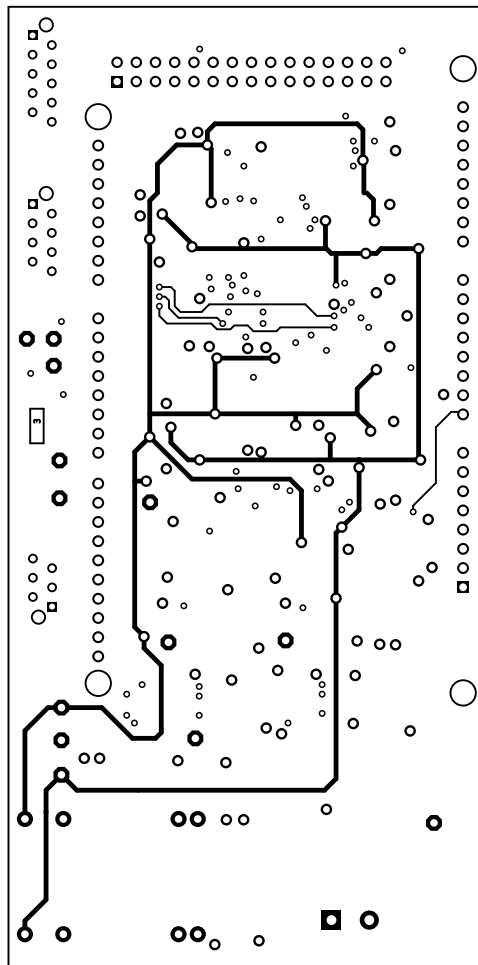
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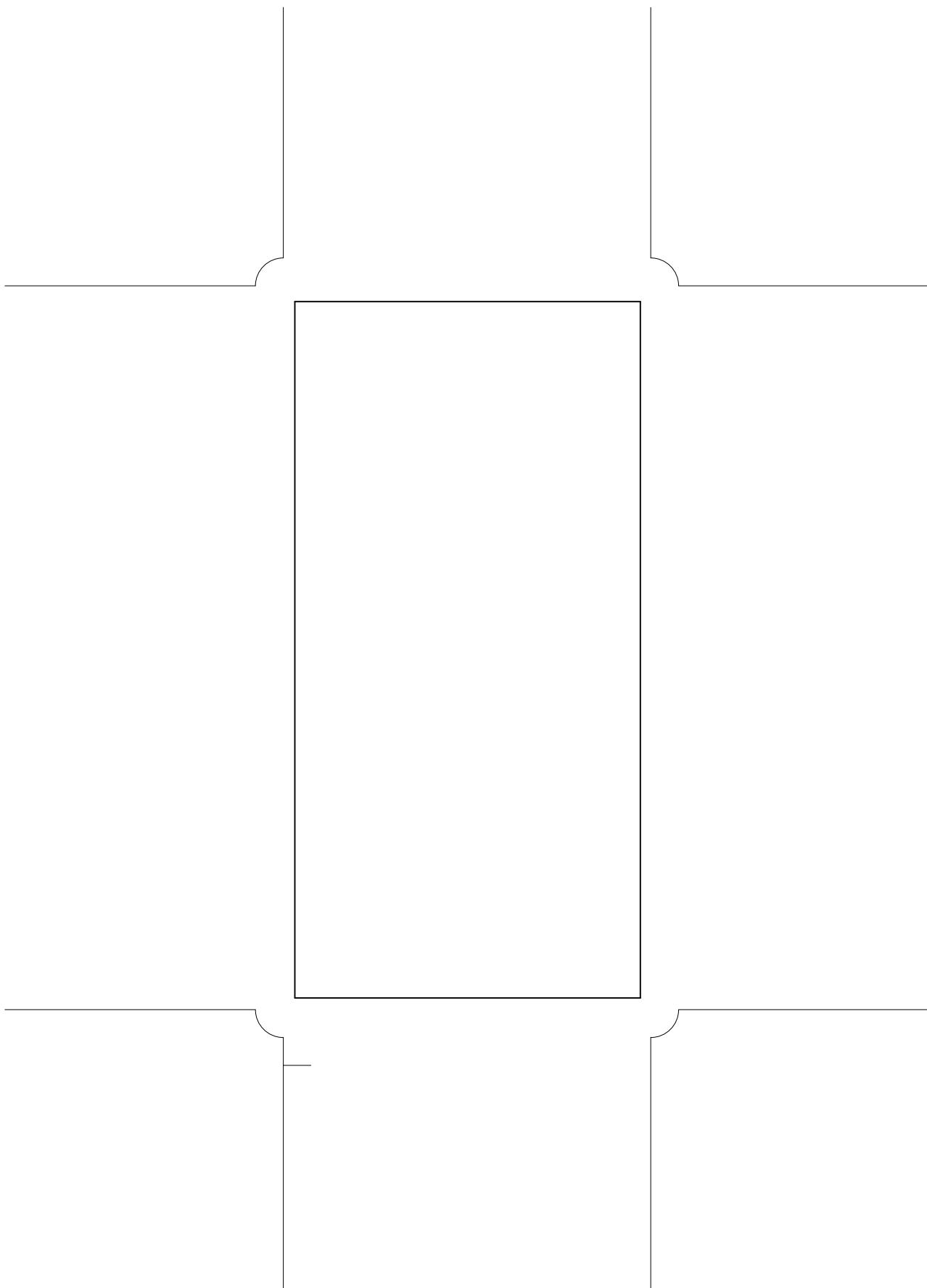
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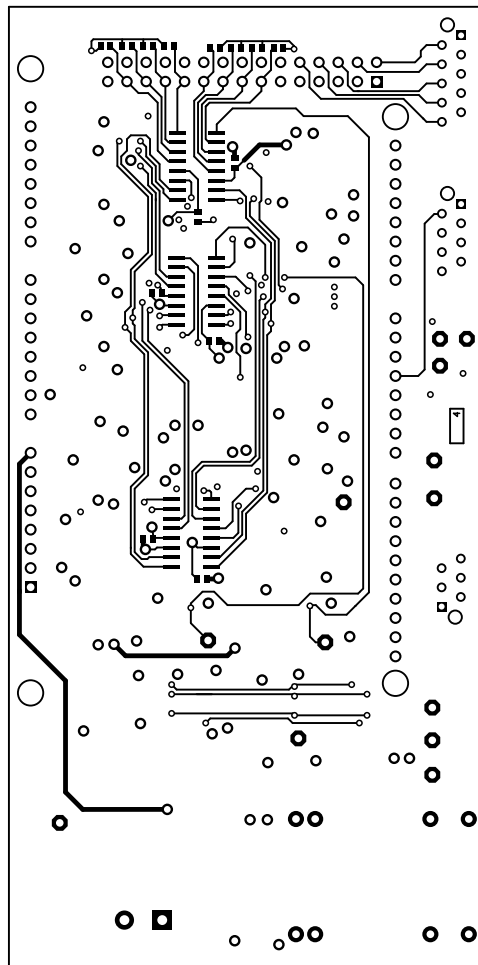
2. component
3. GND
4. layer3
5. outline
6. solder
7. plated-drill
8. componentmask
9. soldermask
10. topsilk
11. bottomsilk
12. toppaste
13. bottompaste
14. topassembly
15. bottomassembly
16. fab

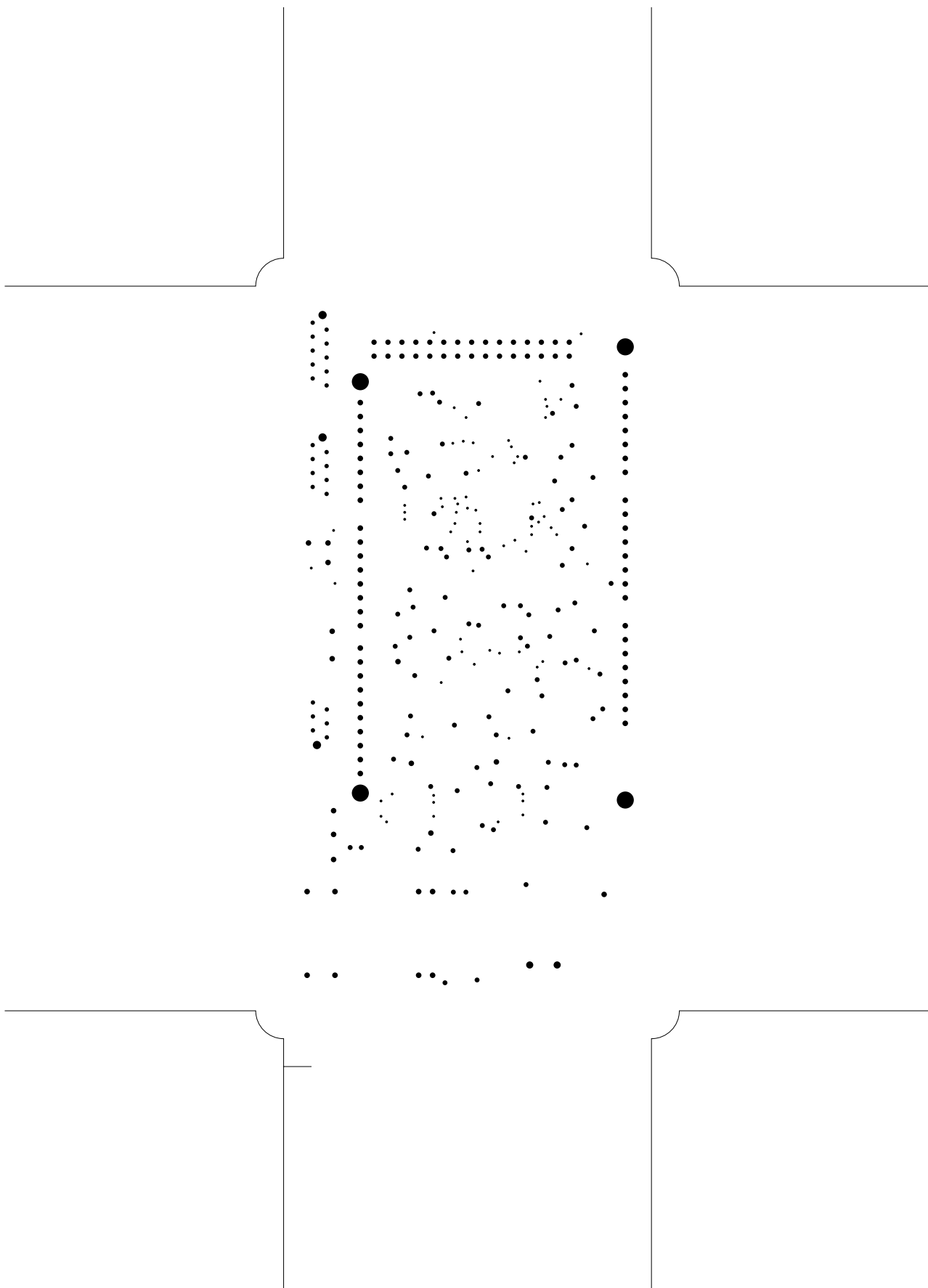


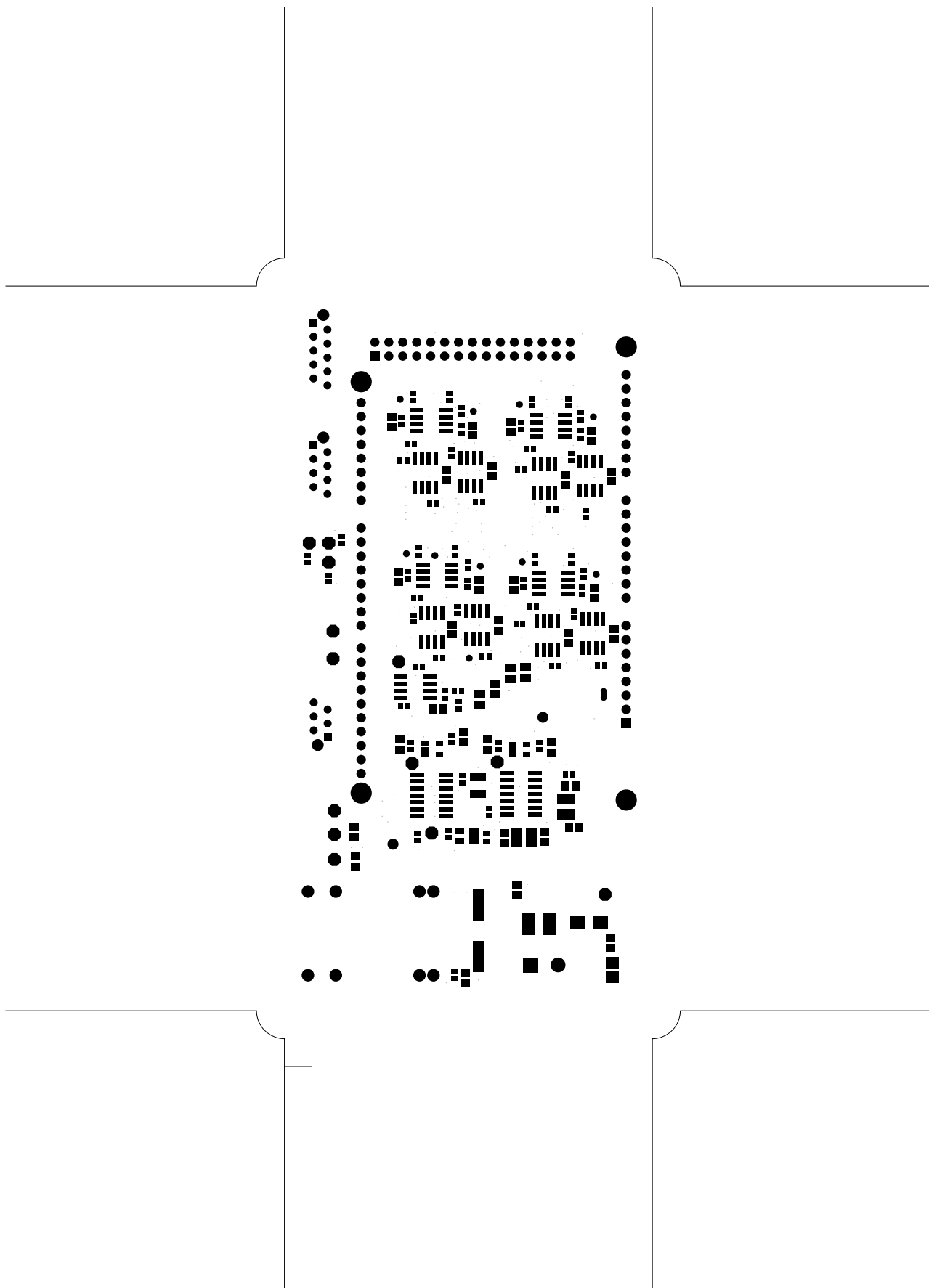


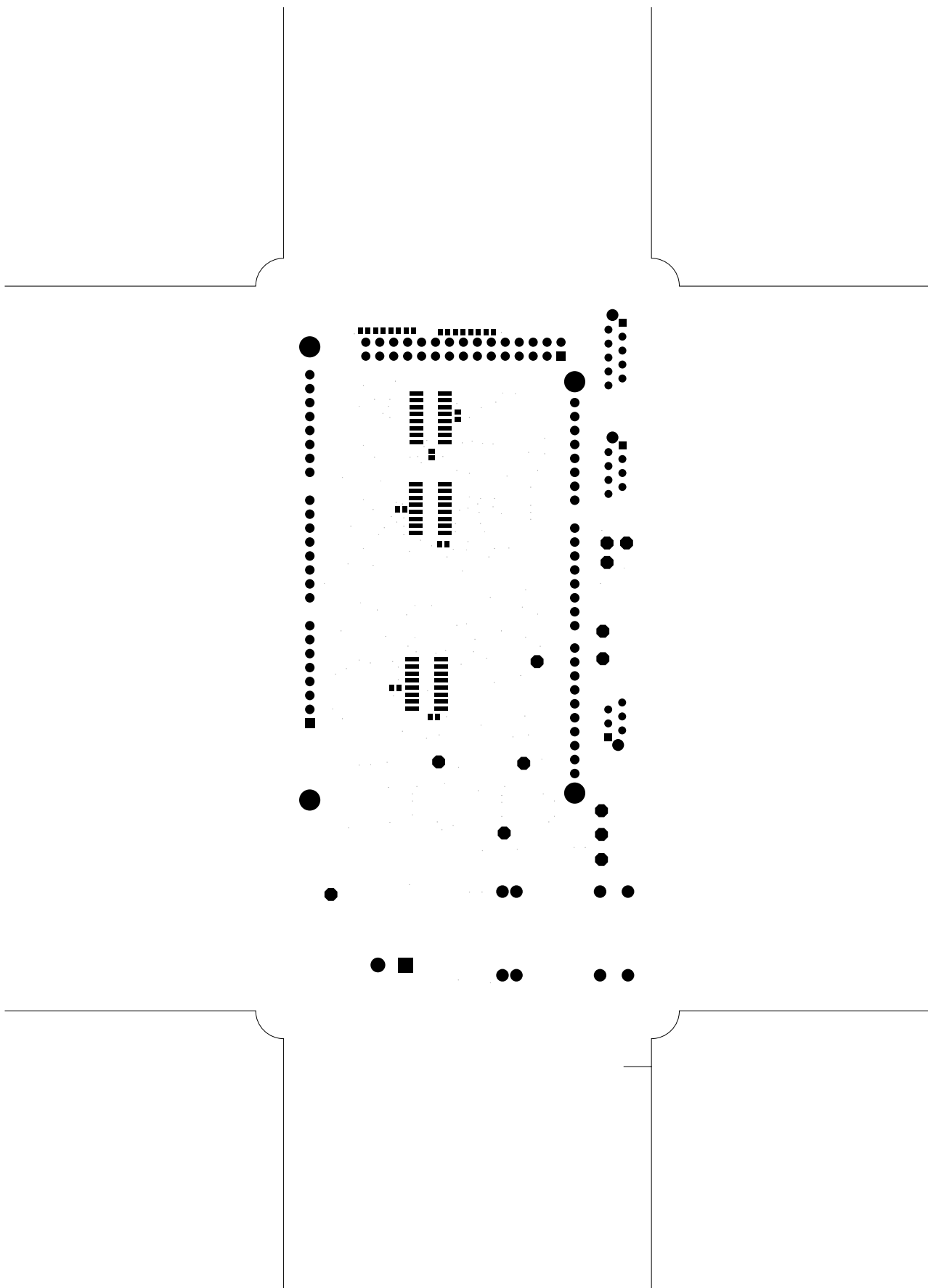


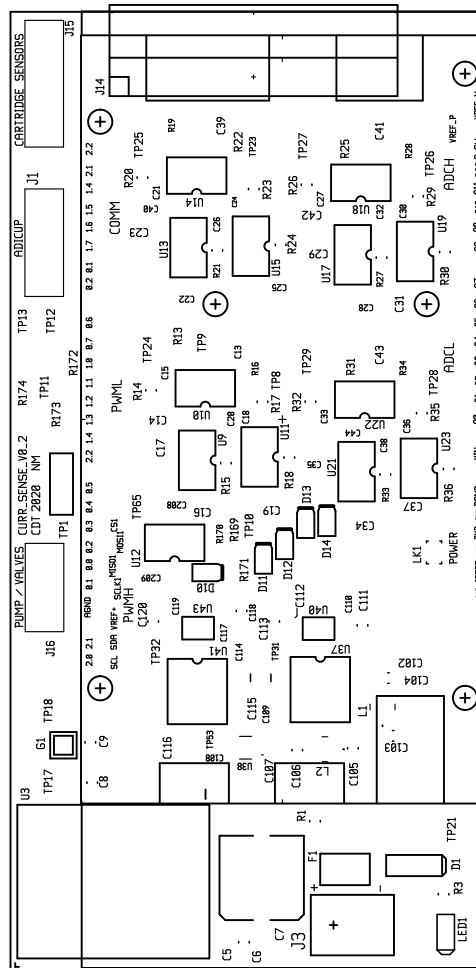


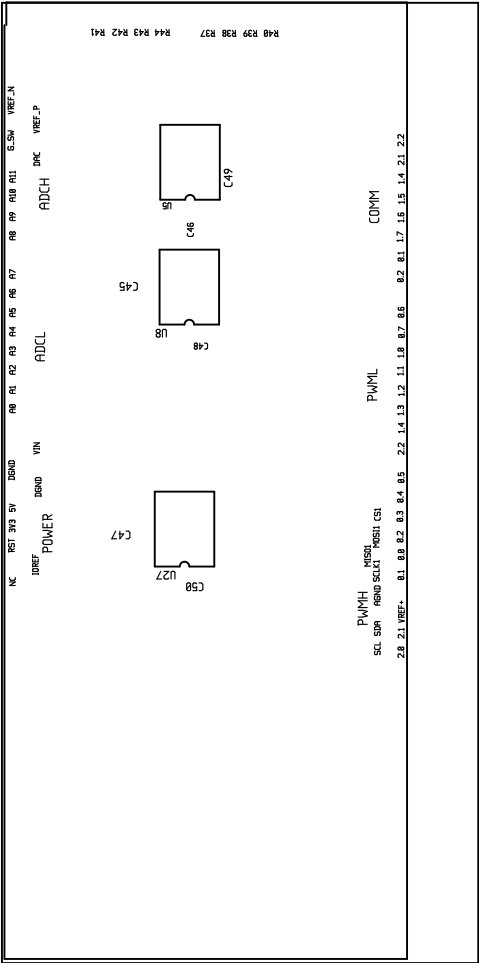




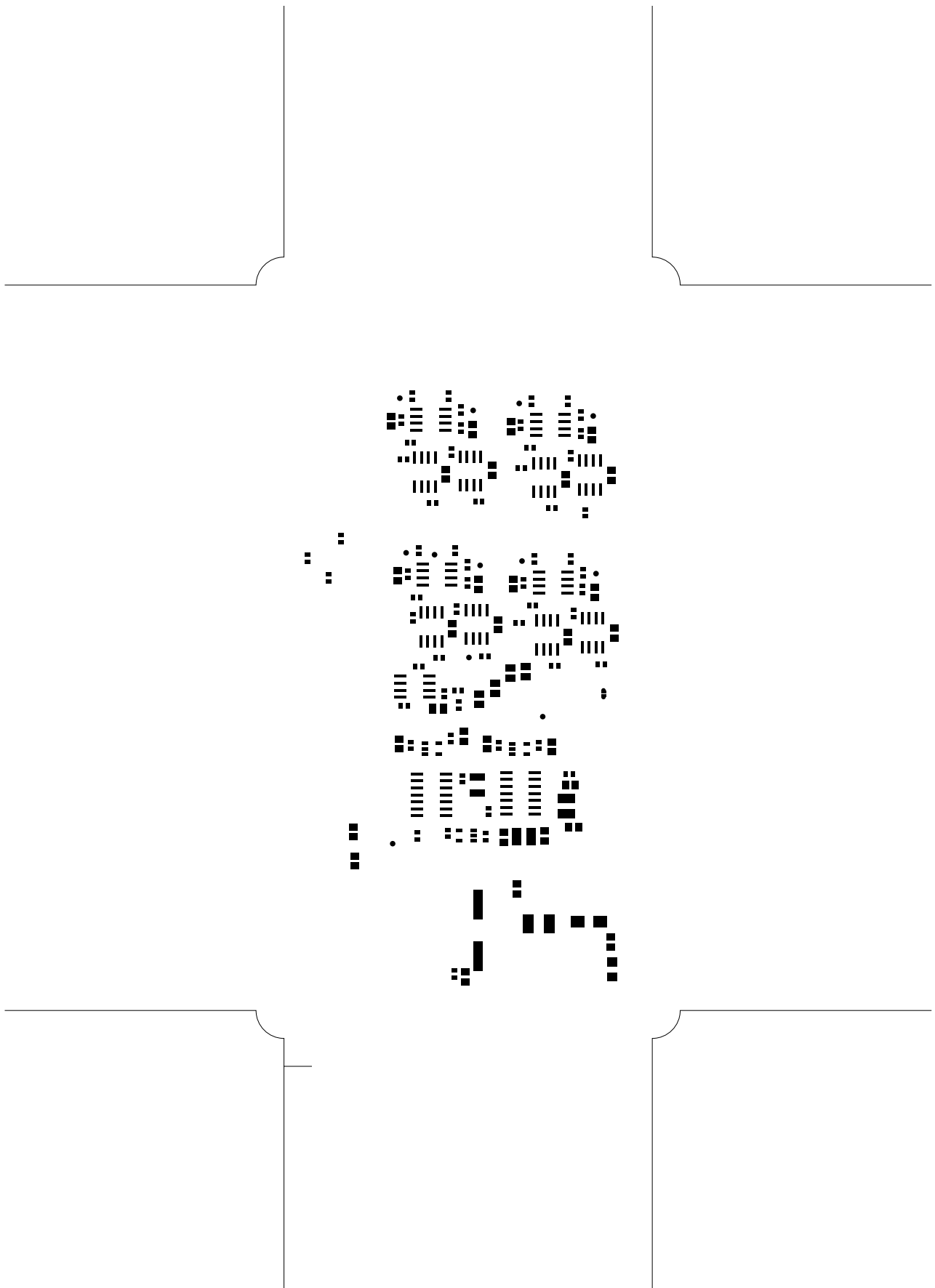


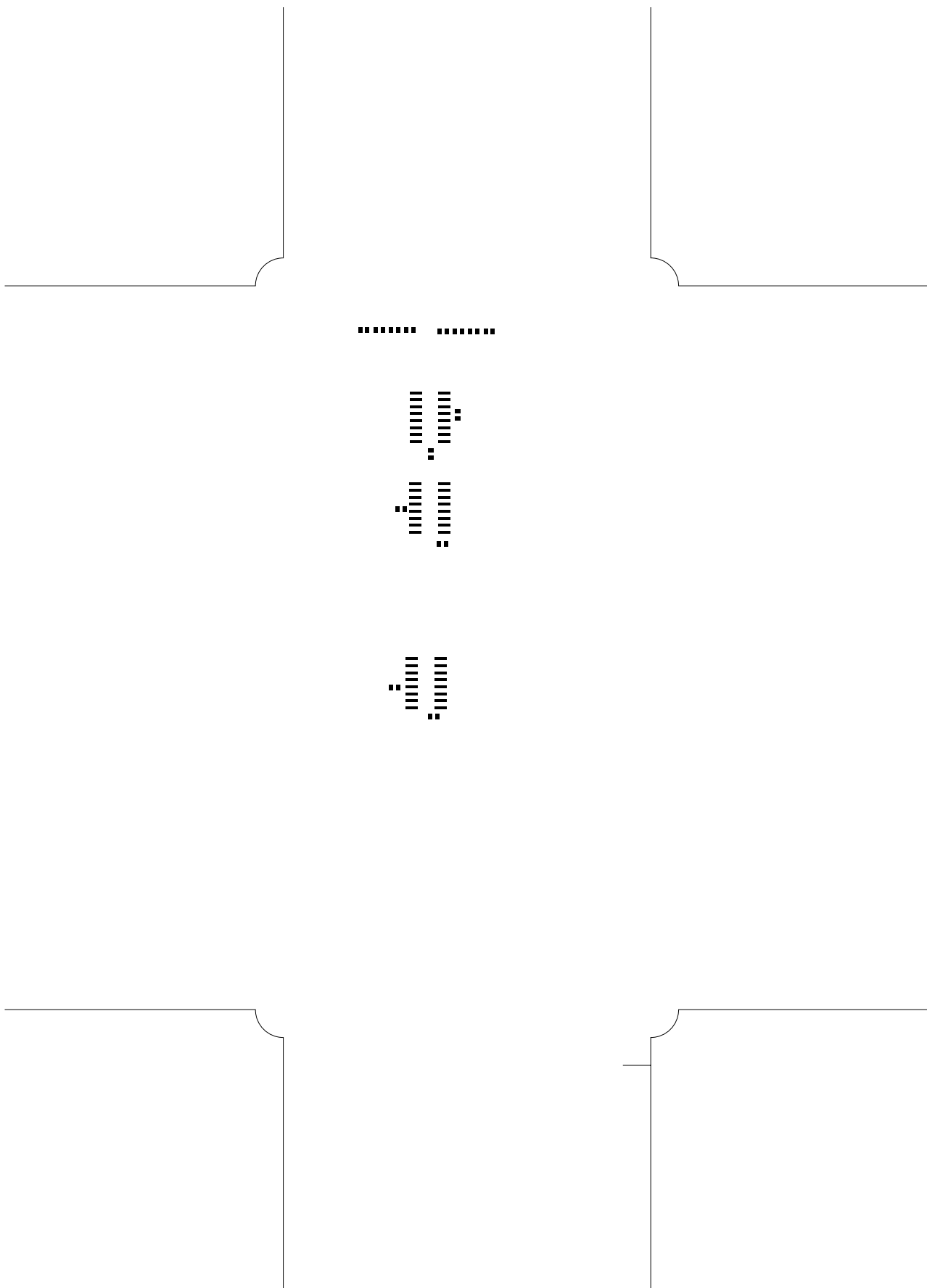


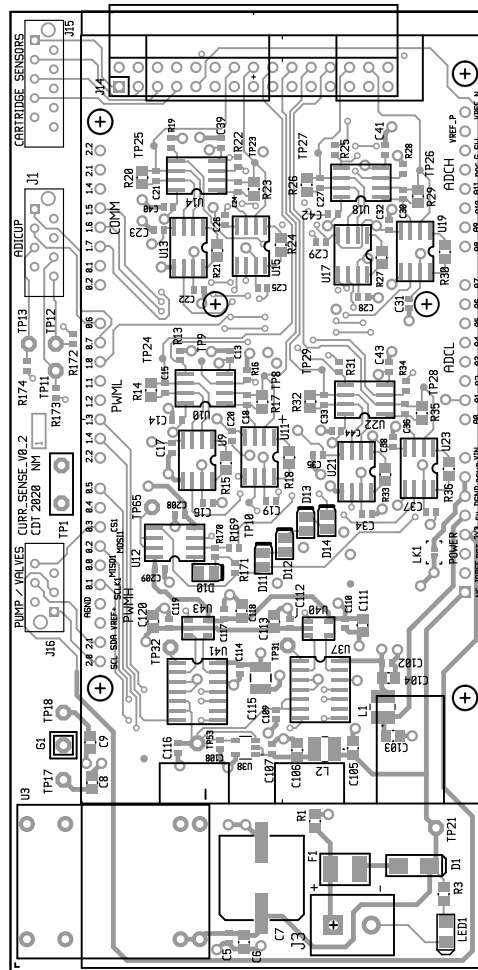


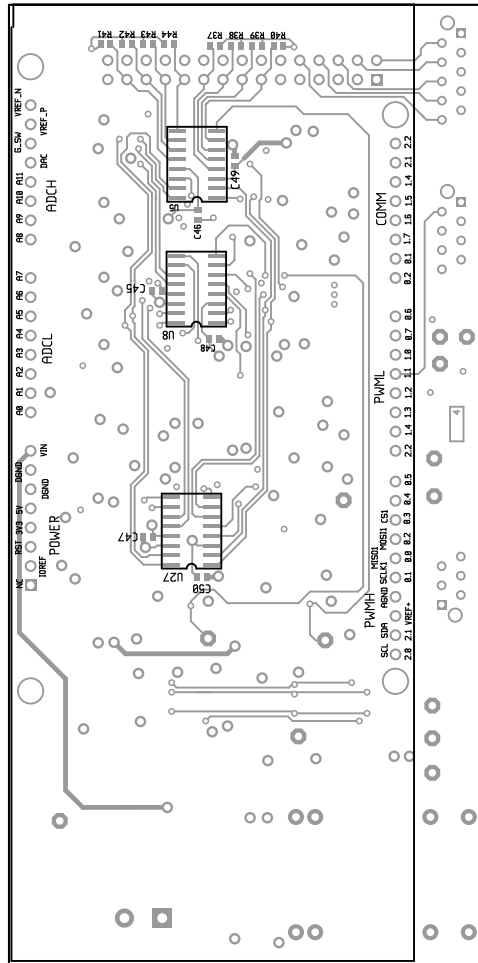


CURR_SENSE_V0_2, bottomsilk (mirrored), scale = 1:1.000
currSense.pcb









CURR_SENSE_V0_2, bottomassembly (mirrored), scale = 1:1.000
currSense.pcb

There are 9 different drill sizes used in this layout, 298 holes total

Symbol	Diam. (Inch)	Count	Plated?
Y	0.020	73	YES
+	0.031	24	YES
x	0.035	91	YES
o	0.038	30	YES
□	0.039	13	YES
▽	0.040	58	YES
#	0.051	2	YES
⊗	0.059	3	YES
⊙	0.122	4	YES

Title: CURR_SENSE_V0_2 - Fabrication Drawing

Author: niallmac

Date: Wed Jul 8 16:04:01 2020 UTC

