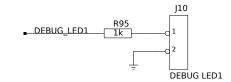
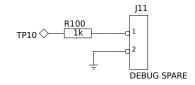


Zero Ohm used to create a seperate logical net for IO REF voltage and distinguish it from items drawing power from BOR 3V3.

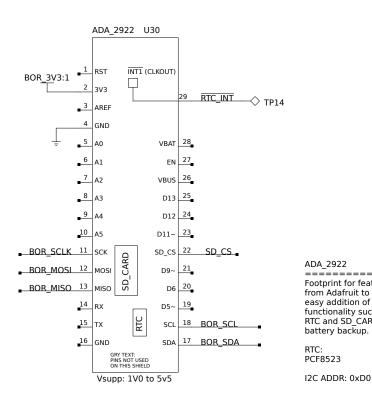


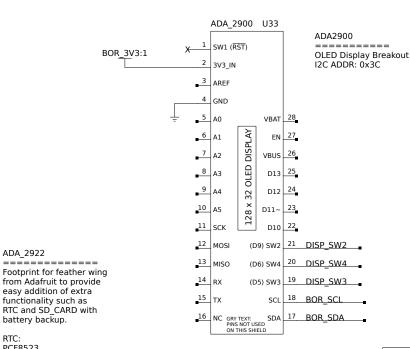


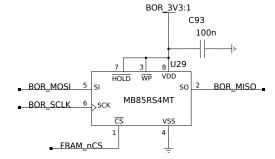
Module Boards:

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Provides for flexibility of testing several options on the prototype. Later to save power and cost the minimum set of components can be incorporated directly on the main







4MB FRAM provides non-volatile memory for data logging or/and additional external RAM to implement data processing algorithms. Retains memory without needing battery backup.

Maybe useful to store data between once per hour or once per day CAT-M1 power-up and TX to cloud.

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