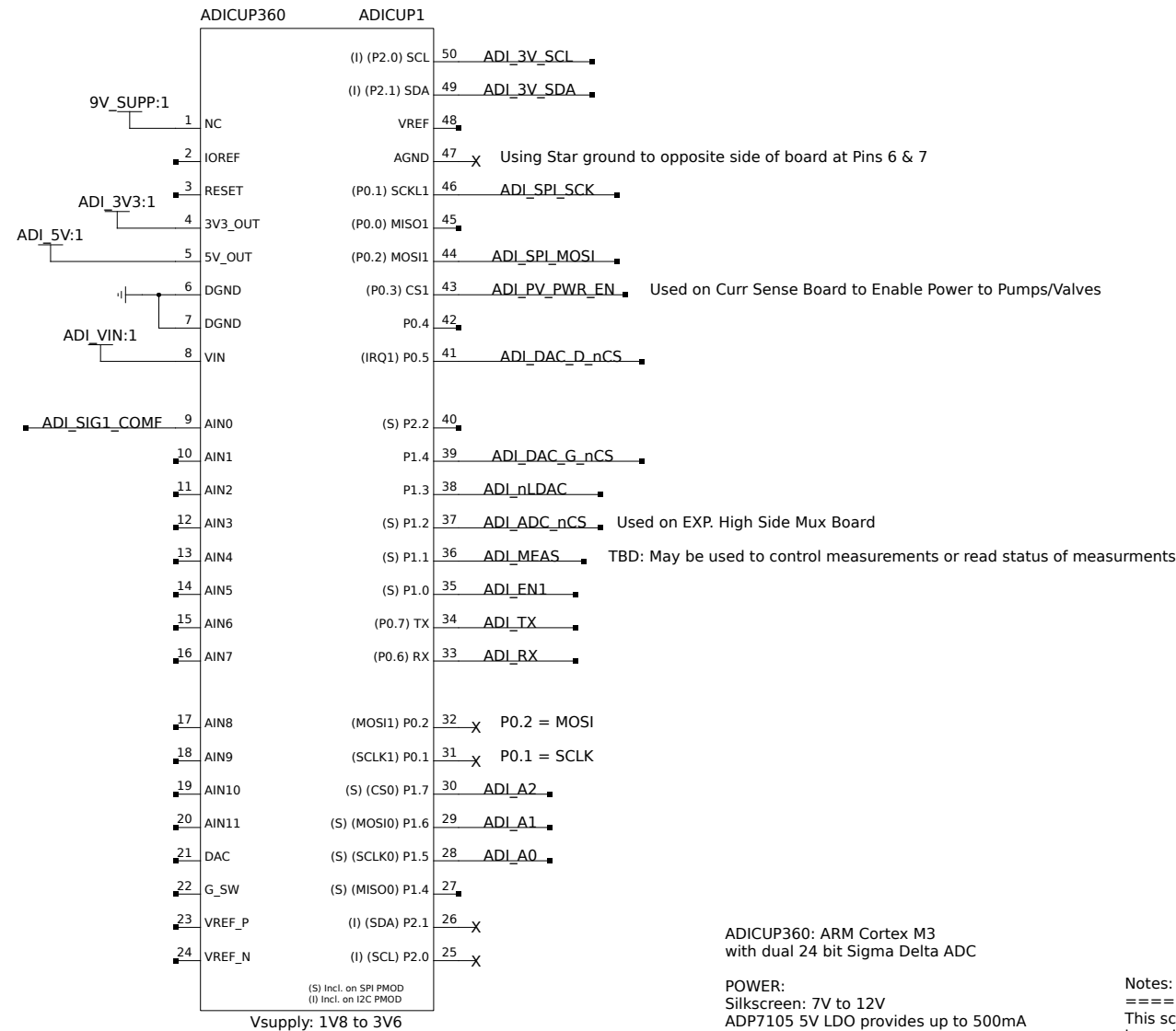


# PROJECT ADICUP BREAKOUT PIN MAPPING



Input Voltages must not exceed {AGND + 0.1V, AVDD - 0.1V}, note this means negative voltages such as VDCOM/ VGCOM cannot be directly connected to the ADICUP360.

ADICUP360: ARM Cortex M3 with dual 24 bit Sigma Delta ADC

POWER:  
Silkscreen: 7V to 12V  
ADP7105 5V LDO provides up to 500mA  
max VIN 20V

NOTE 1: 9V\_SUPP is derived from ADI\_VIN the spare NC pin on the ADICUP EVAL board is used to distribute this fused and reverse polarity protected supply to other daughter boards. If high current is needed then use a wired connection.

Notes:  
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This schemactice page at the project level is referenced by multiple sub-boards at the design level. The breakout represents a common PIN mapping at the edges of all of these stackable boards.

TITLE ADICUP Stackable Common Pin Mapping V0_1		CDT CONFIDENTIAL FOR INTERNAL USE ONLY
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