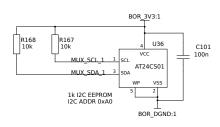
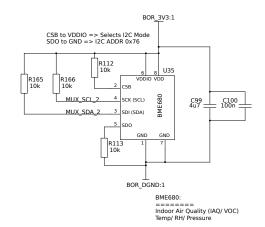
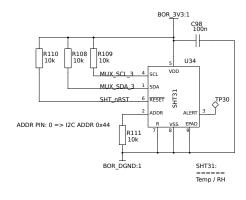
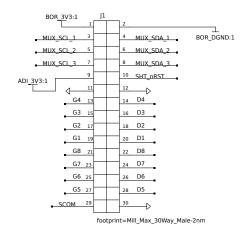
GS_FET PLUG-IN MODULE TYPE A - V0_1









SCOM G4 11 G8 11 D4 10 D8 G7 8 G3 8 D3 D7 G2 5 G6 _ D2 D6 G5 2 G1 2 D1 D5 S/D designations above may be reversed with respect to the nominal designations on the Mask design. The pin naming above is preserved for consistaency with V0_0. LK1 🖨

FFC CONNECTORS

LK1 Default: High Side Current Measurement (Drain current measurment), LK1 is bridged. For low side measurment (Common) source current measurement cut shorting link to open LK1.

V0_0: Used on Jan 2020 Field trial. This had a 24 pin header and all sensors shared a common I2C bus.

V0_1: This version has a 30 pin header. The sensors are on seperate I2C buses to mitigate against bus lockup by one failed sensor.

TITLE GS_FET MODULE TYPE A - V0_1							CDT CONFIDENTIAL FOR INTERNAL USE ONLY
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