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//Precision RTL Synthesis 64-bit 2024.2.0.4 (Production Release) Wed Aug 14 00:06:06 PDT 2024
//
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//
//Running on Linux runner@2a1576ce498d #232-Ubuntu SMP Sat Mar 15 15:34:35 UTC 2025 5.4.0-212-generic
x86_64
//
//
// Verilog description for cell Top_Level,
//

module Top_Level ( Reset, Timeset, Alarmset, Minadv, Hrsadv, Alarmon, Pulse,
S1disp, S0disp, M1disp, M0disp, H1disp, H0disp, Buzz ) ;

input Reset ;
input Timeset ;
input Alarmset ;
input Minadv ;
input Hrsadv ;
input Alarmon ;
input Pulse ;
output [6:0]S1disp ;
output [6:0]S0disp ;
output [6:0]M1disp ;
output [6:0]M0disp ;
output [6:0]H1disp ;
output [6:0]H0disp ;
output Buzz ;

wire \S1disp_1_0(2) , \S0disp_1_0(2) , \M1disp_1_0(2) , \M0disp_1_0(2) ,
\H1disp_1_0(2) , \H0disp_1_0(2) ;
wire [5:0]TSec;
wire [6:0]TMin;
wire [6:0]THrs;
wire [6:0]AMin;
wire [6:0]AHrs;
wire \inc_d(0) , nx8474z1, \inc_d(1) , nx8475z1, \inc_d(2) , nx8476z1,
\inc_d(3) , nx8477z1, \inc_d(4) , nx51683z1, \inc_d(5) ,
\inc_d(0)_1_93 , nx63830z1, \inc_d(1)_1_96 , nx6079z1,
\inc_d(2)_1_99 , nx51672z1, \inc_d(3)_1_102 , nx43887z1,
\inc_d(4)_1_105 , nx58480z1, \inc_d(5)_1_108 , nx50695z1,
\inc_d(6)_1_111 , \inc_d(0)_1_137 , nx61836z1, \inc_d(1)_1_140 ,
nx4085z1, \inc_d(2)_1_143 , nx53666z1, \inc_d(3)_1_146 , nx45881z1,
\inc_d(4)_1_149 , nx38096z1, \inc_d(5)_1_152 , nx52689z1,
\inc_d(6)_1_155 , \inc_d(0)_1_181 , nx59842z1, \inc_d(1)_1_184 ,
nx2091z1, \inc_d(2)_1_187 , nx55660z1, \inc_d(3)_1_190 , nx47875z1,
\inc_d(4)_1_193 , nx62468z1, \inc_d(5)_1_196 , nx54683z1,
\inc_d(6)_1_199 , \inc_d(0)_1_225 , nx57848z1, \inc_d(1)_1_228 ,
nx97z1, \inc_d(2)_1_231 , nx57654z1, \inc_d(3)_1_234 , nx49869z1,
\inc_d(4)_1_237 , nx42084z1, \inc_d(5)_1_240 , nx56677z1,
\inc_d(6)_1_243 ;
wire \a(4) ;
wire Reset_int, Timeset_int, Alarmset_int, Minadv_int, Hrsadv_int,
Alarmon_int;
wire Pulse_int;
wire nx15072z1, nx38425z1, nx37428z1, nx36431z1, nx33440z1, nx56674z1,
nx22895z1, nx25886z1, nx26883z1, nx36455z1, nx39446z1, nx15048z1,
nx28185z1, nx7077z1, nx41745z1, nx8473z1, nx2982z1, nx11274z1, nx4976z1,
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nx39672z1, nx7286z1, nx28360z1, nx51271z1, nx56256z1, nx13078z1,
nx14075z1, nx16069z1, nx18063z1, nx19060z1, nx35434z1, nx32443z1,
nx38425z4, nx58668z1, nx57671z1, nx55677z1, nx53683z1, nx52686z1,
nx20901z1, nx21898z1, nx23892z1, nx34461z1, nx35458z1, nx37452z1,
nx40443z1, nx17042z1, nx16045z1, nx14051z1, nx12057z1, nx11060z1,
nx45858z1, nx45858z6, nx45858z7, nx45858z8, nx45858z4, nx45858z5,
nx45858z2, nx45858z3, nx60217z1, nx34461z14, nx34461z5, nx34461z12,
nx34461z11, nx17042z2, nx34461z13, nx34461z10, nx34461z6, nx34461z4,
nx34461z3, nx34461z9, nx34461z2, nx34461z8, nx34461z7, nx49747z1,
nx58668z14, nx58668z5, nx58668z13, nx20901z2, nx58668z11, nx58668z10,
nx58668z12, nx58668z6, nx58668z4, nx58668z3, nx58668z9, nx58668z2,
nx58668z8, nx58668z7, nx925z1, nx38425z3, nx38425z2, nx2982z2, nx4976z2,
nx6970z1, nx6970z3, nx6970z2, nx54956z1, nx7286z2, nx54956z3, nx54956z2,
nx28360z2, nx51271z2;



FDRE \reg_q(5) (.Q (TSec[5]), .C (Pulse_int), .CE (nx56256z1), .D (
\inc_d(5) ), .R (nx51271z1)) ;
FDRE \reg_q(4) (.Q (TSec[4]), .C (Pulse_int), .CE (nx56256z1), .D (
\inc_d(4) ), .R (nx51271z1)) ;
FDRE \reg_q(3) (.Q (TSec[3]), .C (Pulse_int), .CE (nx56256z1), .D (
\inc_d(3) ), .R (nx51271z1)) ;
FDRE \reg_q(2) (.Q (TSec[2]), .C (Pulse_int), .CE (nx56256z1), .D (
\inc_d(2) ), .R (nx51271z1)) ;
FDRE \reg_q(1) (.Q (TSec[1]), .C (Pulse_int), .CE (nx56256z1), .D (
\inc_d(1) ), .R (nx51271z1)) ;
FDRE \reg_q(0) (.Q (TSec[0]), .C (Pulse_int), .CE (nx56256z1), .D (
\inc_d(0) ), .R (nx51271z1)) ;
XORCY xorcy_0 (.O (\inc_d(0) ), .CI (nx8473z1), .LI (TSec[0])) ;
XORCY xorcy_1 (.O (\inc_d(1) ), .CI (nx8474z1), .LI (TSec[1])) ;
XORCY xorcy_2 (.O (\inc_d(2) ), .CI (nx8475z1), .LI (TSec[2])) ;
XORCY xorcy_3 (.O (\inc_d(3) ), .CI (nx8476z1), .LI (TSec[3])) ;
XORCY xorcy_4 (.O (\inc_d(4) ), .CI (nx8477z1), .LI (TSec[4])) ;
XORCY xorcy_5 (.O (\inc_d(5) ), .CI (nx51683z1), .LI (TSec[5])) ;
FDRE \reg_q(6)_1_0 (.Q (TMin[6]), .C (Pulse_int), .CE (nx28360z1), .D (
\inc_d(6)_1_111 ), .R (nx54956z1)) ;
FDRE \reg_q(5)_1_1 (.Q (TMin[5]), .C (Pulse_int), .CE (nx28360z1), .D (
\inc_d(5)_1_108 ), .R (nx54956z1)) ;
FDRE \reg_q(4)_1_2 (.Q (TMin[4]), .C (Pulse_int), .CE (nx28360z1), .D (
\inc_d(4)_1_105 ), .R (nx54956z1)) ;
FDRE \reg_q(3)_1_3 (.Q (TMin[3]), .C (Pulse_int), .CE (nx28360z1), .D (
\inc_d(3)_1_102 ), .R (nx54956z1)) ;
FDRE \reg_q(2)_1_4 (.Q (TMin[2]), .C (Pulse_int), .CE (nx28360z1), .D (
\inc_d(2)_1_99 ), .R (nx54956z1)) ;
FDRE \reg_q(1)_1_5 (.Q (TMin[1]), .C (Pulse_int), .CE (nx28360z1), .D (
\inc_d(1)_1_96 ), .R (nx54956z1)) ;
FDRE \reg_q(0)_1_6 (.Q (TMin[0]), .C (Pulse_int), .CE (nx28360z1), .D (
\inc_d(0)_1_93 ), .R (nx54956z1)) ;
XORCY xorcy_0_1_7 (.O (\inc_d(0)_1_93 ), .CI (nx8473z1), .LI (TMin[0])) ;
XORCY xorcy_1_1_9 (.O (\inc_d(1)_1_96 ), .CI (nx63830z1), .LI (TMin[1])) ;
XORCY xorcy_2_1_11 (.O (\inc_d(2)_1_99 ), .CI (nx6079z1), .LI (TMin[2])) ;
XORCY xorcy_3_1_13 (.O (\inc_d(3)_1_102 ), .CI (nx51672z1), .LI (TMin[3])
) ;
XORCY xorcy_4_1_15 (.O (\inc_d(4)_1_105 ), .CI (nx43887z1), .LI (TMin[4])
) ;
XORCY xorcy_5_1_17 (.O (\inc_d(5)_1_108 ), .CI (nx58480z1), .LI (TMin[5])
) ;
XORCY xorcy_6_1_19 (.O (\inc_d(6)_1_111 ), .CI (nx50695z1), .LI (TMin[6])
) ;
FDRE \reg_q(6)_1_20 (.Q (THrs[6]), .C (Pulse_int), .CE (nx7286z1), .D (
\inc_d(6)_1_155 ), .R (nx6970z1)) ;
FDRE \reg_q(5)_1_21 (.Q (THrs[5]), .C (Pulse_int), .CE (nx7286z1), .D (
\inc_d(5)_1_152 ), .R (nx6970z1)) ;
FDRE \reg_q(4)_1_22 (.Q (THrs[4]), .C (Pulse_int), .CE (nx7286z1), .D (
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\inc_d(4)_1_149 ), .R (nx6970z1)) ;
FDRE \reg_q(3)_1_23 (.Q (THrs[3]), .C (Pulse_int), .CE (nx7286z1), .D (
\inc_d(3)_1_146 ), .R (nx6970z1)) ;
FDRE \reg_q(2)_1_24 (.Q (THrs[2]), .C (Pulse_int), .CE (nx7286z1), .D (
\inc_d(2)_1_143 ), .R (nx6970z1)) ;
FDRE \reg_q(1)_1_25 (.Q (THrs[1]), .C (Pulse_int), .CE (nx7286z1), .D (
\inc_d(1)_1_140 ), .R (nx6970z1)) ;
FDRE \reg_q(0)_1_26 (.Q (THrs[0]), .C (Pulse_int), .CE (nx7286z1), .D (
\inc_d(0)_1_137 ), .R (nx6970z1)) ;
XORCY xorcy_0_1_27 (.O (\inc_d(0)_1_137 ), .CI (nx8473z1), .LI (THrs[0])) ;
XORCY xorcy_1_1_29 (.O (\inc_d(1)_1_140 ), .CI (nx61836z1), .LI (THrs[1])
) ;
XORCY xorcy_2_1_31 (.O (\inc_d(2)_1_143 ), .CI (nx4085z1), .LI (THrs[2])) ;
XORCY xorcy_3_1_33 (.O (\inc_d(3)_1_146 ), .CI (nx53666z1), .LI (THrs[3])
) ;
XORCY xorcy_4_1_35 (.O (\inc_d(4)_1_149 ), .CI (nx45881z1), .LI (THrs[4])
) ;
XORCY xorcy_5_1_37 (.O (\inc_d(5)_1_152 ), .CI (nx38096z1), .LI (THrs[5])
) ;
XORCY xorcy_6_1_39 (.O (\inc_d(6)_1_155 ), .CI (nx52689z1), .LI (THrs[6])
) ;
FDRE \reg_q(6)_1_40 (.Q (AMin[6]), .C (Pulse_int), .CE (nx39672z1), .D (
\inc_d(6)_1_199 ), .R (nx4976z1)) ;
FDRE \reg_q(5)_1_41 (.Q (AMin[5]), .C (Pulse_int), .CE (nx39672z1), .D (
\inc_d(5)_1_196 ), .R (nx4976z1)) ;
FDRE \reg_q(4)_1_42 (.Q (AMin[4]), .C (Pulse_int), .CE (nx39672z1), .D (
\inc_d(4)_1_193 ), .R (nx4976z1)) ;
FDRE \reg_q(3)_1_43 (.Q (AMin[3]), .C (Pulse_int), .CE (nx39672z1), .D (
\inc_d(3)_1_190 ), .R (nx4976z1)) ;
FDRE \reg_q(2)_1_44 (.Q (AMin[2]), .C (Pulse_int), .CE (nx39672z1), .D (
\inc_d(2)_1_187 ), .R (nx4976z1)) ;
FDRE \reg_q(1)_1_45 (.Q (AMin[1]), .C (Pulse_int), .CE (nx39672z1), .D (
\inc_d(1)_1_184 ), .R (nx4976z1)) ;
FDRE \reg_q(0)_1_46 (.Q (AMin[0]), .C (Pulse_int), .CE (nx39672z1), .D (
\inc_d(0)_1_181 ), .R (nx4976z1)) ;
XORCY xorcy_0_1_47 (.O (\inc_d(0)_1_181 ), .CI (nx8473z1), .LI (AMin[0])) ;
XORCY xorcy_1_1_49 (.O (\inc_d(1)_1_184 ), .CI (nx59842z1), .LI (AMin[1])
) ;
XORCY xorcy_2_1_51 (.O (\inc_d(2)_1_187 ), .CI (nx2091z1), .LI (AMin[2])) ;
XORCY xorcy_3_1_53 (.O (\inc_d(3)_1_190 ), .CI (nx55660z1), .LI (AMin[3])
) ;
XORCY xorcy_4_1_55 (.O (\inc_d(4)_1_193 ), .CI (nx47875z1), .LI (AMin[4])
) ;
XORCY xorcy_5_1_57 (.O (\inc_d(5)_1_196 ), .CI (nx62468z1), .LI (AMin[5])
) ;
XORCY xorcy_6_1_59 (.O (\inc_d(6)_1_199 ), .CI (nx54683z1), .LI (AMin[6])
) ;
FDRE \reg_q(6)_1_60 (.Q (AHrs[6]), .C (Pulse_int), .CE (nx11274z1), .D (
\inc_d(6)_1_243 ), .R (nx2982z1)) ;
FDRE \reg_q(5)_1_61 (.Q (AHrs[5]), .C (Pulse_int), .CE (nx11274z1), .D (
\inc_d(5)_1_240 ), .R (nx2982z1)) ;
FDRE \reg_q(4)_1_62 (.Q (AHrs[4]), .C (Pulse_int), .CE (nx11274z1), .D (
\inc_d(4)_1_237 ), .R (nx2982z1)) ;
FDRE \reg_q(3)_1_63 (.Q (AHrs[3]), .C (Pulse_int), .CE (nx11274z1), .D (
\inc_d(3)_1_234 ), .R (nx2982z1)) ;
FDRE \reg_q(2)_1_64 (.Q (AHrs[2]), .C (Pulse_int), .CE (nx11274z1), .D (
\inc_d(2)_1_231 ), .R (nx2982z1)) ;
FDRE \reg_q(1)_1_65 (.Q (AHrs[1]), .C (Pulse_int), .CE (nx11274z1), .D (
\inc_d(1)_1_228 ), .R (nx2982z1)) ;
FDRE \reg_q(0)_1_66 (.Q (AHrs[0]), .C (Pulse_int), .CE (nx11274z1), .D (
\inc_d(0)_1_225 ), .R (nx2982z1)) ;
XORCY xorcy_0_1_67 (.O (\inc_d(0)_1_225 ), .CI (nx8473z1), .LI (AHrs[0])) ;
XORCY xorcy_1_1_69 (.O (\inc_d(1)_1_228 ), .CI (nx57848z1), .LI (AHrs[1])
) ;
XORCY xorcy_2_1_71 (.O (\inc_d(2)_1_231 ), .CI (nx97z1), .LI (AHrs[2])) ;
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XORCY xorcy_3_1_73 (.O (\inc_d(3)_1_234 ), .CI (nx57654z1), .LI (AHrs[3])
) ;
XORCY xorcy_4_1_75 (.O (\inc_d(4)_1_237 ), .CI (nx49869z1), .LI (AHrs[4])
) ;
XORCY xorcy_5_1_77 (.O (\inc_d(5)_1_240 ), .CI (nx42084z1), .LI (AHrs[5])
) ;
XORCY xorcy_6_1_79 (.O (\inc_d(6)_1_243 ), .CI (nx56677z1), .LI (AHrs[6])
) ;
(* IS_INTERNAL_DONT_TOUCH = "true" *)
INV \ix263_S0disp(2) (.O (\S0disp_1_0(2) ), .I (nx41745z1)) ;
(* IS_INTERNAL_DONT_TOUCH = "true" *)
INV \ix291_S1disp(2) (.O (\S1disp_1_0(2) ), .I (nx925z1)) ;
GND ix300 (.G (\a(4) )) ;
(* IS_INTERNAL_DONT_TOUCH = "true" *)
INV \ix344_M0disp(2) (.O (\M0disp_1_0(2) ), .I (nx7077z1)) ;
(* IS_INTERNAL_DONT_TOUCH = "true" *)
INV \ix372_M1disp(2) (.O (\M1disp_1_0(2) ), .I (nx49747z1)) ;
(* IS_INTERNAL_DONT_TOUCH = "true" *)
INV \ix439_H0disp(2) (.O (\H0disp_1_0(2) ), .I (nx28185z1)) ;
(* IS_INTERNAL_DONT_TOUCH = "true" *)
INV \ix467_H1disp(2) (.O (\H1disp_1_0(2) ), .I (nx60217z1)) ;
OBUF Buzz_obuf (.O (Buzz), .I (nx45858z1)) ;
OBUF \H0disp_obuf(0) (.O (H0disp[0]), .I (nx11060z1)) ;
OBUF \H0disp_obuf(1) (.O (H0disp[1]), .I (nx12057z1)) ;
OBUF \H0disp_obuf(2) (.O (H0disp[2]), .I (\H0disp_1_0(2) )) ;
OBUF \H0disp_obuf(3) (.O (H0disp[3]), .I (nx14051z1)) ;
OBUF \H0disp_obuf(4) (.O (H0disp[4]), .I (nx15048z1)) ;
OBUF \H0disp_obuf(5) (.O (H0disp[5]), .I (nx16045z1)) ;
OBUF \H0disp_obuf(6) (.O (H0disp[6]), .I (nx17042z1)) ;
OBUF \H1disp_obuf(0) (.O (H1disp[0]), .I (nx40443z1)) ;
OBUF \H1disp_obuf(1) (.O (H1disp[1]), .I (nx39446z1)) ;
OBUF \H1disp_obuf(2) (.O (H1disp[2]), .I (\H1disp_1_0(2) )) ;
OBUF \H1disp_obuf(3) (.O (H1disp[3]), .I (nx37452z1)) ;
OBUF \H1disp_obuf(4) (.O (H1disp[4]), .I (nx36455z1)) ;
OBUF \H1disp_obuf(5) (.O (H1disp[5]), .I (nx35458z1)) ;
OBUF \H1disp_obuf(6) (.O (H1disp[6]), .I (nx34461z1)) ;
OBUF \M0disp_obuf(0) (.O (M0disp[0]), .I (nx26883z1)) ;
OBUF \M0disp_obuf(1) (.O (M0disp[1]), .I (nx25886z1)) ;
OBUF \M0disp_obuf(2) (.O (M0disp[2]), .I (\M0disp_1_0(2) )) ;
OBUF \M0disp_obuf(3) (.O (M0disp[3]), .I (nx23892z1)) ;
OBUF \M0disp_obuf(4) (.O (M0disp[4]), .I (nx22895z1)) ;
OBUF \M0disp_obuf(5) (.O (M0disp[5]), .I (nx21898z1)) ;
OBUF \M0disp_obuf(6) (.O (M0disp[6]), .I (nx20901z1)) ;
OBUF \M1disp_obuf(0) (.O (M1disp[0]), .I (nx52686z1)) ;
OBUF \M1disp_obuf(1) (.O (M1disp[1]), .I (nx53683z1)) ;
OBUF \M1disp_obuf(2) (.O (M1disp[2]), .I (\M1disp_1_0(2) )) ;
OBUF \M1disp_obuf(3) (.O (M1disp[3]), .I (nx55677z1)) ;
OBUF \M1disp_obuf(4) (.O (M1disp[4]), .I (nx56674z1)) ;
OBUF \M1disp_obuf(5) (.O (M1disp[5]), .I (nx57671z1)) ;
OBUF \M1disp_obuf(6) (.O (M1disp[6]), .I (nx58668z1)) ;
OBUF \S0disp_obuf(0) (.O (S0disp[0]), .I (nx32443z1)) ;
OBUF \S0disp_obuf(1) (.O (S0disp[1]), .I (nx33440z1)) ;
OBUF \S0disp_obuf(2) (.O (S0disp[2]), .I (\S0disp_1_0(2) )) ;
OBUF \S0disp_obuf(3) (.O (S0disp[3]), .I (nx35434z1)) ;
OBUF \S0disp_obuf(4) (.O (S0disp[4]), .I (nx36431z1)) ;
OBUF \S0disp_obuf(5) (.O (S0disp[5]), .I (nx37428z1)) ;
OBUF \S0disp_obuf(6) (.O (S0disp[6]), .I (nx38425z1)) ;
OBUF \S1disp_obuf(0) (.O (S1disp[0]), .I (nx19060z1)) ;
OBUF \S1disp_obuf(1) (.O (S1disp[1]), .I (nx18063z1)) ;
OBUF \S1disp_obuf(2) (.O (S1disp[2]), .I (\S1disp_1_0(2) )) ;
OBUF \S1disp_obuf(3) (.O (S1disp[3]), .I (nx16069z1)) ;
OBUF \S1disp_obuf(4) (.O (S1disp[4]), .I (nx15072z1)) ;
OBUF \S1disp_obuf(5) (.O (S1disp[5]), .I (nx14075z1)) ;
OBUF \S1disp_obuf(6) (.O (S1disp[6]), .I (nx13078z1)) ;
IBUF Alarmon_ibuf (.O (Alarmon_int), .I (Alarmon)) ;
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IBUF Hrsadv_ibuf (.O (Hrsadv_int), .I (Hrsadv)) ;
IBUF Minadv_ibuf (.O (Minadv_int), .I (Minadv)) ;
IBUF Alarmset_ibuf (.O (Alarmset_int), .I (Alarmset)) ;
IBUF Timeset_ibuf (.O (Timeset_int), .I (Timeset)) ;
IBUF Reset_ibuf (.O (Reset_int), .I (Reset)) ;
LUT6 ix15072z49377 (.O (nx15072z1), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix15072z49377.INIT = 64'h7F7F7FFFFBBFBBBF;
(* HLUTNM = "LUT62_1_11" *)
LUT4 ix38425z34017 (.O (nx38425z1), .I0 (nx38425z2), .I1 (nx38425z3), .I2 (
nx38425z4), .I3 (TSec[0])) ;
defparam ix38425z34017.INIT = 16'h7FBF;
(* HLUTNM = "LUT62_1_15" *)
LUT3 ix37428z1504 (.O (nx37428z1), .I0 (nx38425z2), .I1 (nx38425z3), .I2 (
TSec[0])) ;
defparam ix37428z1504.INIT = 8'hBE;
(* HLUTNM = "LUT62_1_15" *)
LUT3 ix36431z1567 (.O (nx36431z1), .I0 (nx38425z2), .I1 (nx38425z3), .I2 (
TSec[0])) ;
defparam ix36431z1567.INIT = 8'hFD;
(* HLUTNM = "LUT62_1_10" *)
LUT4 ix33440z21761 (.O (nx33440z1), .I0 (nx38425z2), .I1 (nx38425z3), .I2 (
nx38425z4), .I3 (TSec[0])) ;
defparam ix33440z21761.INIT = 16'h4FDF;
LUT6 ix56674z53281 (.O (nx56674z1), .I0 (TMin[6]), .I1 (AMin[6]), .I2 (
Alarmset_int), .I3 (nx58668z2), .I4 (nx58668z3), .I5 (nx58668z5)) ;
defparam ix56674z53281.INIT = 64'hFFFFFF35FFFFCAFF;
LUT6 ix22895z1313 (.O (nx22895z1), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
nx58668z10), .I3 (nx58668z11), .I4 (nx20901z2), .I5 (nx58668z13)) ;
defparam ix22895z1313.INIT = 64'hAEBDFFFFDFFFFFFF;
LUT6 ix25886z5146 (.O (nx25886z1), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
nx58668z10), .I3 (nx58668z11), .I4 (nx20901z2), .I5 (nx58668z13)) ;
defparam ix25886z5146.INIT = 64'hAEBD2C0DDFFF0EF8;
LUT6 ix26883z11282 (.O (nx26883z1), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
nx58668z10), .I3 (nx58668z11), .I4 (nx20901z2), .I5 (nx58668z13)) ;
defparam ix26883z11282.INIT = 64'hF7D77547A6F526F0;
LUT6 ix36455z1313 (.O (nx36455z1), .I0 (THrs[4]), .I1 (AHrs[4]), .I2 (
Alarmset_int), .I3 (nx34461z2), .I4 (nx34461z3), .I5 (nx34461z5)) ;
defparam ix36455z1313.INIT = 64'hFFFFFF3535FFFFFF;
(* HLUTNM = "LUT62_1_3" *)
LUT5 ix39446z55128 (.O (nx39446z1), .I0 (nx34461z2), .I1 (nx34461z3), .I2 (
nx34461z4), .I3 (nx34461z5), .I4 (nx34461z14)) ;
defparam ix39446z55128.INIT = 32'hC636D236;
LUT6 ix15048z1313 (.O (nx15048z1), .I0 (nx34461z6), .I1 (nx34461z10), .I2 (
\a(4) ), .I3 (nx17042z2), .I4 (nx34461z11), .I5 (nx34461z12)) ;
defparam ix15048z1313.INIT = 64'hFFBAFFB3FFFDFFFF;
LUT6 ix28185z65500 (.O (nx28185z1), .I0 (nx17042z2), .I1 (nx34461z6), .I2 (
nx34461z10), .I3 (\a(4) ), .I4 (nx34461z11), .I5 (nx34461z12)) ;
defparam ix28185z65500.INIT = 64'hABEAABAEBAAAFABA;
LUT6 ix7077z64793 (.O (nx7077z1), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
nx58668z10), .I3 (nx58668z11), .I4 (nx58668z13), .I5 (nx20901z2)) ;
defparam ix7077z64793.INIT = 64'h240406D0F7F7F7F7;
(* HLUTNM = "LUT62_1_20" *)
LUT3 ix41745z1558 (.O (nx41745z1), .I0 (nx38425z2), .I1 (nx38425z3), .I2 (
TSec[0])) ;
defparam ix41745z1558.INIT = 8'hF4;
VCC ps_vcc (.P (nx8473z1)) ;
LUT5 ix2982z1058 (.O (nx2982z1), .I0 (nx2982z2), .I1 (AHrs[1]), .I2 (AHrs[0]
), .I3 (Reset_int), .I4 (Hrsadv_int)) ;
defparam ix2982z1058.INIT = 32'hFF80FF00;
LUT3 ix11274z1548 (.O (nx11274z1), .I0 (Reset_int), .I1 (Alarmset_int), .I2 (
Hrsadv_int)) ;
defparam ix11274z1548.INIT = 8'hEA;
LUT5 ix4976z9506 (.O (nx4976z1), .I0 (nx4976z2), .I1 (AMin[2]), .I2 (AMin[1]
), .I3 (AMin[0]), .I4 (Reset_int)) ;
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defparam ix4976z9506.INIT = 32'hFFFF2000;
LUT3 ix39672z1548 (.O (nx39672z1), .I0 (Reset_int), .I1 (Alarmset_int), .I2 (
Minadv_int)) ;
defparam ix39672z1548.INIT = 8'hEA;
(* HLUTNM = "LUT62_1_12" *)
LUT4 ix7286z63027 (.O (nx7286z1), .I0 (nx28360z2), .I1 (nx7286z2), .I2 (
Timeset_int), .I3 (Hrsadv_int)) ;
defparam ix7286z63027.INIT = 16'hF111;
LUT3 ix28360z1527 (.O (nx28360z1), .I0 (nx28360z2), .I1 (Timeset_int), .I2 (
Minadv_int)) ;
defparam ix28360z1527.INIT = 8'hD5;
(* HLUTNM = "LUT62_1_1" *)
LUT5 ix51271z1442 (.O (nx51271z1), .I0 (nx51271z2), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (Reset_int)) ;
defparam ix51271z1442.INIT = 32'hFFFF0080;
(* HLUTNM = "LUT62_1_20" *)
LUT2 ix56256z1325 (.O (nx56256z1), .I0 (Reset_int), .I1 (Timeset_int)) ;
defparam ix56256z1325.INIT = 4'hB;
LUT6 ix13078z54517 (.O (nx13078z1), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix13078z54517.INIT = 64'hF7F7F777CFCBCFD3;
LUT6 ix14075z31905 (.O (nx14075z1), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix14075z31905.INIT = 64'hFEFEFEFE7777777F;
LUT6 ix16069z54517 (.O (nx16069z1), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix16069z54517.INIT = 64'hE7E7E763CFCBCFD3;
LUT6 ix18063z44763 (.O (nx18063z1), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix18063z44763.INIT = 64'h67676763A9A9A9B9;
LUT6 ix19060z62476 (.O (nx19060z1), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix19060z62476.INIT = 64'hC5C7C743EEEAEEEA;
(* HLUTNM = "LUT62_1_11" *)
LUT4 ix35434z29665 (.O (nx35434z1), .I0 (nx38425z2), .I1 (nx38425z3), .I2 (
nx38425z4), .I3 (TSec[0])) ;
defparam ix35434z29665.INIT = 16'h6EBF;
(* HLUTNM = "LUT62_1_10" *)
LUT4 ix32443z29601 (.O (nx32443z1), .I0 (nx38425z2), .I1 (nx38425z3), .I2 (
nx38425z4), .I3 (TSec[0])) ;
defparam ix32443z29601.INIT = 16'h6E7F;
LUT6 ix38425z50444 (.O (nx38425z4), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix38425z50444.INIT = 64'hDEBFFD7BFD7BBFE7;
(* HLUTNM = "LUT62_1_7" *)
LUT5 ix58668z54368 (.O (nx58668z1), .I0 (nx58668z2), .I1 (nx58668z3), .I2 (
nx58668z4), .I3 (nx58668z5), .I4 (nx58668z14)) ;
defparam ix58668z54368.INIT = 32'hFB3BCF3E;
(* HLUTNM = "LUT62_1_7" *)
LUT5 ix57671z64785 (.O (nx57671z1), .I0 (nx58668z2), .I1 (nx58668z3), .I2 (
nx58668z4), .I3 (nx58668z5), .I4 (nx58668z14)) ;
defparam ix57671z64785.INIT = 32'hE7EFF7EF;
(* HLUTNM = "LUT62_1_6" *)
LUT5 ix55677z44288 (.O (nx55677z1), .I0 (nx58668z5), .I1 (nx58668z14), .I2 (
nx58668z2), .I3 (nx58668z3), .I4 (nx58668z4)) ;
defparam ix55677z44288.INIT = 32'hAADDA7DE;
(* HLUTNM = "LUT62_1_6" *)
LUT5 ix53683z43252 (.O (nx53683z1), .I0 (nx58668z5), .I1 (nx58668z14), .I2 (
nx58668z2), .I3 (nx58668z3), .I4 (nx58668z4)) ;
defparam ix53683z43252.INIT = 32'hA50FA3D2;
(* HLUTNM = "LUT62_1_5" *)
LUT5 ix52686z25236 (.O (nx52686z1), .I0 (nx58668z2), .I1 (nx58668z3), .I2 (
nx58668z4), .I3 (nx58668z5), .I4 (nx58668z14)) ;
defparam ix52686z25236.INIT = 32'hFBFA5D72;
LUT6 ix20901z46111 (.O (nx20901z1), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
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nx58668z10), .I3 (nx58668z11), .I4 (nx20901z2), .I5 (nx58668z13)) ;
defparam ix20901z46111.INIT = 64'hDBFFFFDFF9AFAEFD;
LUT6 ix21898z65105 (.O (nx21898z1), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
nx58668z10), .I3 (nx58668z11), .I4 (nx20901z2), .I5 (nx58668z13)) ;
defparam ix21898z65105.INIT = 64'h7D6FDBFB7FFAF92F;
LUT6 ix23892z13338 (.O (nx23892z1), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
nx58668z10), .I3 (nx58668z11), .I4 (nx20901z2), .I5 (nx58668z13)) ;
defparam ix23892z13338.INIT = 64'hDBFF7D4FF9AF2EF8;
(* HLUTNM = "LUT62_1_2" *)
LUT5 ix34461z64128 (.O (nx34461z1), .I0 (nx34461z2), .I1 (nx34461z3), .I2 (
nx34461z4), .I3 (nx34461z5), .I4 (nx34461z14)) ;
defparam ix34461z64128.INIT = 32'hEF5EF55E;
(* HLUTNM = "LUT62_1_2" *)
LUT5 ix35458z1105 (.O (nx35458z1), .I0 (nx34461z5), .I1 (nx34461z14), .I2 (
nx34461z2), .I3 (nx34461z3), .I4 (nx34461z4)) ;
defparam ix35458z1105.INIT = 32'hF53FFF2F;
(* HLUTNM = "LUT62_1_3" *)
LUT5 ix37452z58106 (.O (nx37452z1), .I0 (nx34461z5), .I1 (nx34461z14), .I2 (
nx34461z2), .I3 (nx34461z3), .I4 (nx34461z4)) ;
defparam ix37452z58106.INIT = 32'hAAA3DDD8;
(* HLUTNM = "LUT62_1_4" *)
LUT5 ix40443z30800 (.O (nx40443z1), .I0 (nx34461z2), .I1 (nx34461z3), .I2 (
nx34461z4), .I3 (nx34461z5), .I4 (nx34461z14)) ;
defparam ix40443z30800.INIT = 32'h6F2E732E;
LUT6 ix17042z58077 (.O (nx17042z1), .I0 (nx34461z6), .I1 (nx34461z10), .I2 (
\a(4) ), .I3 (nx17042z2), .I4 (nx34461z11), .I5 (nx34461z12)) ;
defparam ix17042z58077.INIT = 64'hDFE7DEEDD7BFDDBB;
LUT6 ix16045z16656 (.O (nx16045z1), .I0 (nx34461z6), .I1 (nx34461z10), .I2 (
\a(4) ), .I3 (nx17042z2), .I4 (nx34461z11), .I5 (nx34461z12)) ;
defparam ix16045z16656.INIT = 64'hE77DED7FBF7B3BEE;
LUT6 ix14051z53725 (.O (nx14051z1), .I0 (nx34461z6), .I1 (nx34461z10), .I2 (
\a(4) ), .I3 (nx17042z2), .I4 (nx34461z11), .I5 (nx34461z12)) ;
defparam ix14051z53725.INIT = 64'h5DE75EED53BFCCBB;
LUT6 ix12057z53793 (.O (nx12057z1), .I0 (nx34461z6), .I1 (nx34461z10), .I2 (
\a(4) ), .I3 (nx17042z2), .I4 (nx34461z11), .I5 (nx34461z12)) ;
defparam ix12057z53793.INIT = 64'h18BA12B351FDCCFF;
LUT6 ix11060z63251 (.O (nx11060z1), .I0 (nx17042z2), .I1 (nx34461z6), .I2 (
nx34461z10), .I3 (\a(4) ), .I4 (nx34461z11), .I5 (nx34461z12)) ;
defparam ix11060z63251.INIT = 64'h73F773FC731FF1F1;
LUT4 ix45858z34082 (.O (nx45858z1), .I0 (nx45858z2), .I1 (nx45858z4), .I2 (
nx45858z6), .I3 (nx45858z8)) ;
defparam ix45858z34082.INIT = 16'h8000;
LUT5 ix45858z3369 (.O (nx45858z6), .I0 (nx45858z7), .I1 (TMin[6]), .I2 (
TMin[1]), .I3 (AMin[6]), .I4 (AMin[1])) ;
defparam ix45858z3369.INIT = 32'h80200802;
LUT5 ix45858z1320 (.O (nx45858z7), .I0 (TMin[4]), .I1 (THrs[3]), .I2 (
AMin[4]), .I3 (AHrs[3]), .I4 (Alarmon_int)) ;
defparam ix45858z1320.INIT = 32'hC4310000;
LUT6 ix45858z1834 (.O (nx45858z8), .I0 (THrs[6]), .I1 (THrs[2]), .I2 (
THrs[1]), .I3 (AHrs[6]), .I4 (AHrs[2]), .I5 (AHrs[1])) ;
defparam ix45858z1834.INIT = 64'h8040201008040201;
LUT5 ix45858z3375 (.O (nx45858z4), .I0 (nx45858z5), .I1 (TMin[5]), .I2 (
THrs[4]), .I3 (AMin[5]), .I4 (AHrs[4])) ;
defparam ix45858z3375.INIT = 32'h80A0080A;
LUT6 ix45858z5163 (.O (nx45858z5), .I0 (TMin[5]), .I1 (TMin[3]), .I2 (
TMin[0]), .I3 (AMin[5]), .I4 (AMin[3]), .I5 (AMin[0])) ;
defparam ix45858z5163.INIT = 64'hC040F0500C040F05;
LUT5 ix45858z3365 (.O (nx45858z2), .I0 (nx45858z3), .I1 (TMin[2]), .I2 (
THrs[5]), .I3 (AMin[2]), .I4 (AHrs[5])) ;
defparam ix45858z3365.INIT = 32'h80200802;
LUT6 ix45858z1831 (.O (nx45858z3), .I0 (TMin[4]), .I1 (TMin[3]), .I2 (
THrs[0]), .I3 (AMin[4]), .I4 (AMin[3]), .I5 (AHrs[0])) ;
defparam ix45858z1831.INIT = 64'hA0F020300A0F0203;
(* HLUTNM = "LUT62_1_4" *)
LUT5 ix60217z44953 (.O (nx60217z1), .I0 (nx34461z5), .I1 (nx34461z14), .I2 (
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nx34461z2), .I3 (nx34461z3), .I4 (nx34461z4)) ;
defparam ix60217z44953.INIT = 32'h55FAAA77;
(* HLUTNM = "LUT62_1_8" *)
LUT4 ix34461z19059 (.O (nx34461z14), .I0 (nx34461z6), .I1 (nx34461z10), .I2 (
\a(4) ), .I3 (nx34461z12)) ;
defparam ix34461z19059.INIT = 16'h4544;
(* HLUTNM = "LUT62_1_8" *)
LUT5 ix34461z60436 (.O (nx34461z5), .I0 (nx34461z6), .I1 (nx34461z10), .I2 (
\a(4) ), .I3 (nx34461z11), .I4 (nx34461z12)) ;
defparam ix34461z60436.INIT = 32'hE7E7E6EE;
LUT5 ix34461z34581 (.O (nx34461z12), .I0 (\a(4) ), .I1 (nx34461z9), .I2 (
nx34461z3), .I3 (nx34461z4), .I4 (nx34461z13)) ;
defparam ix34461z34581.INIT = 32'h7E1781E8;
LUT6 ix34461z16774 (.O (nx34461z11), .I0 (THrs[4]), .I1 (AHrs[4]), .I2 (
\a(4) ), .I3 (Alarmset_int), .I4 (nx34461z9), .I5 (nx34461z3)) ;
defparam ix34461z16774.INIT = 64'h3C5AC3A5C3A53C5A;
LUT6 ix17042z58403 (.O (nx17042z2), .I0 (nx34461z6), .I1 (nx34461z10), .I2 (
\a(4) ), .I3 (THrs[0]), .I4 (AHrs[0]), .I5 (Alarmset_int)) ;
defparam ix17042z58403.INIT = 64'hDFDF0000DF00DF00;
(* HLUTNM = "LUT62_1_13" *)
LUT5 ix34461z27540 (.O (nx34461z13), .I0 (THrs[6]), .I1 (THrs[2]), .I2 (
AHrs[6]), .I3 (AHrs[2]), .I4 (Alarmset_int)) ;
defparam ix34461z27540.INIT = 32'h0FF06666;
LUT6 ix34461z44756 (.O (nx34461z10), .I0 (nx34461z7), .I1 (nx34461z8), .I2 (
nx34461z2), .I3 (nx34461z9), .I4 (nx34461z3), .I5 (nx34461z4)) ;
defparam ix34461z44756.INIT = 64'h5656566A6AA9A9A9;
LUT6 ix34461z44495 (.O (nx34461z6), .I0 (nx34461z7), .I1 (nx34461z8), .I2 (
nx34461z2), .I3 (nx34461z9), .I4 (nx34461z3), .I5 (nx34461z4)) ;
defparam ix34461z44495.INIT = 64'hFEFEFEEAEAA8A8A8;
LUT3 ix34461z1370 (.O (nx34461z4), .I0 (THrs[4]), .I1 (AHrs[4]), .I2 (
Alarmset_int)) ;
defparam ix34461z1370.INIT = 8'h35;
LUT3 ix34461z1369 (.O (nx34461z3), .I0 (THrs[5]), .I1 (AHrs[5]), .I2 (
Alarmset_int)) ;
defparam ix34461z1369.INIT = 8'h35;
(* HLUTNM = "LUT62_1_17" *)
LUT3 ix34461z1375 (.O (nx34461z9), .I0 (THrs[1]), .I1 (AHrs[1]), .I2 (
Alarmset_int)) ;
defparam ix34461z1375.INIT = 8'h35;
(* HLUTNM = "LUT62_1_13" *)
LUT3 ix34461z1368 (.O (nx34461z2), .I0 (THrs[6]), .I1 (AHrs[6]), .I2 (
Alarmset_int)) ;
defparam ix34461z1368.INIT = 8'h35;
(* HLUTNM = "LUT62_1_16" *)
LUT3 ix34461z1374 (.O (nx34461z8), .I0 (THrs[2]), .I1 (AHrs[2]), .I2 (
Alarmset_int)) ;
defparam ix34461z1374.INIT = 8'h35;
(* HLUTNM = "LUT62_1_16" *)
LUT3 ix34461z1373 (.O (nx34461z7), .I0 (THrs[3]), .I1 (AHrs[3]), .I2 (
Alarmset_int)) ;
defparam ix34461z1373.INIT = 8'h35;
(* HLUTNM = "LUT62_1_5" *)
LUT5 ix49747z24519 (.O (nx49747z1), .I0 (nx58668z5), .I1 (nx58668z14), .I2 (
nx58668z2), .I3 (nx58668z3), .I4 (nx58668z4)) ;
defparam ix49747z24519.INIT = 32'h5FA75AA5;
(* HLUTNM = "LUT62_1_9" *)
LUT4 ix58668z22143 (.O (nx58668z14), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
nx58668z10), .I3 (nx58668z11)) ;
defparam ix58668z22143.INIT = 16'h5150;
(* HLUTNM = "LUT62_1_9" *)
LUT5 ix58668z57632 (.O (nx58668z5), .I0 (nx58668z6), .I1 (\a(4) ), .I2 (
nx58668z10), .I3 (nx58668z11), .I4 (nx58668z13)) ;
defparam ix58668z57632.INIT = 32'hDBDADBFA;
LUT6 ix58668z16776 (.O (nx58668z13), .I0 (TMin[4]), .I1 (AMin[4]), .I2 (
\a(4) ), .I3 (Alarmset_int), .I4 (nx58668z8), .I5 (nx58668z2)) ;
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defparam ix58668z16776.INIT = 64'h3C5AC3A5C3A53C5A;
LUT3 ix20901z1368 (.O (nx20901z2), .I0 (TMin[0]), .I1 (AMin[0]), .I2 (
Alarmset_int)) ;
defparam ix20901z1368.INIT = 8'h35;
LUT5 ix58668z34580 (.O (nx58668z11), .I0 (\a(4) ), .I1 (nx58668z8), .I2 (
nx58668z2), .I3 (nx58668z3), .I4 (nx58668z12)) ;
defparam ix58668z34580.INIT = 32'h7E1781E8;
LUT6 ix58668z44992 (.O (nx58668z10), .I0 (nx58668z7), .I1 (nx58668z8), .I2 (
nx58668z2), .I3 (nx58668z9), .I4 (nx58668z3), .I5 (nx58668z4)) ;
defparam ix58668z44992.INIT = 64'h55566AAA56AAAA95;
(* HLUTNM = "LUT62_1_14" *)
LUT5 ix58668z27539 (.O (nx58668z12), .I0 (TMin[6]), .I1 (TMin[2]), .I2 (
AMin[6]), .I3 (AMin[2]), .I4 (Alarmset_int)) ;
defparam ix58668z27539.INIT = 32'h0FF06666;
LUT6 ix58668z44967 (.O (nx58668z6), .I0 (nx58668z7), .I1 (nx58668z8), .I2 (
nx58668z2), .I3 (nx58668z9), .I4 (nx58668z3), .I5 (nx58668z4)) ;
defparam ix58668z44967.INIT = 64'hFFFEEAAAFEAAAA80;
(* HLUTNM = "LUT62_1_14" *)
LUT3 ix58668z1370 (.O (nx58668z4), .I0 (TMin[6]), .I1 (AMin[6]), .I2 (
Alarmset_int)) ;
defparam ix58668z1370.INIT = 8'h35;
(* HLUTNM = "LUT62_1_18" *)
LUT3 ix58668z1369 (.O (nx58668z3), .I0 (TMin[4]), .I1 (AMin[4]), .I2 (
Alarmset_int)) ;
defparam ix58668z1369.INIT = 8'h35;
(* HLUTNM = "LUT62_1_19" *)
LUT3 ix58668z1375 (.O (nx58668z9), .I0 (TMin[2]), .I1 (AMin[2]), .I2 (
Alarmset_int)) ;
defparam ix58668z1375.INIT = 8'h35;
(* HLUTNM = "LUT62_1_17" *)
LUT3 ix58668z1368 (.O (nx58668z2), .I0 (TMin[5]), .I1 (AMin[5]), .I2 (
Alarmset_int)) ;
defparam ix58668z1368.INIT = 8'h35;
(* HLUTNM = "LUT62_1_19" *)
LUT3 ix58668z1374 (.O (nx58668z8), .I0 (TMin[1]), .I1 (AMin[1]), .I2 (
Alarmset_int)) ;
defparam ix58668z1374.INIT = 8'h35;
(* HLUTNM = "LUT62_1_18" *)
LUT3 ix58668z1373 (.O (nx58668z7), .I0 (TMin[3]), .I1 (AMin[3]), .I2 (
Alarmset_int)) ;
defparam ix58668z1373.INIT = 8'h35;
LUT6 ix925z16336 (.O (nx925z1), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (TSec[3])
, .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix925z16336.INIT = 64'h581A5A9E7ABE3AAE;
LUT6 ix38425z60061 (.O (nx38425z3), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix38425z60061.INIT = 64'hA7E55A9E5A9EE579;
LUT6 ix38425z57694 (.O (nx38425z2), .I0 (TSec[5]), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (TSec[1]), .I5 (\a(4) )) ;
defparam ix38425z57694.INIT = 64'h63DCC6BDC6BDDC3B;
LUT6 ix2982z1315 (.O (nx2982z2), .I0 (AHrs[6]), .I1 (AHrs[5]), .I2 (AHrs[4])
, .I3 (AHrs[3]), .I4 (AHrs[2]), .I5 (Alarmset_int)) ;
defparam ix2982z1315.INIT = 64'h0010000000000000;
LUT6 ix4976z1315 (.O (nx4976z2), .I0 (AMin[6]), .I1 (AMin[5]), .I2 (AMin[4])
, .I3 (AMin[3]), .I4 (Alarmset_int), .I5 (Minadv_int)) ;
defparam ix4976z1315.INIT = 64'h4000000000000000;
LUT6 ix6970z1314 (.O (nx6970z1), .I0 (nx6970z2), .I1 (nx6970z3), .I2 (
THrs[3]), .I3 (THrs[1]), .I4 (THrs[0]), .I5 (Reset_int)) ;
defparam ix6970z1314.INIT = 64'h5555555504000000;
LUT4 ix6970z5412 (.O (nx6970z3), .I0 (THrs[6]), .I1 (THrs[5]), .I2 (THrs[4])
, .I3 (THrs[2])) ;
defparam ix6970z5412.INIT = 16'h1000;
(* HLUTNM = "LUT62_1_12" *)
LUT4 ix6970z5137 (.O (nx6970z2), .I0 (nx28360z2), .I1 (nx7286z2), .I2 (
Timeset_int), .I3 (Hrsadv_int)) ;
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defparam ix6970z5137.INIT = 16'h0EEE;
LUT6 ix54956z1058 (.O (nx54956z1), .I0 (TMin[3]), .I1 (TMin[1]), .I2 (
TMin[0]), .I3 (Reset_int), .I4 (nx54956z2), .I5 (nx54956z3)) ;
defparam ix54956z1058.INIT = 64'h0000FF800000FF00;
LUT5 ix7286z34082 (.O (nx7286z2), .I0 (nx54956z3), .I1 (TMin[3]), .I2 (
TMin[1]), .I3 (TMin[0]), .I4 (Reset_int)) ;
defparam ix7286z34082.INIT = 32'h00007FFF;
LUT4 ix54956z1380 (.O (nx54956z3), .I0 (TMin[6]), .I1 (TMin[5]), .I2 (
TMin[4]), .I3 (TMin[2])) ;
defparam ix54956z1380.INIT = 16'h0040;
LUT3 ix54956z1357 (.O (nx54956z2), .I0 (nx28360z2), .I1 (Timeset_int), .I2 (
Minadv_int)) ;
defparam ix54956z1357.INIT = 8'h2A;
(* HLUTNM = "LUT62_1_1" *)
LUT5 ix28360z1186 (.O (nx28360z2), .I0 (nx51271z2), .I1 (TSec[4]), .I2 (
TSec[3]), .I3 (TSec[2]), .I4 (Reset_int)) ;
defparam ix28360z1186.INIT = 32'h0000FF7F;
LUT5 ix51271z1443 (.O (nx51271z2), .I0 (TSec[5]), .I1 (TSec[1]), .I2 (
TSec[0]), .I3 (\a(4) ), .I4 (Timeset_int)) ;
defparam ix51271z1443.INIT = 32'h00000080;
BUFGP Pulse_ibuf (.O (Pulse_int), .I (Pulse)) ;
MUXCY muxcy_0 (.O (nx8474z1), .CI (nx8473z1), .DI (\a(4) ), .S (TSec[0])) ;
MUXCY muxcy_1 (.O (nx8475z1), .CI (nx8474z1), .DI (\a(4) ), .S (TSec[1])) ;
MUXCY muxcy_2 (.O (nx8476z1), .CI (nx8475z1), .DI (\a(4) ), .S (TSec[2])) ;
MUXCY muxcy_3 (.O (nx8477z1), .CI (nx8476z1), .DI (\a(4) ), .S (TSec[3])) ;
MUXCY muxcy_4 (.O (nx51683z1), .CI (nx8477z1), .DI (\a(4) ), .S (TSec[4])
) ;
MUXCY muxcy_0_1_8 (.O (nx63830z1), .CI (nx8473z1), .DI (\a(4) ), .S (
TMin[0])) ;
MUXCY muxcy_1_1_10 (.O (nx6079z1), .CI (nx63830z1), .DI (\a(4) ), .S (
TMin[1])) ;
MUXCY muxcy_2_1_12 (.O (nx51672z1), .CI (nx6079z1), .DI (\a(4) ), .S (
TMin[2])) ;
MUXCY muxcy_3_1_14 (.O (nx43887z1), .CI (nx51672z1), .DI (\a(4) ), .S (
TMin[3])) ;
MUXCY muxcy_4_1_16 (.O (nx58480z1), .CI (nx43887z1), .DI (\a(4) ), .S (
TMin[4])) ;
MUXCY muxcy_5_1_18 (.O (nx50695z1), .CI (nx58480z1), .DI (\a(4) ), .S (
TMin[5])) ;
MUXCY muxcy_0_1_28 (.O (nx61836z1), .CI (nx8473z1), .DI (\a(4) ), .S (
THrs[0])) ;
MUXCY muxcy_1_1_30 (.O (nx4085z1), .CI (nx61836z1), .DI (\a(4) ), .S (
THrs[1])) ;
MUXCY muxcy_2_1_32 (.O (nx53666z1), .CI (nx4085z1), .DI (\a(4) ), .S (
THrs[2])) ;
MUXCY muxcy_3_1_34 (.O (nx45881z1), .CI (nx53666z1), .DI (\a(4) ), .S (
THrs[3])) ;
MUXCY muxcy_4_1_36 (.O (nx38096z1), .CI (nx45881z1), .DI (\a(4) ), .S (
THrs[4])) ;
MUXCY muxcy_5_1_38 (.O (nx52689z1), .CI (nx38096z1), .DI (\a(4) ), .S (
THrs[5])) ;
MUXCY muxcy_0_1_48 (.O (nx59842z1), .CI (nx8473z1), .DI (\a(4) ), .S (
AMin[0])) ;
MUXCY muxcy_1_1_50 (.O (nx2091z1), .CI (nx59842z1), .DI (\a(4) ), .S (
AMin[1])) ;
MUXCY muxcy_2_1_52 (.O (nx55660z1), .CI (nx2091z1), .DI (\a(4) ), .S (
AMin[2])) ;
MUXCY muxcy_3_1_54 (.O (nx47875z1), .CI (nx55660z1), .DI (\a(4) ), .S (
AMin[3])) ;
MUXCY muxcy_4_1_56 (.O (nx62468z1), .CI (nx47875z1), .DI (\a(4) ), .S (
AMin[4])) ;
MUXCY muxcy_5_1_58 (.O (nx54683z1), .CI (nx62468z1), .DI (\a(4) ), .S (
AMin[5])) ;
MUXCY muxcy_0_1_68 (.O (nx57848z1), .CI (nx8473z1), .DI (\a(4) ), .S (
AHrs[0])) ;
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MUXCY muxcy_1_1_70 (.O (nx97z1), .CI (nx57848z1), .DI (\a(4) ), .S (AHrs[1]
)) ;
MUXCY muxcy_2_1_72 (.O (nx57654z1), .CI (nx97z1), .DI (\a(4) ), .S (AHrs[2]
)) ;
MUXCY muxcy_3_1_74 (.O (nx49869z1), .CI (nx57654z1), .DI (\a(4) ), .S (
AHrs[3])) ;
MUXCY muxcy_4_1_76 (.O (nx42084z1), .CI (nx49869z1), .DI (\a(4) ), .S (
AHrs[4])) ;
MUXCY muxcy_5_1_78 (.O (nx56677z1), .CI (nx42084z1), .DI (\a(4) ), .S (
AHrs[5])) ;
endmodule
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