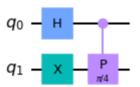
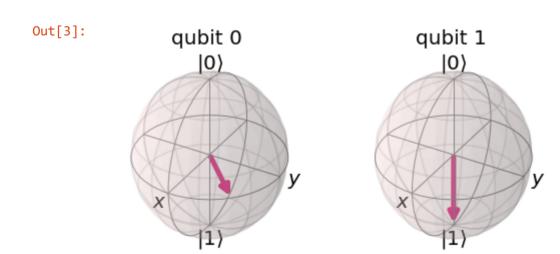
### Phase Kickback

### **Solutions**

Please note that the circuits follow qiskit's ordering of qubits: topmost qubit in the circuit is written in the left most place.

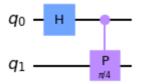


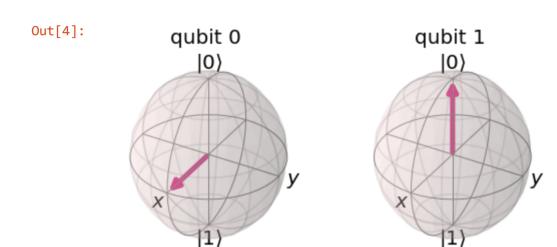


## **Quick Exercises**

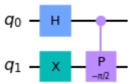
1. What would be the resulting state of the control qubit  $(q_0)$  if the target qubit

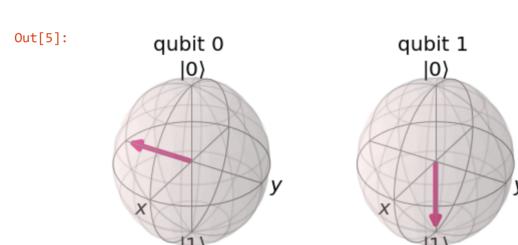
# $(q_1)$ was in the state $|0\rangle$ ? (as shown in the circuit below)? Use Qiskit to check your answer.



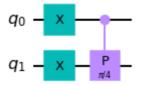


2. What would happen to the control qubit  $(q_0)$  if the if the target qubit  $(q_1)$  was in the state  $|1\rangle$ , and the circuit used a controlled-Sdg gate instead of the controlled-T (as shown in the circuit below)?

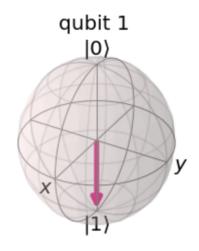




3. What would happen to the control qubit  $(q_0)$  if it was in the state  $|1\rangle$  instead of the state  $|+\rangle$  before application of the controlled-T (as shown in the circuit below)?



# Out[6]: qubit 0 |0) y



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