**Project Name: Test  
Tape-out Date: 11-20-2016  
Preparer: Jon**

|  |  |  |  |
| --- | --- | --- | --- |
| File name | Description | File size (bytes) | Date/Time |
| PTN5100\_A0\_24NOV14.streamout.log |  | 892100 | 2016-10-22 06:09:56 |
| PTN5100\_A0\_24NOV14.drc\_errors.ascii |  | 488122 | 2016-10-19 09:51:42 |
| PTN5100\_A0\_24NOV14.cksum |  | 45 | 2016-10-19 09:51:42 |
| PTN5100\_A0\_24NOV14.gds |  | 154284032 | 2016-10-19 09:51:44 |
| PTN5100\_A0\_24NOV14.rep.cls |  | 1445224 | 2016-10-19 09:51:44 |
| PTN5100\_A0\_24NOV14.sum\_erc |  | 29580 | 2016-10-19 09:51:44 |
| PTN5100\_A0\_24NOV14.sum |  | 162956 | 2016-10-19 09:51:44 |
| sample\_picture2.png |  | 10916 | 2016-10-19 09:51:44 |
| summary.docx |  | 363492 | 2016-11-20 23:18:20 |
| sample\_picture1.png |  | 317669 | 2016-10-19 09:51:44 |
| pvsuierc.log |  | 2637687 | 2016-10-30 17:40:30 |

**DRC:**

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*  
  
\*\*\* PVS HDRC SUMMARY  
  
\*\*\*  
  
Execute on Date/Time : Mon Nov 24 15:22:05 2014  
  
PVS HDRC VERSION : 13.1.1-s107 Mon Jun 23 15:00:47 PDT 2014  
  
Rule Deck Path : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/drc/.config.rul  
  
Rule Deck Title : "PcCMOS14P\_5M4S1UPTP 6.5.3 Friday July 4 2014 R&D/Design Platforms/Foundation IP/PDK"  
  
Layout System : GDSII  
  
Layout Path : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/drc/PTN5100\_A0\_24NOV14.gds  
  
Current Directory : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/drc  
  
User Name: : nxp72977  
  
Maximum Results : 1000  
  
Maximum Result Vertices : 4096  
  
Layout Depth : ALL  
  
Text Depth : 0  
  
Summary File : PTN5100\_A0\_24NOV14.sum  
  
Geometry Flagging : ACUTE = YES ANGLED = NO SKEW = YES OFFGRID = YES  
  
 NONSIMPLE POLYGON = YES NONSIMPLE PATH = NO  
  
Keep Empty Checks : YES  
  
Excluded Cells :   
  
Layout Primary Cell : PTN5100\_A0\_24NOV14  
  
ASCII DRC Error Report : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/drc/PTN5100\_A0\_24NOV14.drc\_errors.ascii  
  
DRC Results Database :  
  
--------------------------------------------------------------------------------

**DRC Errors:**

ACTIVE.1.7.1  
  
208 208 3 Nov 24 15:22:43 2014  
  
Rule File Pathname: /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/drc/.config.rul  
  
Rule File Title: "PcCMOS14P\_5M4S1UPTP 6.5.3 Friday July 4 2014 R&D/Design Platforms/Foundation IP/PDK"  
  
Rule: ACTIVE.1.7.1, 135, Minimum internal ACTIVE angle in EDMOS transistor   
  
  
PWELLPROT.0.4  
  
26 26 3 Nov 24 15:23:41 2014  
  
Rule File Pathname: /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/drc/.config.rul  
  
Rule File Title: "PcCMOS14P\_5M4S1UPTP 6.5.3 Friday July 4 2014 R&D/Design Platforms/Foundation IP/PDK"  
  
Rule: PWELLPROT.0.4, PWELLPROT interacting with more than one (n-type well + N+ACTIVE) is not allowed

**LVS:**

##############################################################################################################  
  
#####   
  
##### PVS LVS COMPARISON  
  
#####   
  
##### Version : 13.1.1-s107  
  
##### NVN Run Start : Mon Nov 24 16:02:49 2014  
  
##### Report File : PTN5100\_A0\_24NOV14.rep.cls  
  
##### Extract Report : PTN5100\_A0\_24NOV14.rep  
  
##### Top Cell : PTN5100\_A0\_24NOV14 <vs> VICTORIA  
  
#####   
  
##### Run Result : MATCH  
  
#####   
  
##### Run Summary : [INFO] Extraction Clean  
  
##### : [INFO] Some Sections Have Been Truncated  
  
#####   
  
##### Layout Design : Victoria\_lib PTN5100\_A0\_24NOV14 layout  
  
##### Schematic File : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/lvs/netlist (cdl)  
  
##### Schematic File : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/lvs/netlist (verilog)  
  
##### Rules File : .config.rul .technology.rul  
  
##### HCell File : /home/Victoria\_workareas/nxp72977/PVS\_LVS/vic\_flat.hcell  
  
##### Pin Swap File : PTN5100\_A0\_24NOV14.rep.cps  
  
#####   
  
##### Extraction CPU Time : 0h 1m 55s - (115s)  
  
##### Extraction Exec Time : 0h 2m 22s - (142s)  
  
##### Extraction Peak Memory Usage : 402.00MB  
  
##### NVN CPU Time : 0h 0m 5s - (5s)   
  
##### NVN Exec Time : 0h 0m 16s - (16s)   
  
##### NVN Peak Memory Usage : 307.54MB  
  
##### LVS Total CPU Time : 0h 2m 0s - (120s)   
  
##### LVS Total Exec Time : 0h 2m 38s - (158s)   
  
##### LVS Total Peak Memory Usage : 402.00MB  
  
#####   
  
##############################################################################################################

**ERC:**

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*  
  
\*\*\* PVS HERC SUMMARY  
  
\*\*\*  
  
Execute on Date/Time : Mon Nov 24 14:10:37 2014  
  
PVS HERC VERSION : 13.1.1-s107 Mon Jun 23 15:00:47 PDT 2014  
  
Rule Deck Path : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/erc/.config.rul  
  
Rule Deck Title : "PcCMOS14P\_5M4S1UPTP 6.5.3 Friday July 4 2014 R&D/Design Platforms/Foundation IP/PDK"   
  
Layout System : GDSII  
  
Layout Path : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/erc/PTN5100\_A0\_24NOV14.gds  
  
Current Directory : /home/Victoria\_scratch/nxp72977/rundir\_pvs/PTN5100\_A0\_24NOV14/erc  
  
User Name: : nxp72977  
  
Maximum Results : 1000  
  
Maximum Result Vertices : 4096  
  
Layout Depth : ALL  
  
Text Depth : 0  
  
Summary File : PTN5100\_A0\_24NOV14.sum  
  
Geometry Flagging : ACUTE = YES ANGLED = NO SKEW = YES OFFGRID = YES  
  
 NONSIMPLE POLYGON = YES NONSIMPLE PATH = NO  
  
Keep Empty Checks : YES  
  
Excluded Cells :   
  
Layout Primary Cell : PTN5100\_A0\_24NOV14  
  
ASCII ERC Error Report : /home/Victoria\_workareas/nxp72977/PTN5100\_A0\_24NOV14.erc\_errors.ascii  
  
ERC Results Database :  
  
--------------------------------------------------------------------------------

--- SUMMARY  
  
---  
  
Total CPU Time : 38(s)  
  
Total Real Time : 61(s)  
  
Peak Memory Used : 163(M)  
  
Total Original Geometry : 3063938 (28195671)  
  
Total ERC RuleChecks : 14  
  
Total ERC Results : 7 (43)

**ERC Errors:**

RULE ERC.1 {  
  
 CAPTION Rule: ERC.1, Nwell or Ndrift not connected ;  
  
 COPY nwellFLOAT;  
  
}

**Images:**



