## Total (12 pts)

Problems 1.3, 1.4, 1.5, 1.14 (3 pts each)

Reading assignment: Section 1.1 to 1.4 (all sub-sections).

Advance reading assignment: Section 2.1 through 2.4 (all subsections)

1.3. What is the 16-bit FP number representation of -5.375 in hex with 1-bit sign, 4-bit biased exponent, and 11-bit fraction, where bias offset = 7?

$$5_{10} = 101_{2}$$
 $0.375_{10} = 011_{2}$ 
 $-5.375_{10} = -101.011_{2}$ 

$$-101.011_2 = -1.01011 \times 2^2$$
  
  $2+7=9_{10}=1001_2$ 

1.4. What is the real number equivalent to FP number 0x3400 with 1-bit sign, 4-bit biased exponent, 11-bit fraction, and bias offset = 7?

$$0 \times 3400 = 00110100 0000 0000$$
 $0110_2 = 6_{10} = E$ 

$$0.11_{2} = 0 \times 2^{\circ} + 1 \times 2^{1} + 1 \times 2^{2}$$

$$= 0 + 0.5 + 0.25$$

$$= 0.75$$

$$e = E - 7 = 6 - 7 = -1$$
  
1. martiesa × 2°  
1.1000000000000 × 2' = 0.11<sub>2</sub>

1.5. What is the real number equivalent to FP number 0x3400 with 1-bit sign, 4-bit biased exponent, 11-bit fraction, and bias offset = 8?

(B)

$$1.1 \times 2^{-2} = 0.011_2$$

$$0.011_{2} = 0 \times 2^{\circ} + 0 \times 2^{\circ} + 1 \times 2^{\circ} + 1 \times 2^{\circ}$$

$$0 + 0 + 0.25 + 0.125$$

$$= 0.375$$

## 1.14 What is a Von Neumann architecture bottleneck?

A Von Neumann architecture bottleneck is a bottleneck between a faster processor and slower memory