

Name:— Aakriti Singh

University Roll No. 2101002

Student Id:— 21712108

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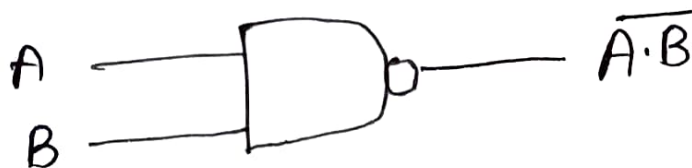
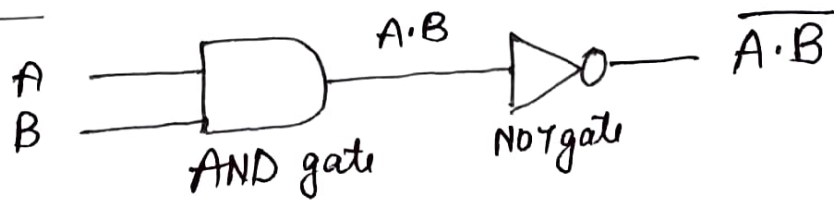
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Ques1. Discuss the working of NAND gate with the help of circuit diagram

⇒ A NAND gate (not AND gate) is a logic gate that produce a low output (0) only if all its input are true & high output (1) otherwise.

- ① Hence the NAND gate is inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate.
- ② Just like an AND gate, a NAND gate may have any number of input but only one output.

Diagram:-



Input		Output
A	B	$X = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

