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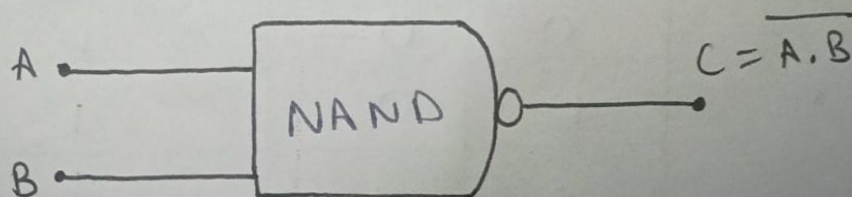
Q1 \rightarrow Discuss the working of NAND gate with the help of circuit diagram and truth table?

Solⁿ \rightarrow The logic NAND gate is generally called as a (Universal gate) because it is one of the most used logic gate types all gates can be implemented by NAND gate. Boolean expression

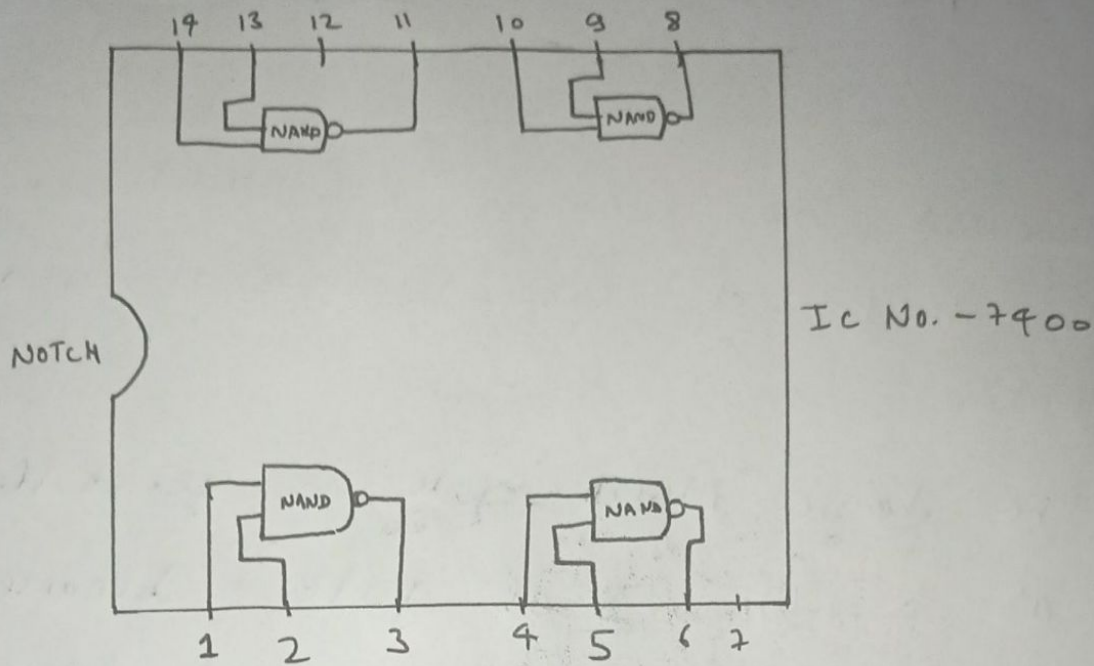
$C = \overline{A \cdot B}$ The NAND gate has an output low(0) when all inputs are high(1)

It is invert of AND gate

(*) Logic Diagram



* Circuit Diagram →



Procedure

- * connect the VCC and ground to the pins specified by data sheet
- * Do all the connection as per IC Diagram
- * Switch ON the power supply.
- * After completing the connection properly observe the outputs of and match in it with the given truth-table

* Truth-Table →

Inputs		Output
A	B	$C = \overline{A \cdot B}$
0	0	1
1	0	1
0	1	1
1	1	0