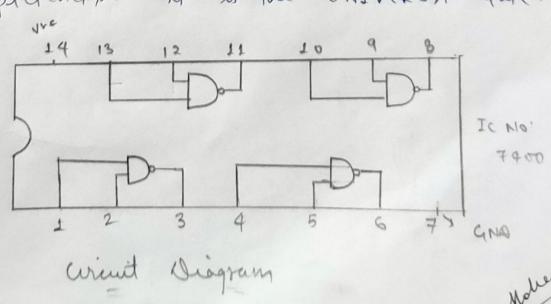
Name! - Mohan Chand Ramela Pather's name! - Ram Chand Ramola ROUNO: - 2101121 Student ID: - 21711034 Section: - B

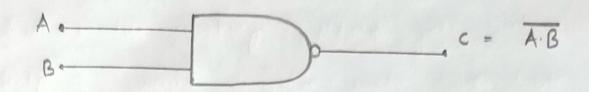
Q:17 Discuss the working of NAND gate with the help of circuit diagram with touth table.

NAND Gote: The NAND gate is a combination of an AND gate and NOT gate. They are connected in capcade form. It is also called Negated And gate. The NAND gate provides the false or lew output only when their outputs is high or tone. The NAND gate is epsential because different types of a boolean function are implemented by uping it. slade when the

> The HAND gate has the property of functional completeness. It is the UNIVERSAL gate.



* logie Dragram: -



Touth table !-

The sections	Output
В	C = A.B
0	1
0	1
1	1
1	0
	B 0 0 1

Repult:- The repult of the enperiment has been verified Eucle & fully.

Mahan