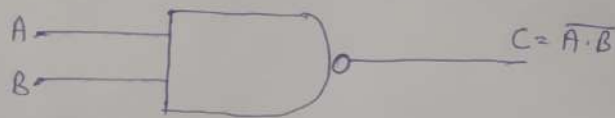Name → Shubham Nakdiyal
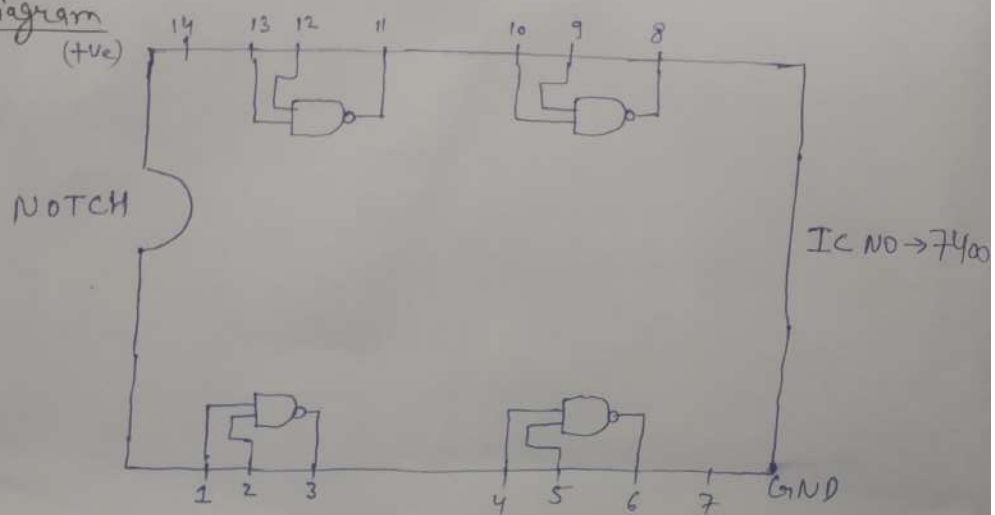
Section → B (MCA)

University roll no → 2101215

Student ID → 21711014

NAND → The logic NAND gate is generally called as a "UNIVERSAL GATE", because it is one of the most used logic gate types all gates can be implemented by NAND gate. Boolean Expression $Q = \overline{A \cdot B}$. The NAND gate has an output low (0) only when all input are high (1). It is inverter of AND.

Logic Diagram →

$$C = \overline{A \cdot B}$$

IC diagram

(+Ve)

NOTCH

IC NO → 7400

GND

Truth table

Input		output
A	B	$C = \overline{A \cdot B}$
0	0	1
1	0	1
0	1	1
1	1	0

Precaution → All the connection should be proper accordingly that Circuit diagram.

⊙ Small switch off the power supply immediately.

☺

Result → The result of the experiment has been verified successfully.