Name         :   Atul Bisht
Class        :   MCA 1st sem.
Roll NO.     :   2101043  , class Roll No: 17
Section      :   B
Student ID:      21561019
Subject      :   Computer Organization & Architecture

Ques. No. ① →

## NAND GATE

The NAND GATE is a combination of an AND Gate and NOT GATE. They are connected in cascade form. It is also called Negated AND gate. The NAND gate provides the false or low output only when their output is high or true.

The NAND gate is essential because different types of a boolean function are implemented by using it.

The NAND gate has the property of functional completeness. The functional completeness means any types of gates can be implemented by using the NAND gate. It performs the function of OR, NOR and AND gate.
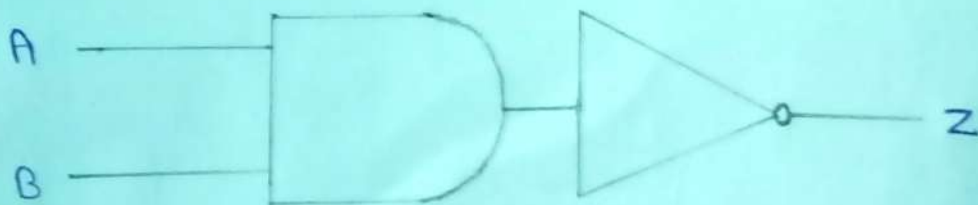
Atul

Name        Atul Bisht
class       MCA18
Rall No:    261043
class Roll No: 17
Student ID :. 2156|019

②

The logic symbol for the gate is below:



The logic circuit of the NAND gate is shown below:



from the logic circuit, the output can be expressed as:

$$Z = \overline{A.B}$$

The equation is read as "Z equals NOT A AND B". Since the logic circuit involves an AND gate followed by an inverter.

Atul

Name : Atul Bisht     ③
class : MCA 1st sem, B
Univ. Roll No. : 2101043
Class Roll No. : 17
Student ID : 21561019

The output can only be low when both the inputs are high.

The Truth table of the NAND gate is given below:

| A | B | Z |
|---|---|---|
| 0 | 0 | 1 |
| 0 | 1 | 1 |
| 1 | 0 | 1 |
| 1 | 1 | 0 |

From the truth table of the gate, it is clear that all the inputs must be high to get a low output and if any of the output is low, the output obtained will be high. If any one of the input is also high the output will be high that is 1.

Atul