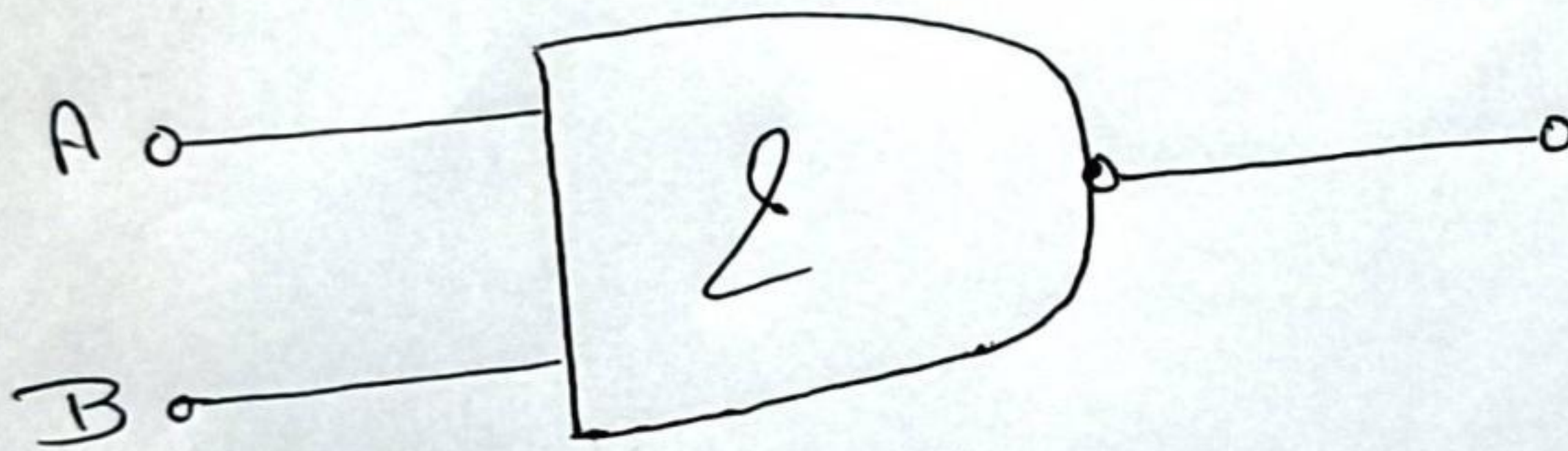Shubham Dhondiyal

Section B

1.

The NAND Gate is a combination of a digital logic AND Gate and a NOT Gate connected together in series.

The NAND gate Has an output that is normally a logic level "1" and only goes "LOW" to logic level "0" when all of its inputs are at logic level "1". The Logic NAND Gate is the reverse or Complementary form of the AND gate we Have seen previously.

Input Logic NAND Gate

Truth table

B	A	Q
0	0	1
0	1	1
1	0	1
1	1	0

Circuit diagram

Vcc	4B	4A	4Y	3B	3A	3Y
14	13	12	11	10	9	8

1	2	3	4	5	6	7
1A	1B	1Y	2A	2B	2Y	GND