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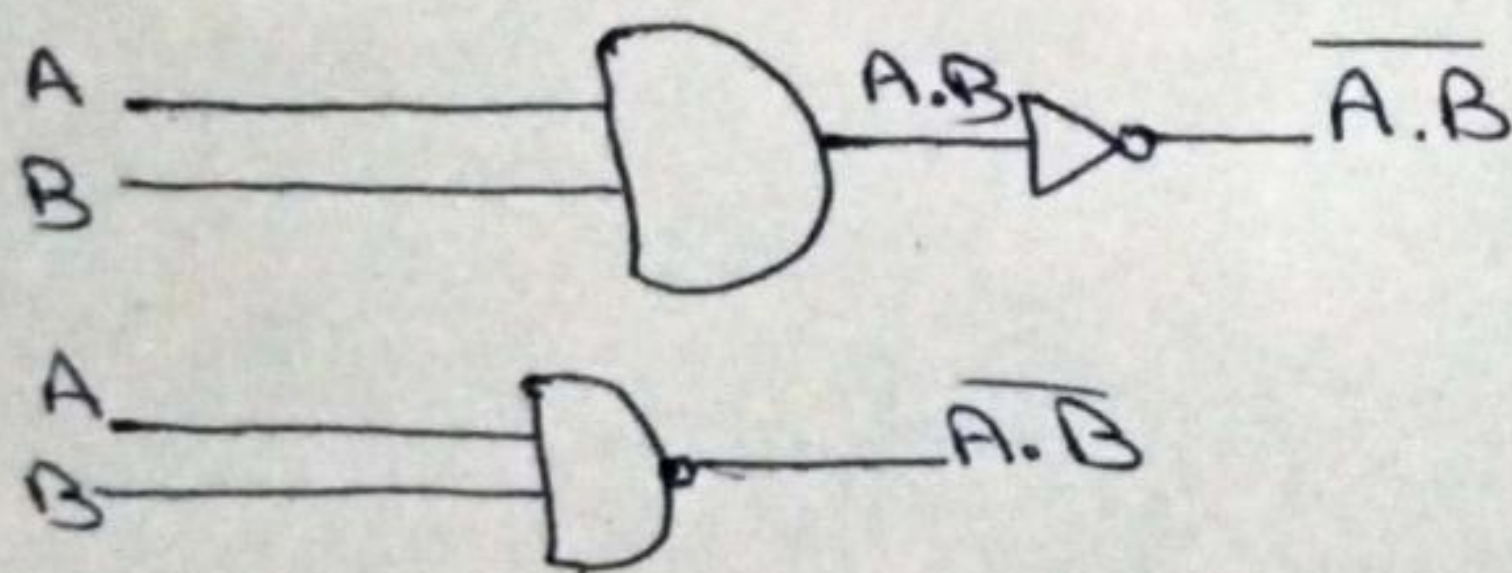
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MCA 1st

1- NAND Gate

It is a logic gate that produces a low output (0) only if all its inputs are true, and high output (1) otherwise. Hence the NAND gate is the inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate.

NAND gate performs the logical NAND operation. NAND gates are known as universal gates, which means they are a type of logic gate which can implement any Boolean function without the need of any other gate type.



Truth Table

A	B	$X = \overline{A.B}$
0	0	1
0	1	1
1	0	1
1	1	0