Name - Paras Panwar
Course - MCA A 1st

University Roll no → 2101141
Rollno - 20

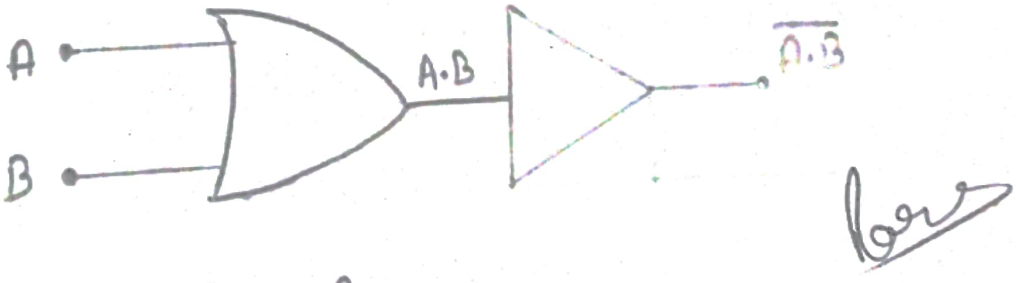
Std → 2171117

Nor gate - It is a universal gate. It is a combination of NOT-OR gate. The output is high only when all the input are low.

The logical Or Boolean expression for the NOR gate is the complement of logical multiplication of input denoted by the plus sign as

$(A+B)' = Y$

The value 'Y' will be true when all of its inputs are set to 0.



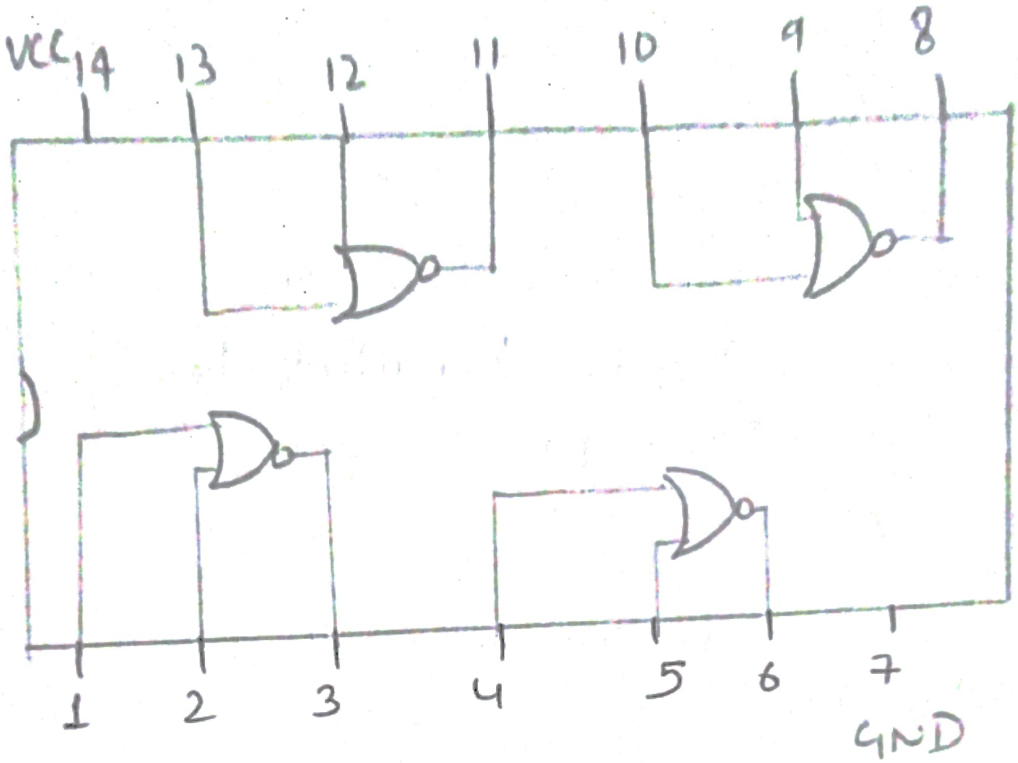
low

NOR

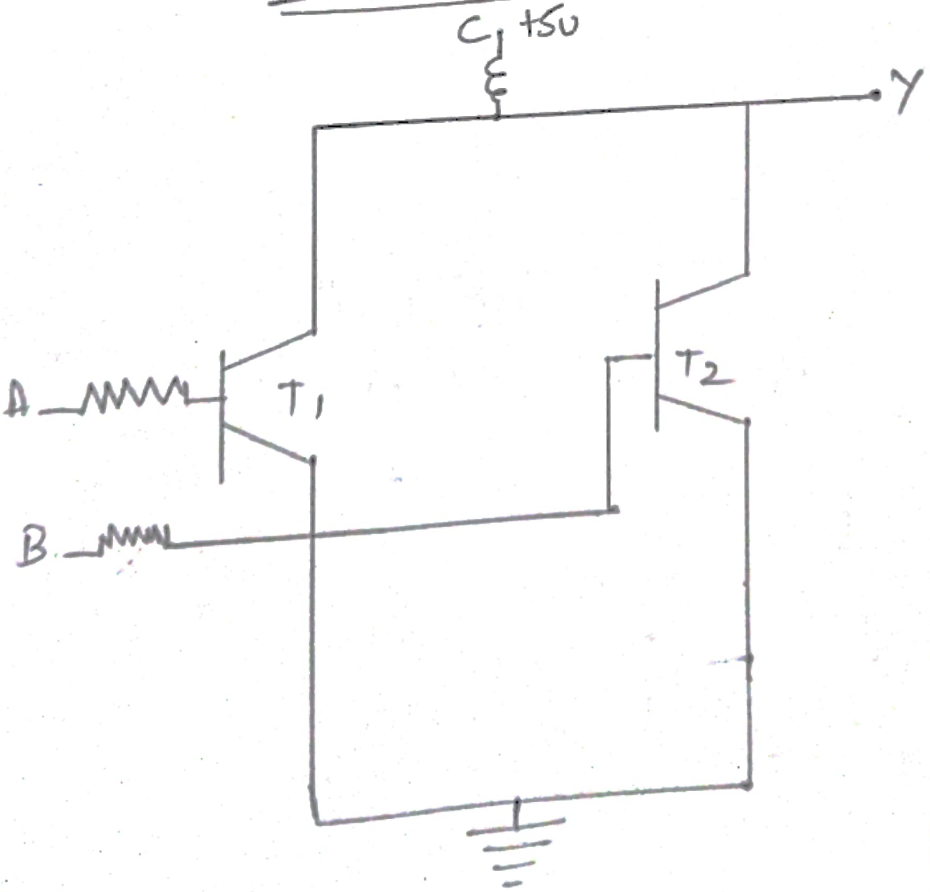
Input			Output
A	B	A.B	$Y = \overline{A.B}$
0	0	0	1
0	1	1	0
1	0	1	0
1	1	1	0

Name - Paras Rawat
Course - MCA A 1st

University
Roll no → 2101141 Std → 21711117
Rollno - 20



IC-7402 NORGATE



Circuit Diagram