

Sakshi Manral.

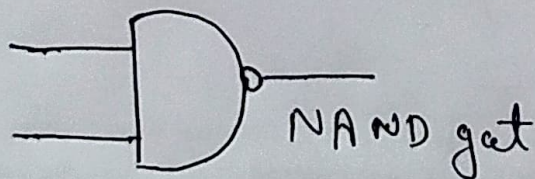
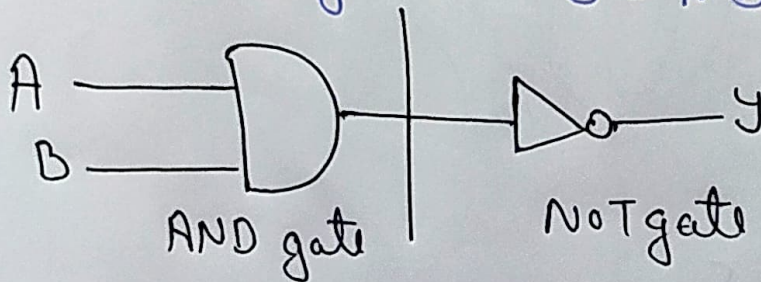
Roll no:- 2101262, Student id:- 21712287

Mid term bratical of Computer Organization.

Ans. 1} NAND gate:- It is a logic gate which provides produces an output is false only if all its input are true.

Symbol and truth table of NAND gate :-

The Symbol of the NAND gate is represented as a combination of AND gate and NOT gate. The Boolean expression is given as $Y = \overline{A \cdot B}$



Truth Table of NAND gate

A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

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