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course: MCA  
Sec — B

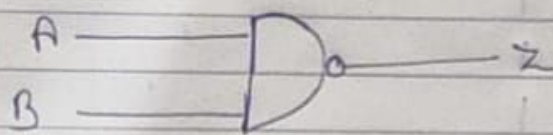
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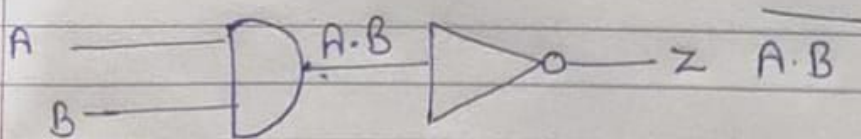
Q1 Discuss the working of NAND gate

The term NAND gate is a contraction of NOT-AND and implies an AND function with complemented (inverted) output

In this gate, output is low when all inputs are high.



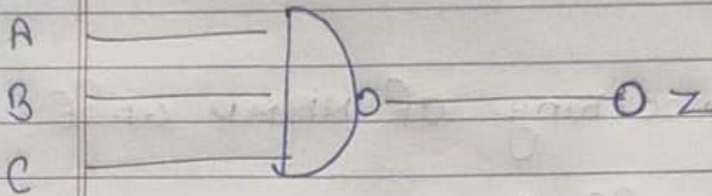
It can also represent as



TRUTH TABLE

A	B	Z
0	0	1
0	1	1
1	0	1
1	1	0

\* 3 input NAND GATE



TRUTH TABLE

A	B	C	Z
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0