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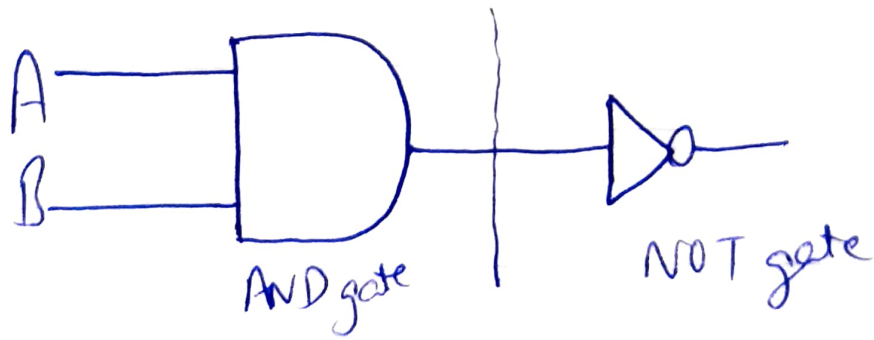
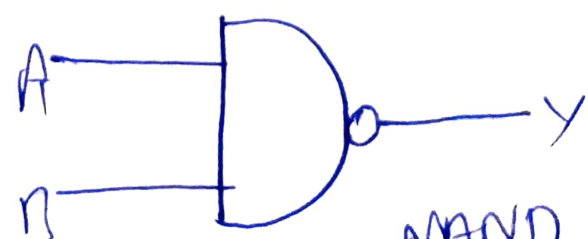
Q1 The NAND gate or "Not AND" gate is the combination of two basic logic gates, the AND gate and the NOT gate connected in series. The NAND gate is a universal gate since the combination of these gates can be used to accomplish any of the basic operations.

The output of a NAND gate is high when either of the inputs is high or if both the inputs are low. In other words, the output is always high & goes low only when both the inputs are high.

Boolean expression ⇒ $Y = \overline{A \cdot B}$





NAND gate.

Truth table

I/P		O/P
A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

IC- diagram of NAND gate.