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Course - MCA

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Answer 1:- Aim:- To study of logic NAND gate

Apparatus Required:- NAND gate IC, power supply, Hookup wires and bread board etc.

Theory:- NAND gate is actually series of AND gate with NOT gate. If we connect the output of and AND gate to the input of a NOT gate, this combination will work as NOT-AND or NAND gate.

It output is 1 when any or all input are 0, otherwise output is 0

The logic NAND gate is generally called as a "UNIVERSAL GATE" because it is one of the most used logic gate types.

Manisha

Procedure :- \* Connect the VCC and ground to the pins specified by the data sheet.

- \* Do all the combinations as per the IC diagram switch on the power supply.
- \* After completing the connections, properly observe the output and match it with the given truth table

Result :- The Result of the experiment has been verified successfully.

Precautions :- All the connections should be properly according the circuit diagram.

- \* Handle the IC's carefully.
- \* In case of any caution or burning smell switch of the power supply immediately.

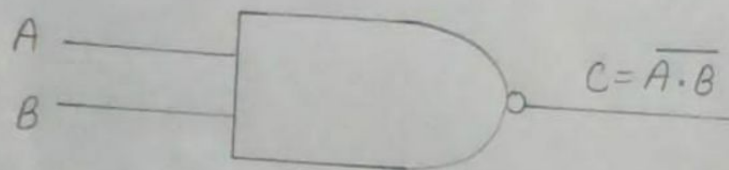
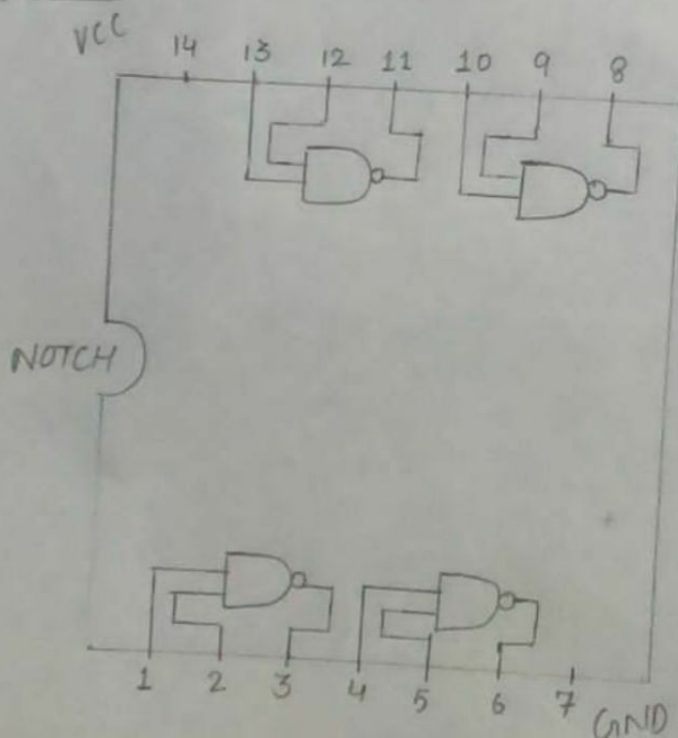
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Truth Table

Input		Output
A	B	$C = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

Logic DiagramIC Diagram :-IC-7400

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