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Date 22/05/2022

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Course MCA

Sec A A

Sub - Computer Organisation & Architecture
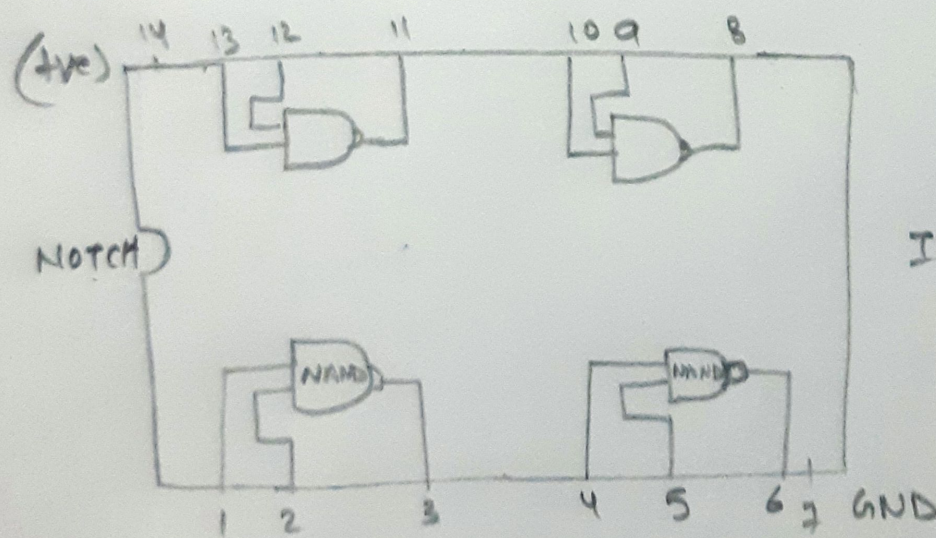
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Sem - I st

Ans 1) The NAND gate or "Not AND" gate is the combination of two basic logic gates, the AND gate and the NOT gate Connected in series.

The output of a NAND gate is high when either of the inputs is high or if the bothe inputs are low.

The logic NAND function is given by the Boolean expression $y = \overline{A \cdot B}$. Here A, B are the inputs and y is the output.

Circuit diagram

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Truth Table of NAND Gate

Inputs		Output
A	B	$Y = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

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