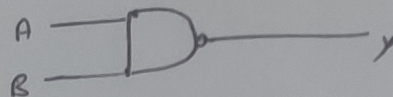Name- Anurag Rawat

Univ. Roll no. 2101033

Student ID- 21711266

Course- MCA 1D
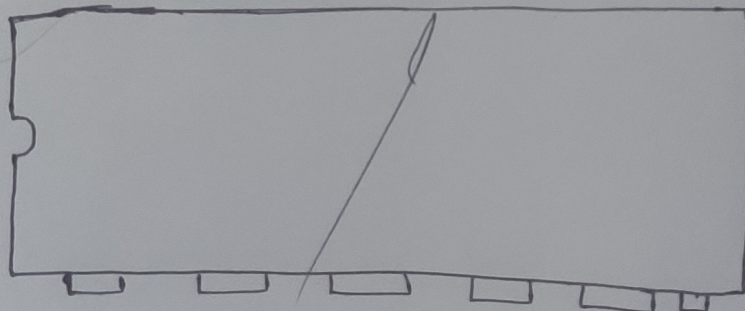
Subject- Computer Organization and Architecture

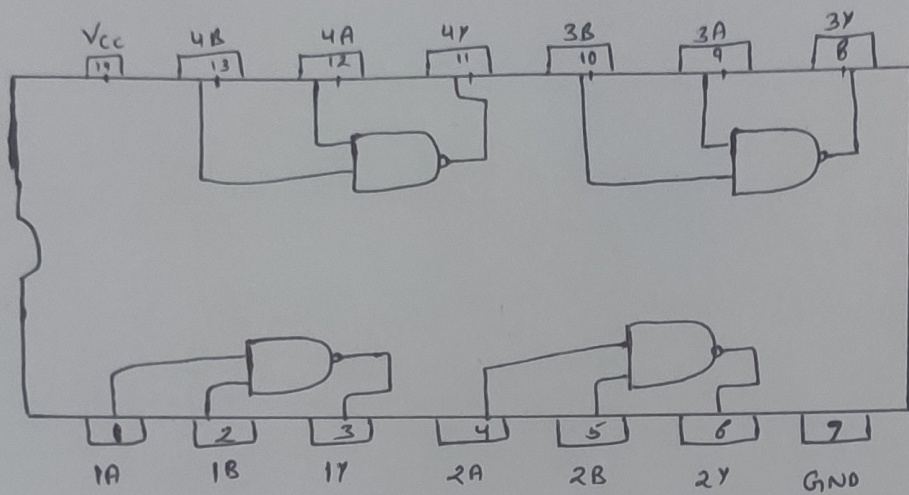PMC-102

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① NAND Gate :- A NOT-AND operation is known as NAND operation. It has n input (n >= 2) and one output.

A ———⟫D∘——— Y
B ———

A	B	$\overline{AB}$
0	0	1
0	1	1
0	0	1
1	0	1
1	1	0

Truth table

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Univ. Roll no. 2101033

Student ID- 21711266

| Vcc | 4B | 4A | 4Y | 3B | 3A | 3Y |
| 14 | 13 | 12 | 11 | 10 | 9 | 8 |

| 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| 1A | 1B | 1Y | 2A | 2B | 2Y | GND |

Circuit diagram