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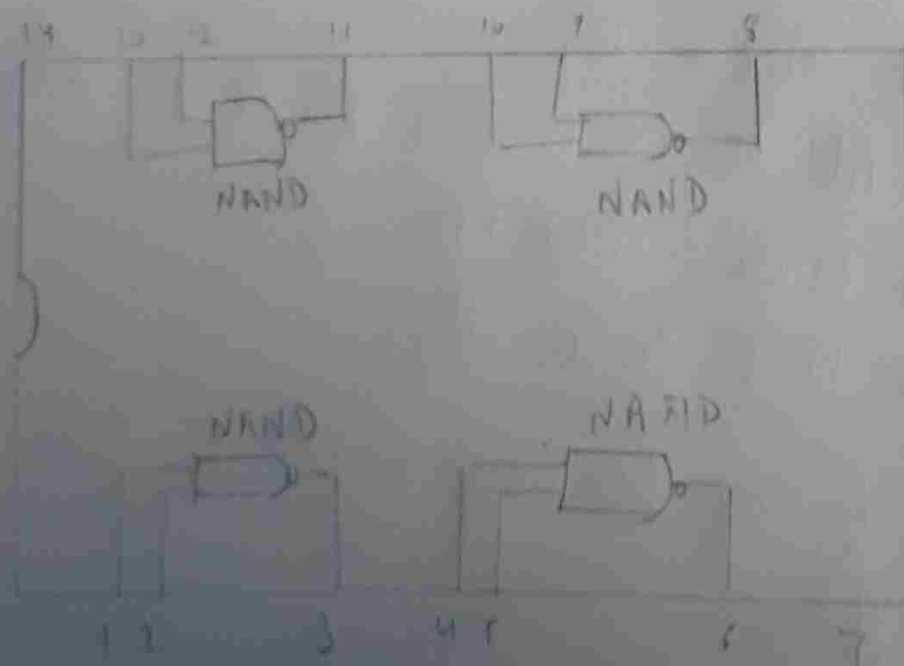
Id - 21711030

Class - MCA 1 B

Roll No - 25

Object :- To Verify the truth table of NAND gate.

The logic NAND gate is generally called as a "universal gate" because it is one of the most used logic gate types all gates can be implemented by NAND gate. Boolean expression  $Q = \overline{A \cdot B}$  The NAND gate has an output low (0) only when all inputs are high (1). it is inverse of AND gate.



## Logic Diagram :-



## Truth table :-

Inputs		Output
A	B	$C = \overline{A \cdot B}$
0	0	1
1	0	1
0	1	1
1	1	0

Result :- The result of the experiment has been verified successfully.

Precaution :- (i) All the connections should be proper according to the circuit diagram.

(ii) Handle the IC's carefully.

(iii) In case of any burning smell switch off the power supply.