

(1)

Name: Shivam Rawat

University Roll no: 2101203 (39)

Section: B

Course: MCA 1<sup>st</sup>

Subject: Computer Organisation

Ans 1) Aim: To discuss the working of NAND gate.

Apparatus Required: NAND gate IC, power supply, hook up wires and bread board etc.

Theory: The logical NAND gate is generally classed as a "universal gate" because it is one of the most used logic gate types, all gates can be implemented by NAND gate. Boolean expression  $Q = \overline{A \cdot B}$ .

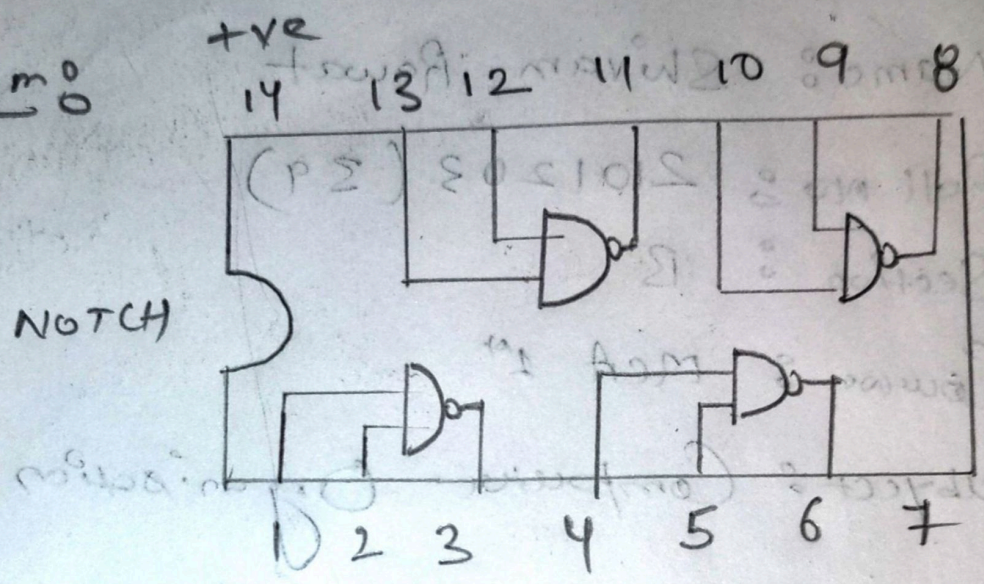
The NAND gate has an output low (0) only when all inputs are high (1). It is inverter of AND gate.

*[Signature]*

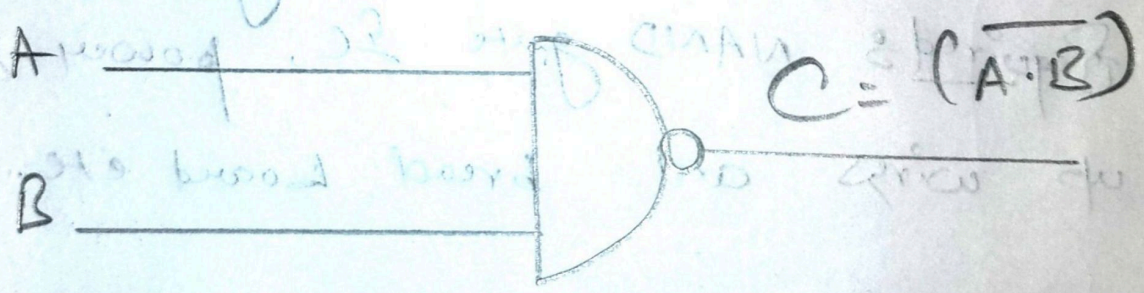


(2)

IC diagram



Logic Diagram



*Slavh*