

Name - Ajay Chakraborty

Rollno - 2101009

Course - MCA-1(B)

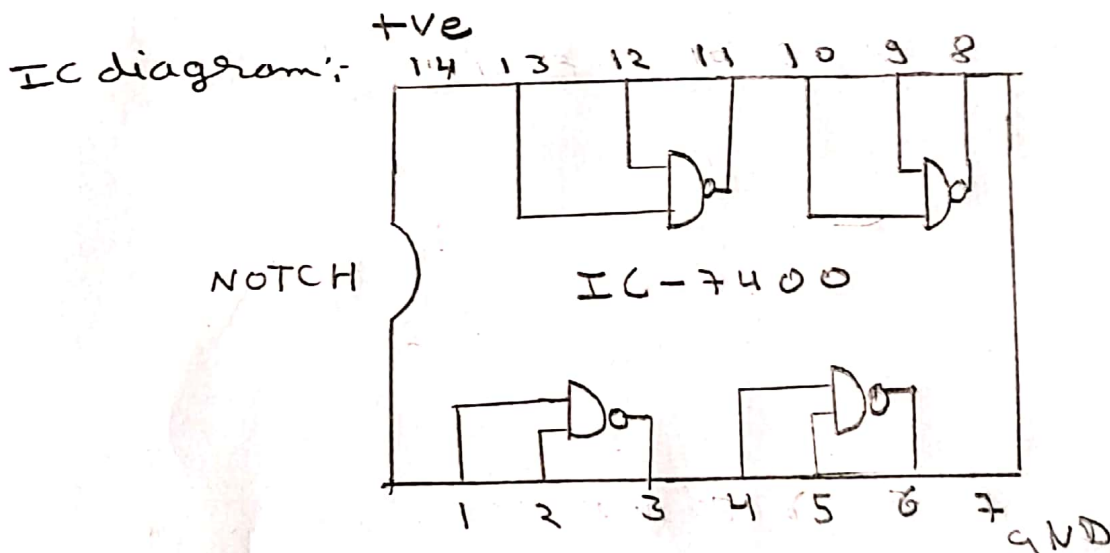
Subject: CO

one - (1) Ans

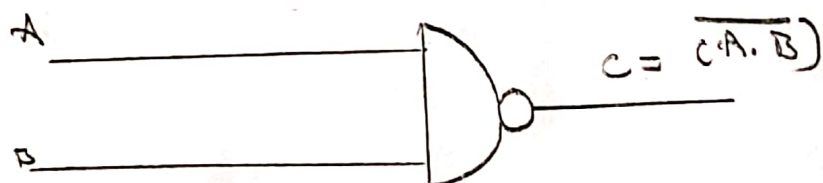
Aim: To discuss the working of NAND gate.

Apparatus required: NAND gate IC, power supply, hook up wires and bread board etc.

Theory:- The logical NAND gate is generally classed as a "universal gate" because it is one of the most used logic gate types, all gates can be implemented by NAND gate. Boolean expression $C = \overline{A \cdot B}$. The NAND gate has an output low (0) only when all inputs are high (1), it is inverter of AND gate.



Logic diagram:-



truth table.

Input		Output
A	B	$C = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

Procedure:-

Connect the VCC and ground to pins specified by the data sheet.

Do all the connections per the IC diagram
Switch on the power supply.

After completing the connection properly observe the outputs and match it with the given truth table.

Precautions:-

All the connection should be proper accordingly the circuit diagram.

Handle IC's carefully.

In case of any the caution or burning smell switch off the power supply immediately