Roll No - 2101204

S.Id - 21711012

Course. MCA(B)

Ques 1 :- NAND GATE :- The NOT-AND gate which is, equal to an AND gate. The NAND gate gives high output if any of these inputs are low. The NAND gate is represented by a AND gate with a small circle on the output. They are connected in cascode form. It is also called Negated and gate. The NAND gate provides the false or low output only when their outputs is high or true. The NAND gate is essential because different types of boolean function are implemented by using it.

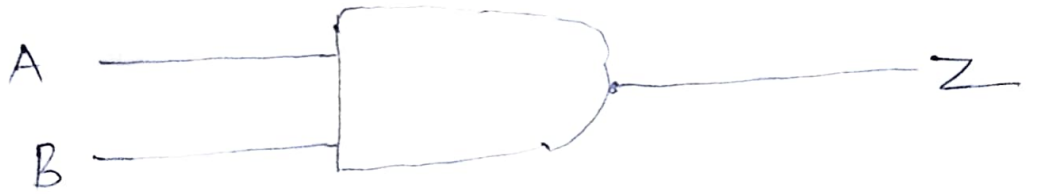
The NAND gate has the property of functional completeness. The function completeness.

It performs the function OR, NOR and AND gate.

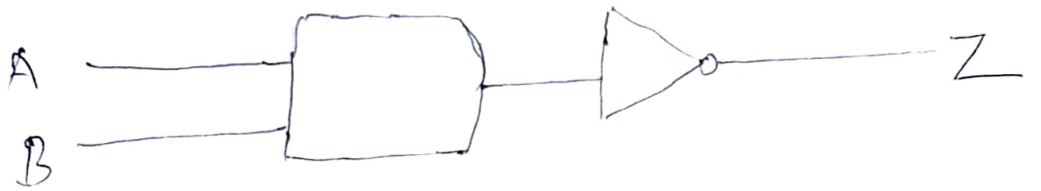
Name - Shivanand Mishra
Roll No - 2101204

SId - 21711042
Course - MCA(B).

Symbol :-



The logic circuit of the NAND gate is



from the logic circuit, the output can be expressed as:

$$Z = \overline{A \cdot B}$$ - Algebra Boolean

Truth Table

A	B	Z
0	0	1
0	1	1
1	0	1
1	1	0