

Graphic Era Hill University, Dehradun  
(Answer Sheet for Online Examination Feb. 2022)

Please tick (✓) your campus: (DEHRADUN/BHIMTAL/HALDWANI)

Name: Bhuvanesh K. Chatur Univ. Roll No. 2101054 Student ID 21391036

Date: 02.13.22 Course: MCA Branch: IT Sem.: 1<sup>st</sup> Section: B

Subject Name: Computer Organization & Architecture Subject Code: TC102 Page No. 1

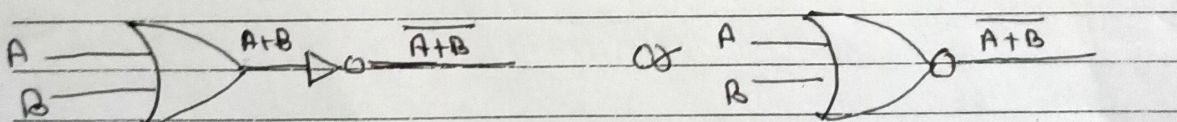
Q2 Discuss the working of NOR gate with the help of circuit diagram and truth table.

A

NOR Gate:- A NOR gate ("not OR gate") is a logic gate that produces a high output (1) only if all its inputs are false, and low output (0) otherwise.

The NOR gate is the inverse of an OR gate, and its connecting an OR gate to a NOT gate. Just like an OR gate, a NOR gate may have any number of input probes but only one output probe.

The basic logic construction of the NOR gate is



Signature of Student

Bhuvanesh K. Chatur



Graphic Era Hill University, Dehradun  
(Answer Sheet for Online Examination Feb. 2022)

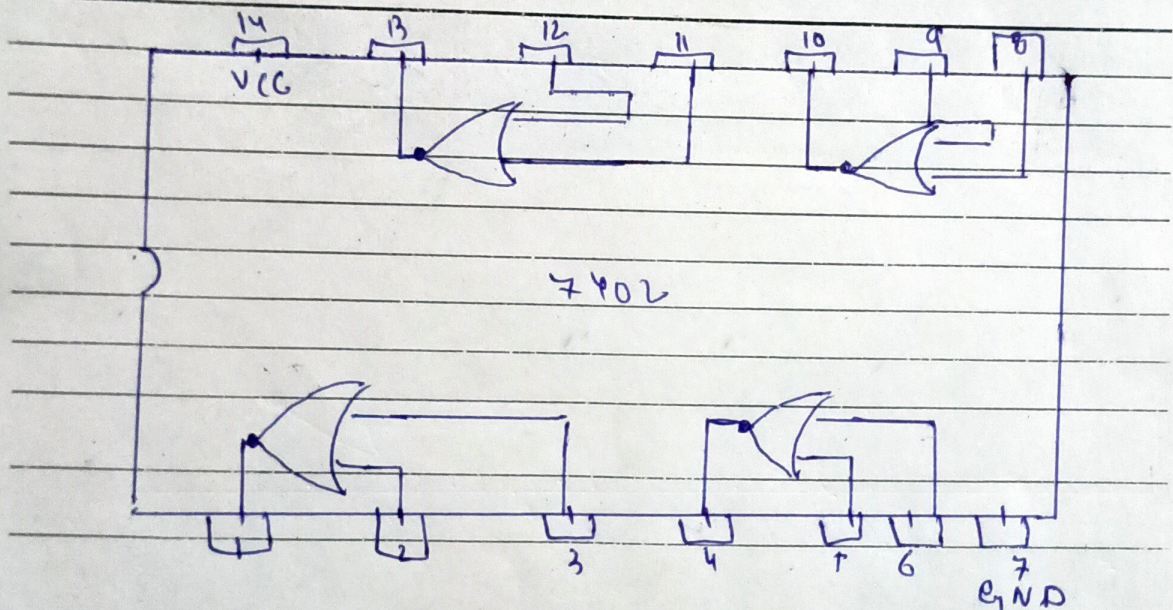
Please tick (✓) your campus: (DEHRADUN/BHIMTAL/HALDWANI)

Name: Bhuvnesh Kumar Chauhan Univ. Roll No. 2101057 Student ID 21391026  
Date: 22/11/22 Course: MCA Branch: IT Sem.: 1<sup>st</sup> Section: B  
Subject Name: COA Subject Code: TMCB2 Page No. 2

NOR Gate Truth Table

Inputs		Output
A	B	$X = \overline{A+B}$
0	0	1
0	1	0
1	0	0
1	1	0

NOR Gate Circuit diagram



Signature of Student

Bhuvnesh Kumar