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Course:- MCA

Section:- 1<sup>st</sup>

## Midterm Practical Exam

### NAND GATE

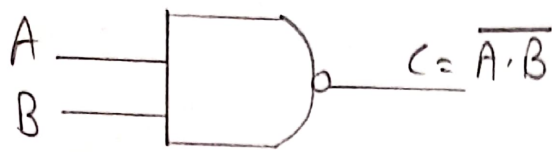
In digital Electronics the NAND Gate also known as Not AND Gate work as:-

1. Connect the vcc and ground to the pins specified by the data sheets.
2. Do all the connections as per the IC Diagram.
3. After completing the connections properly observe the output and match it with the given truth table

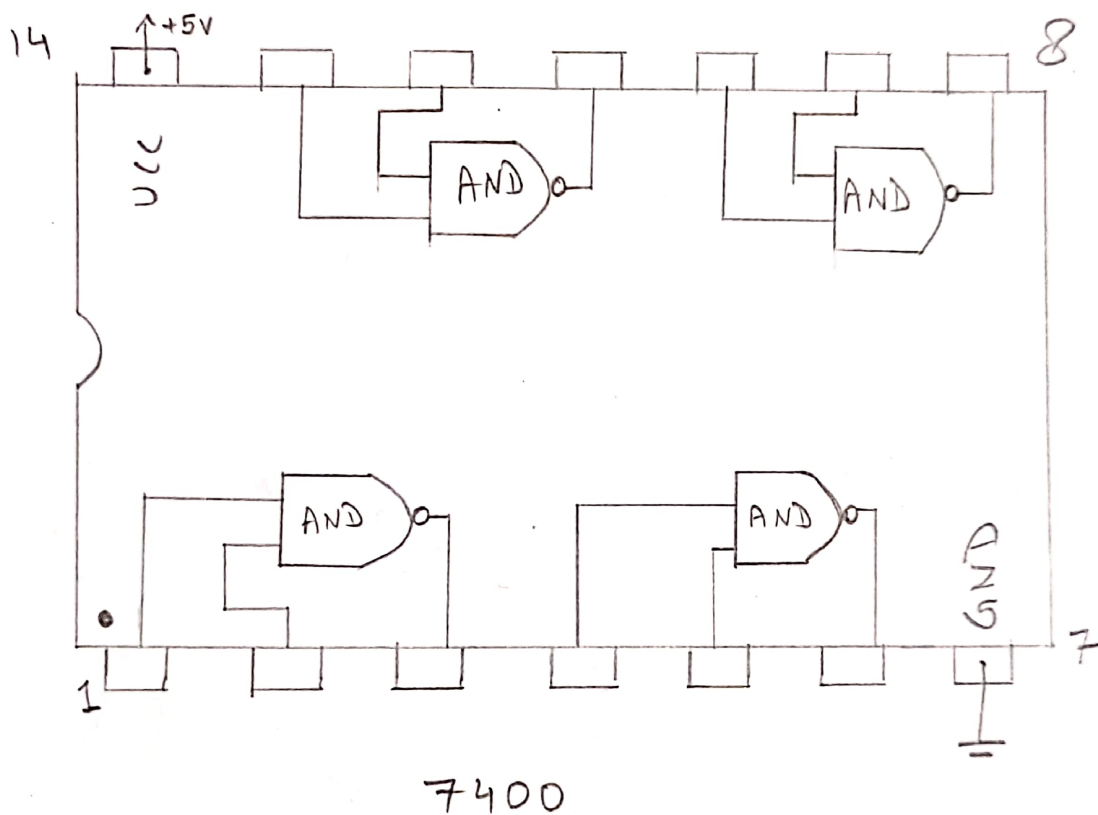
| INPUT |   | Output                     |
|-------|---|----------------------------|
| A     | B | $C = \overline{A \cdot B}$ |
| 0     | 0 | 1                          |
| 0     | 1 | 1                          |
| 1     | 0 | 1                          |
| 1     | 1 | 0                          |

Payal

## Logic Diagram



## Circuit Diagram (PIN Diagram)



## Conclusion

A NAND Gate is a logic gate which produces an output which is false only if all its inputs are true. The output produced will be true if one or both inputs are false.

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