

Name :- Himanshu

Enrolment No :- PV-18210058

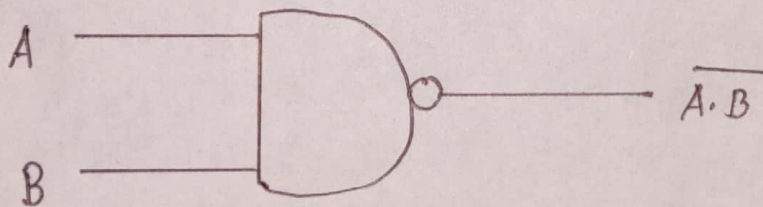
Father Name :- Amir Chandra Course :- MCA

University Roll No :- 2101080

1) NAND GATE :-

A NAND gate is a logic gate that produces a low output (0) only if all its inputs are true, and high output (1) otherwise. Hence the NAND gate is the inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate.

NAND gates are known as universal gates, which mean they are a type of logic gate which can implement any Boolean function.



NAND GATE

Truth Table :-

A	B	output
		$x = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

Circuit Diagram of NAND Gate :-

