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Date: - May. 22.22

Gwuse! - MrA

Semester: - 1st

section: - B

Subject: - Computer augenization L'Andri-Subject cod: - 7MC 102. Questo Discuss the working of NAND gate with the help of circuit diagram

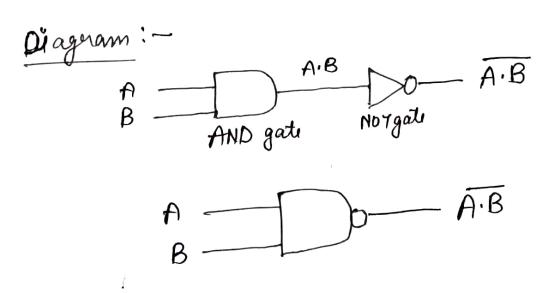
=> OA NAND gate (not AND gate) is a logic gate

that produce a low autput (0) only if all

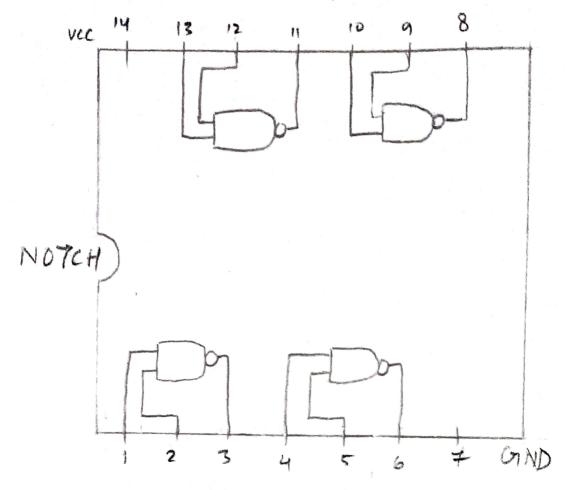
its input are true & high autput (1)

Hence the NAND gate is include of an AND gate, and its conducted by gate to a NOT gate.

Just like an AND gate, a NAND gate may have any number at input but only one autput.



Input		Output
. A	В	Output X = A·B
0	0	3
0	1	1
1	O	1
,	1	0



ile know autput of the AND gate is only night and when all the inputs are high on 1. In other cases the autput of the

AND gall is low and.

In NAND, the fact is the opposite, how,
the autput is only logical D when and
the autput is only logical D when and
only when all imput of the gate are
only when all other cases. The autput
is and in all other cases. The autput
is and in all other cases. The autput
af the NAND gate is high well.