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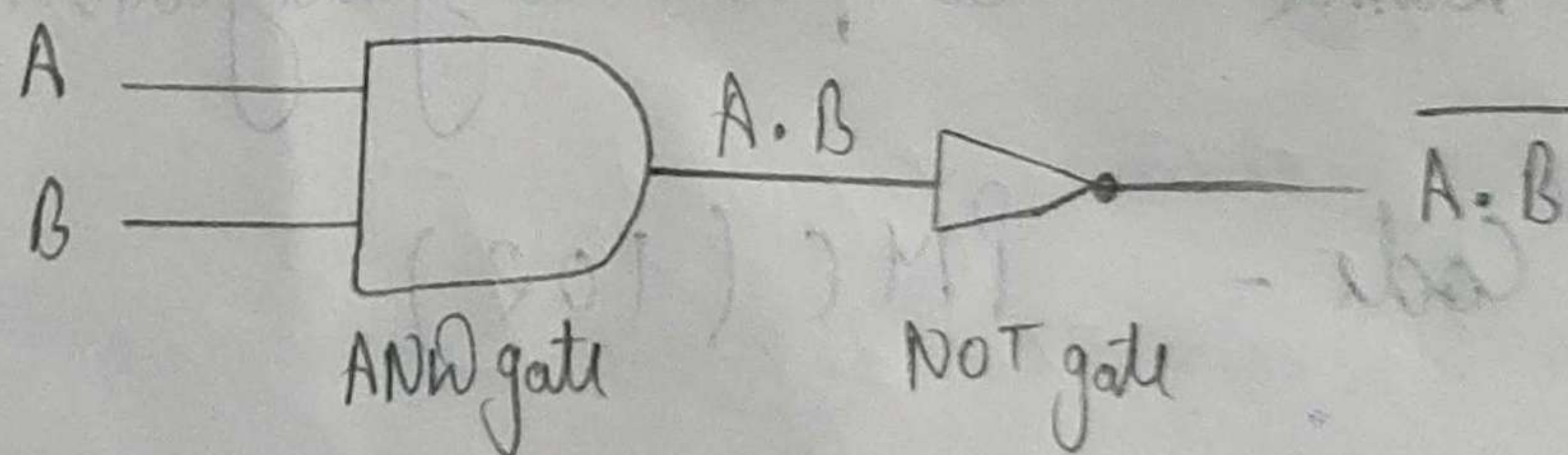
Date - 22/3/22

Section - B

Ans 1   NAND — A Nand gate ("not AND gate") is a logic gate that produces a low output (0) only if all its input are true, and high output (1) otherwise. Hence the NAND gate is the inverse of an AND gate, and its circuit is produced by connecting an AND gate to a NOT gate.

The basic logical construction of the NAND gate is shown below (you can see it is and AND gate followed by a NOT gate)

A ——
B ——
AND gate   A.B   NOT gate $\overline{A.B}$

A ——
B ——
NAND gate $\overline{A.B}$

Truth table

Inputs		Output
A	B	$X = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0