U2(VOLTAGE REGULATOR)
LM7805ECT_NL

R1 (RESET)
10k

VIRTUAL TERMINAL
RXD
TXD
RTS
CTS

C5
0.1uF

C6(BYPASS CAPASITORS)
0.1uF

C3
10u

C4
1u

SERVO MOTOR
+88.8

U1
30 PD0/RXD/PCINT16
31 PD1/TXD/PCINT17
32 PD2/INT0/PCINT18
1 PD3/INT1/OC2B/PCINT19
2 PD4/T0/XCK/PCINT20
9 PD5/T1/OC0B/PCINT21
10 PD6/AIN0/OC0A/PCINT22
11 PD7/AIN1/PCINT23

PB0/ICP1/CLKO/PCINT0 12
PB1/OC1A/PCINT1 13
PB2/SS/OC1B/PCINT2 14
PB3/MOSI/OC2A/PCINT3 15
PB4/MISO/PCINT4 16
PB5/SCK/PCINT5 17
PB6/TOSC1/XTAL1/PCINT6 7
PB7/TOSC2/XTAL2/PCINT7 8

20 AREF
18 AVCC
19 ADC6
22 ADC7

PC0/ADC0/PCINT8 23
PC1/ADC1/PCINT9 24
PC2/ADC2/PCINT10 25
PC3/ADC3/PCINT11 26
PC4/ADC4/SDA/PCINT12 27
PC5/ADC5/SCL/PCINT13 28
PC6/RESET/PCINT14 29

ATMEGA328P

RV1
1k

R3
220

SWITCH

U4(CRYSTAL)
1    2
9C-16

D1
LED-RED

R2
270r

J1
1  1    2  2
3  3    4  4
5  5    6  6
M20-9980346

C1
22pF

C2
22pF

C6
22pF

R1
220

FILE NAME: CourseProject.pdsprj
DESIGN TITLE: CourseProject.pdsprj
PATH: C:\Users\jyri_\OneDrive - TUNI.fi\Koodaus\Microcontrollers\Kurssiproje
BY: @AUTHOR
REV:@REV
DATE: 13.4.2023
PAGE: 1 of 4
TIME: 10.32.38