

TITLE:	OHMv1 SLAVE R0.0   OPAMP SECTION1		
VOIDRON CO.			
DESIGN BY:	AS / MK	FILE:	OHMv1_SLAVE_R0.0_SCH
RELEASE:	24-DEC-2023	SHEET:	1 OF 7

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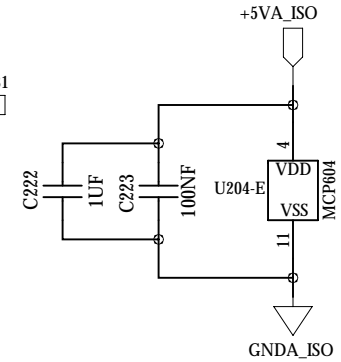
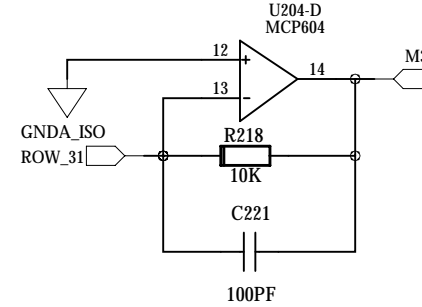
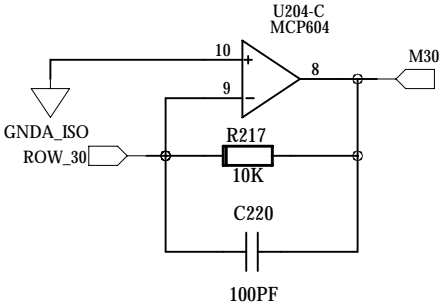
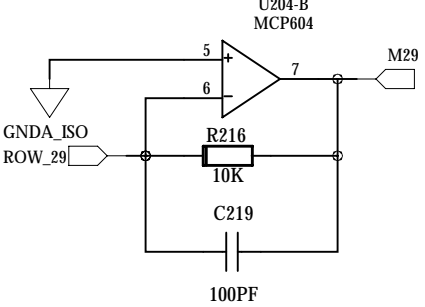
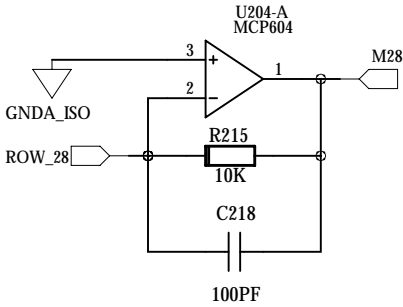
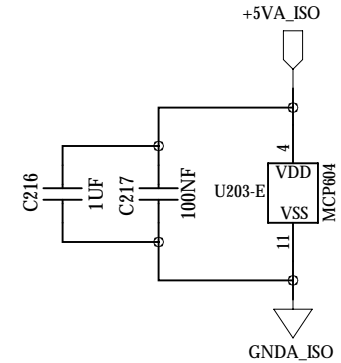
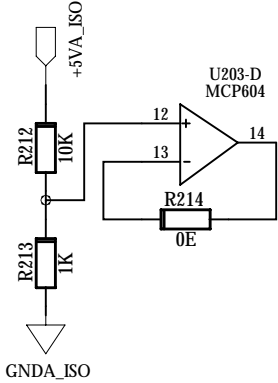
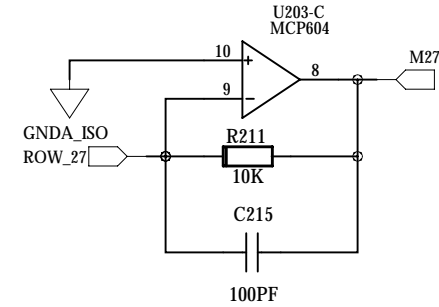
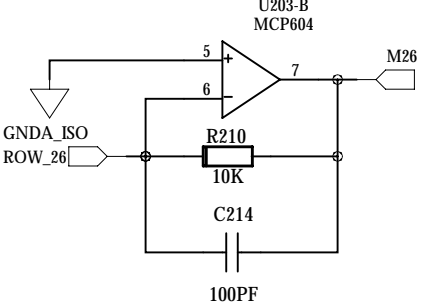
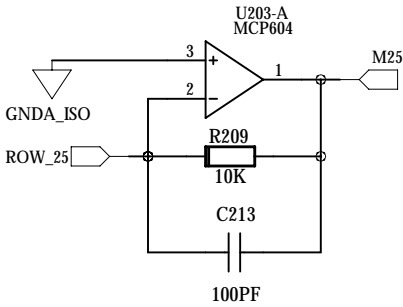
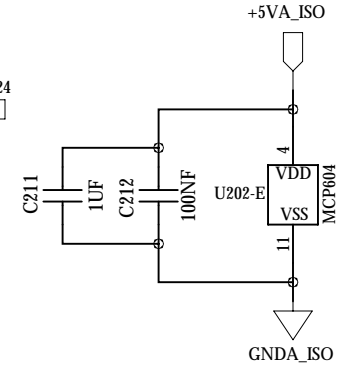
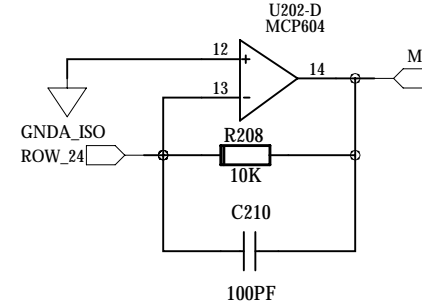
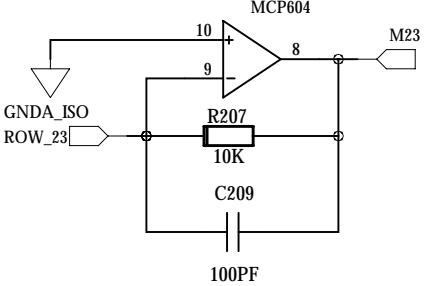
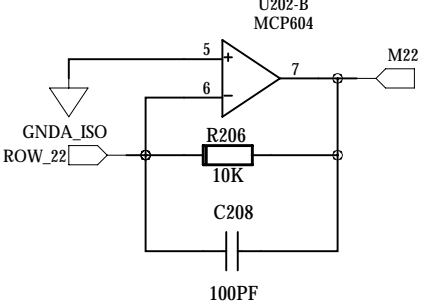
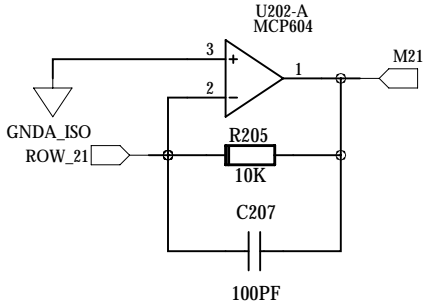
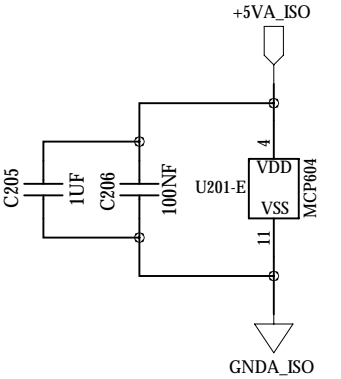
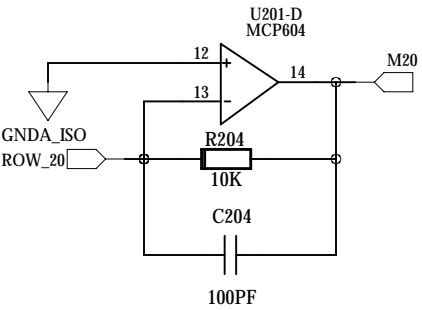
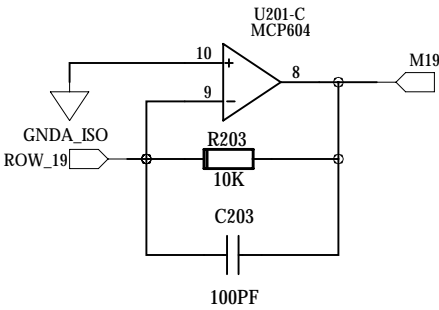
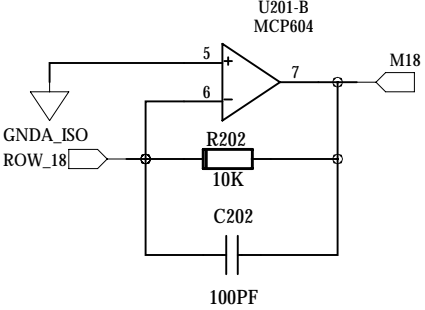
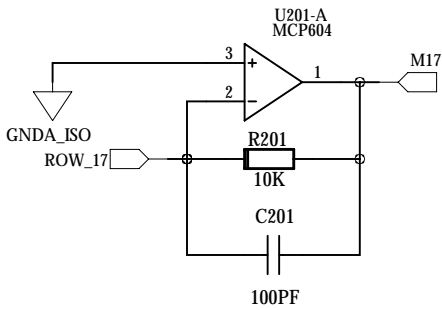
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TITLE:	OHMv1 SLAVE R0.0   OPAMP SECTION2		
VOIDRON CO.			
DESIGN BY:	AS / MK	FILE:	OHMv1_SLAVE_R0.0_SCH
RELEASE:	24-DEC-2023	SHEET:	2 OF 7

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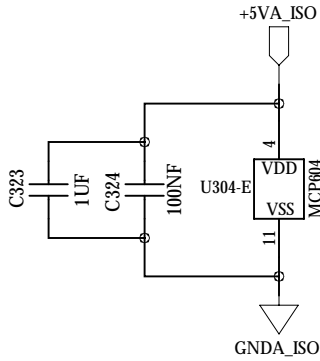
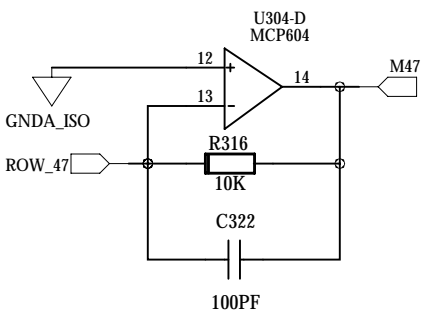
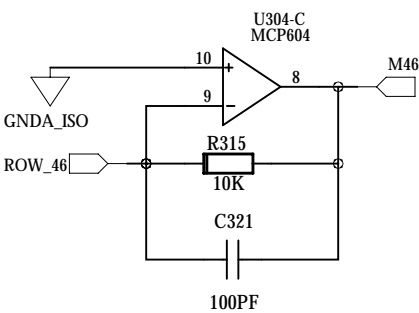
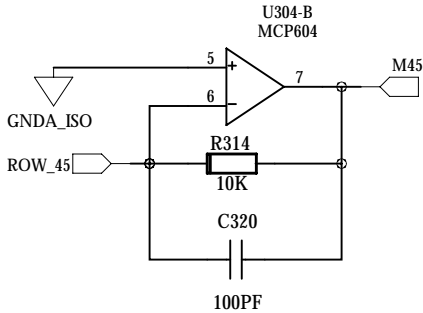
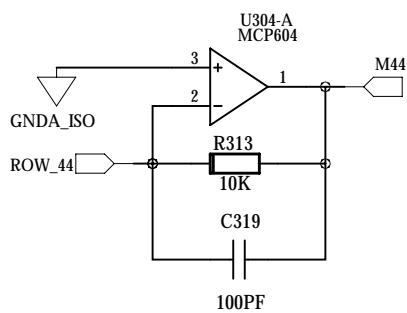
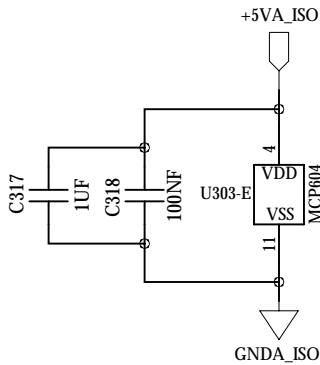
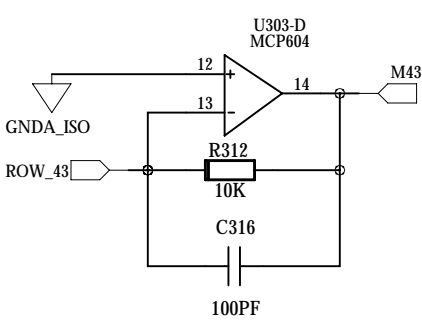
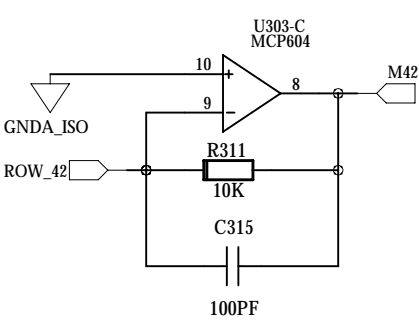
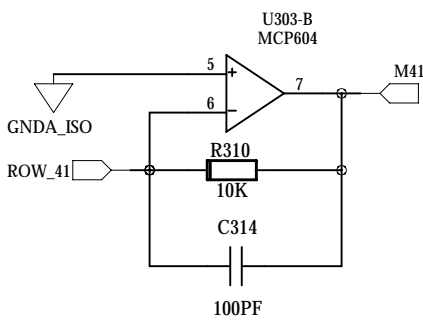
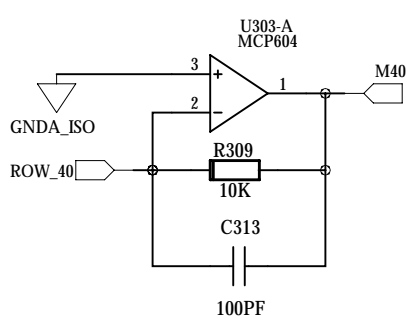
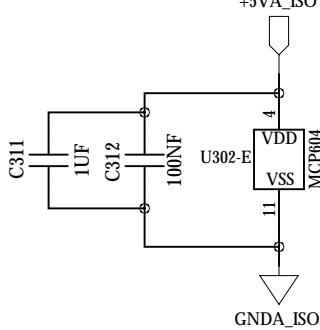
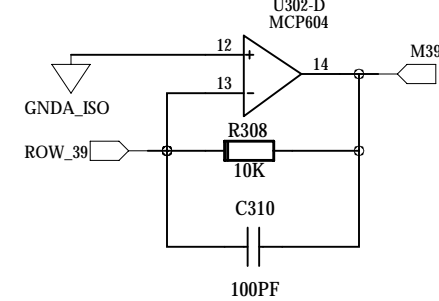
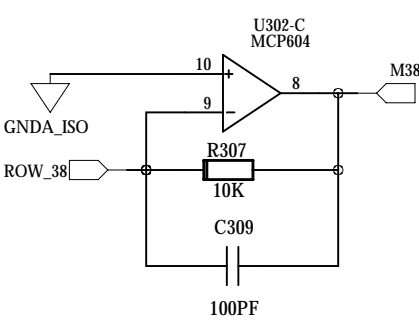
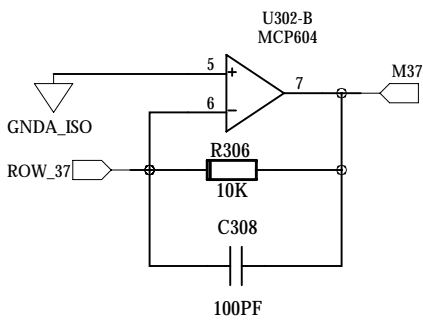
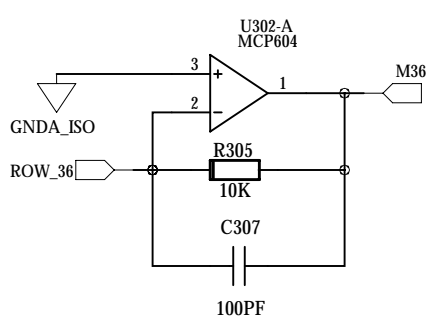
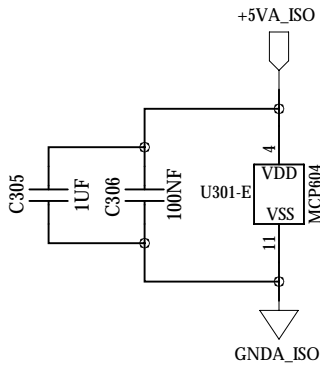
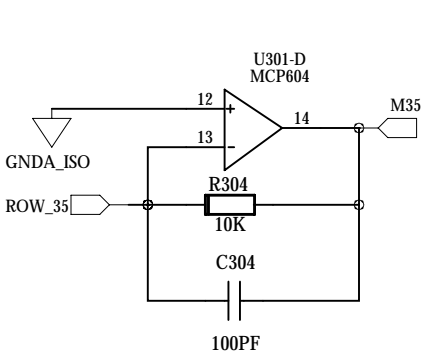
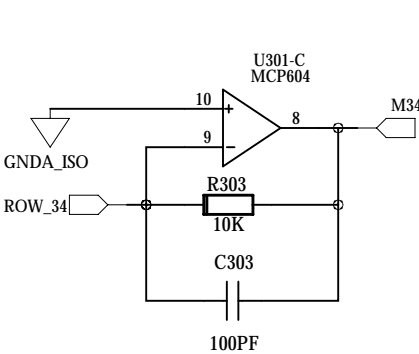
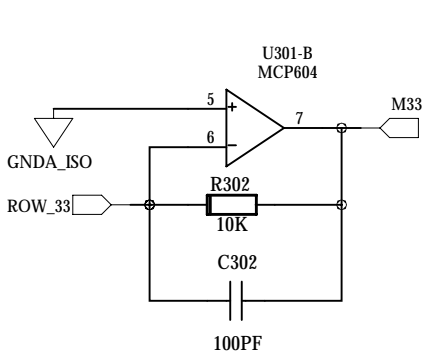
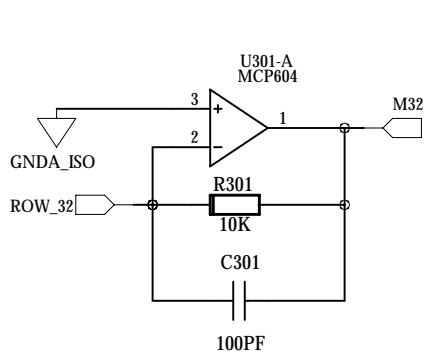
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TITLE:	OHMv1 SLAVE R0.0   OPAMP SECTION3		
VOIDRON CO.			
DESIGN BY:	AS / MK	FILE:	OHMv1_SLAVE_R0.0_SCH
RELEASE:	24-DEC-2023	SHEET:	3 OF 7

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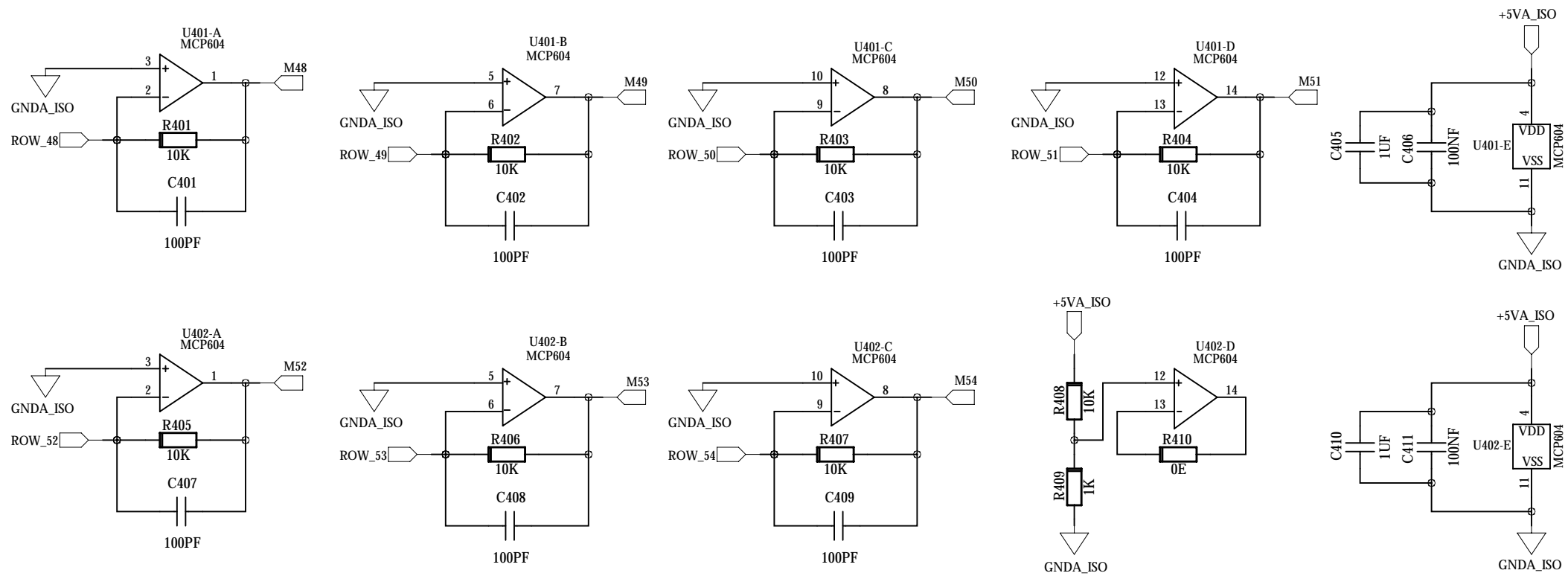
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TITLE:	OHMv1 SLAVE R0.0   OPAMP SECTION4		
VOIDRON CO.			
DESIGN BY:	AS / MK	FILE:	OHMv1_SLAVE_R0.0_SCH
RELEASE:	24-DEC-2023	SHEET:	4 OF 7

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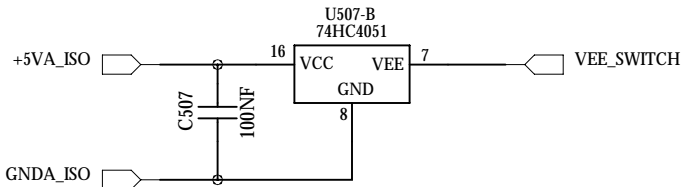
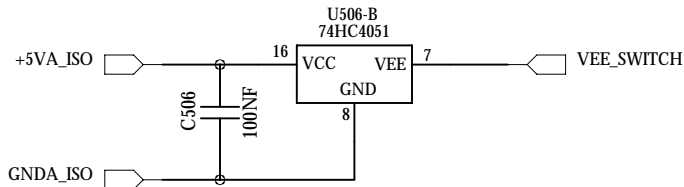
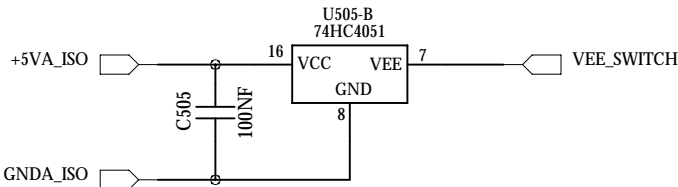
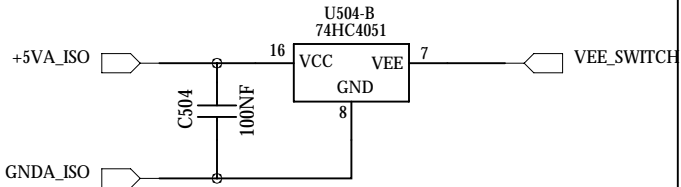
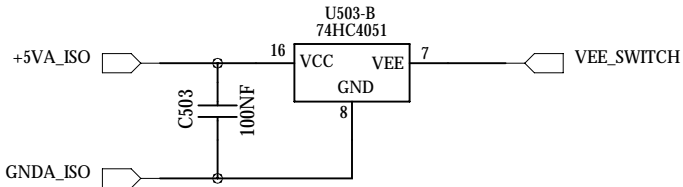
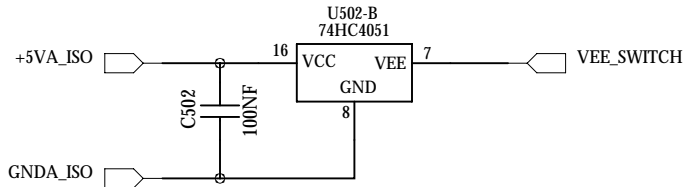
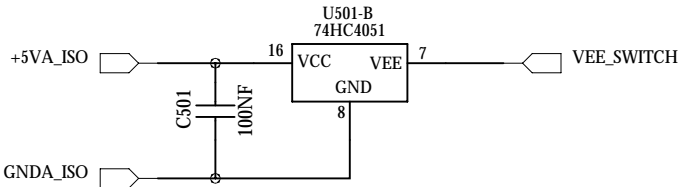
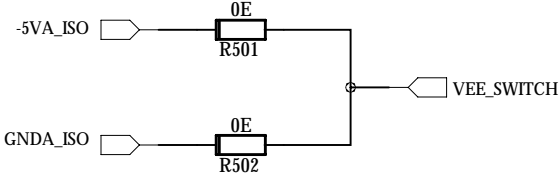
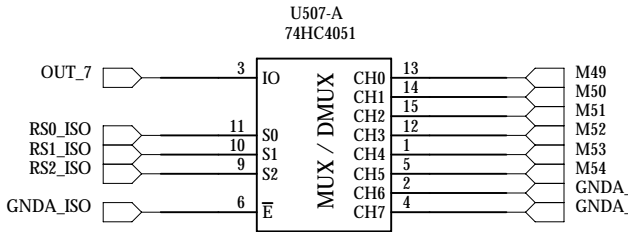
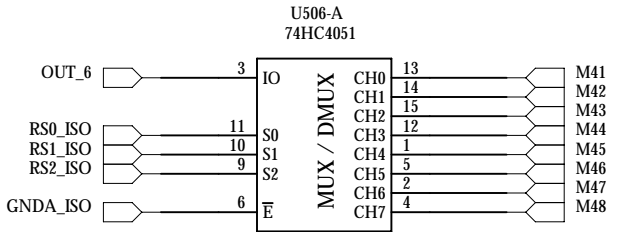
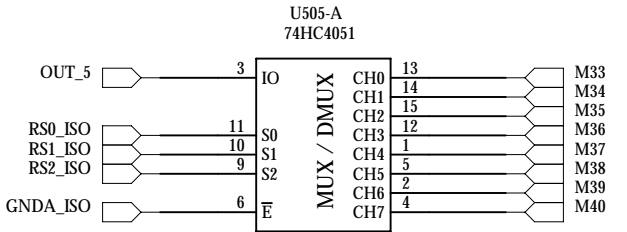
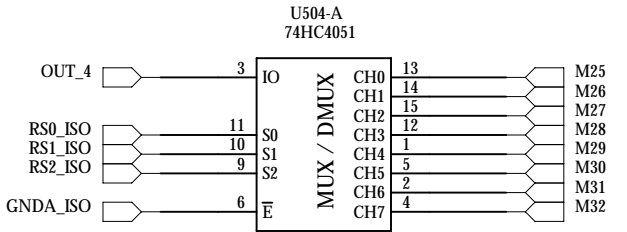
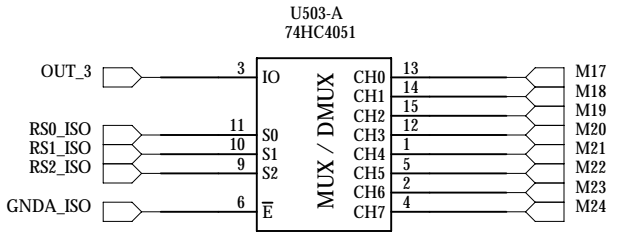
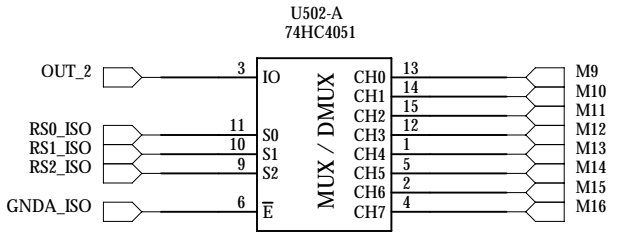
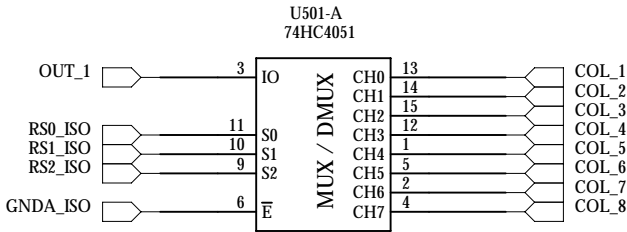
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MUX SECTION

TITLE:	OHMv1 SLAVE R0.0   MUX_SECTION		
VOIDRON CO.			
DESIGN BY:	AS / MK	FILE:	OHMv1_SLAVE_R0.0_SCH
RELEASE:	24-DEC-2023	SHEET:	5 OF 7

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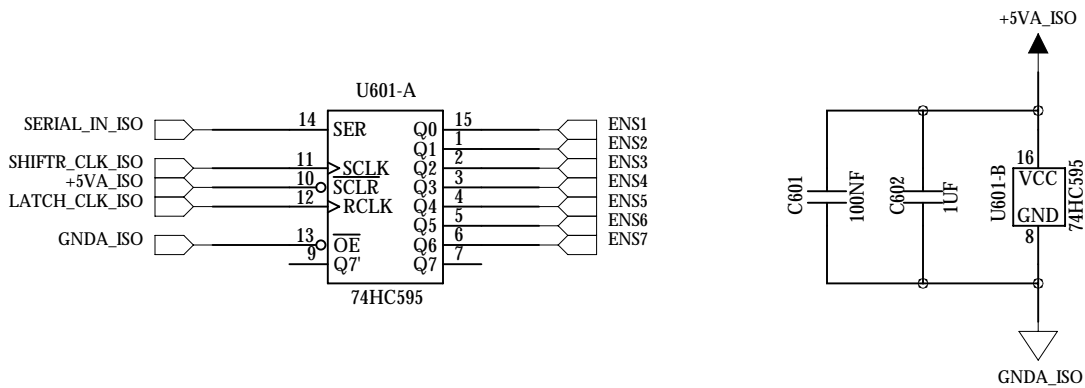
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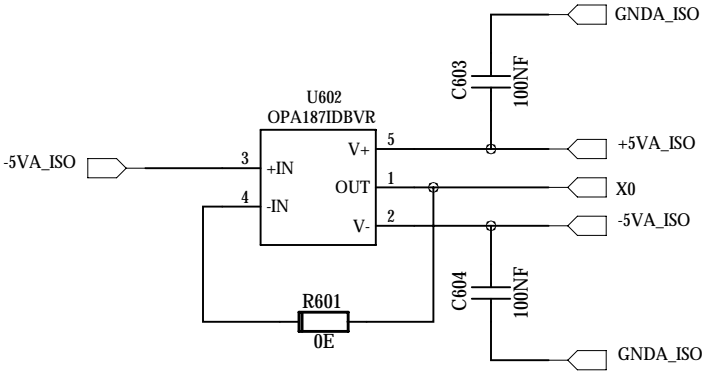
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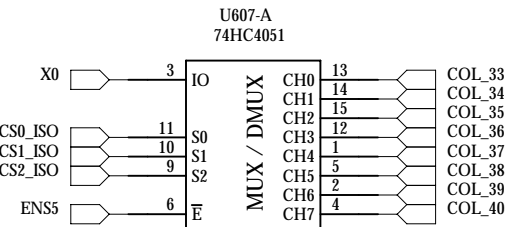
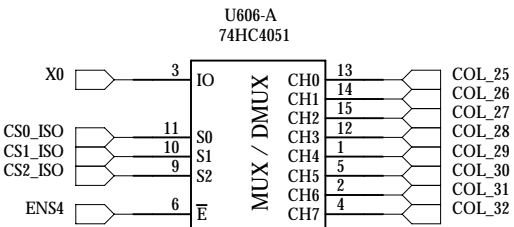
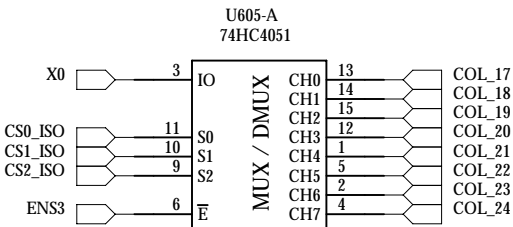
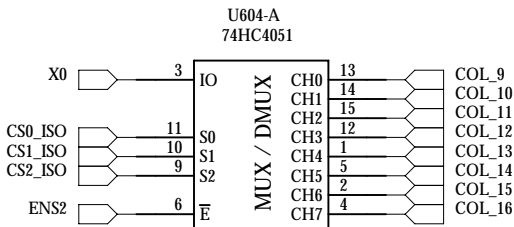
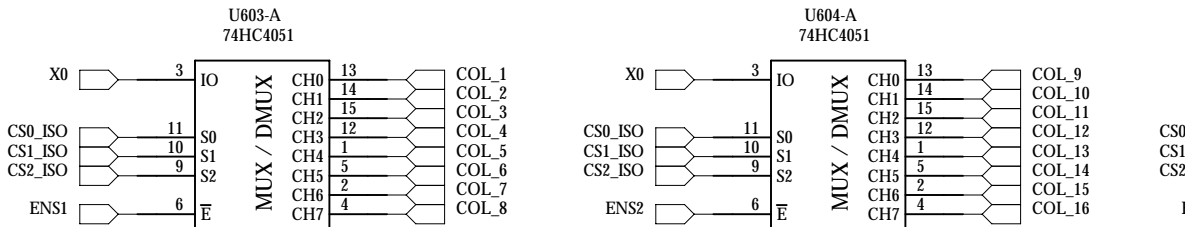
SHIFT REGISTER SECTION



OPA SECTION

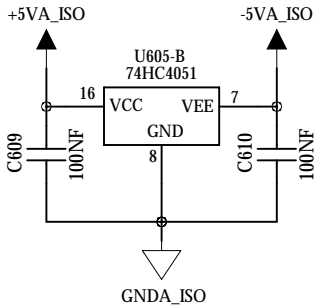
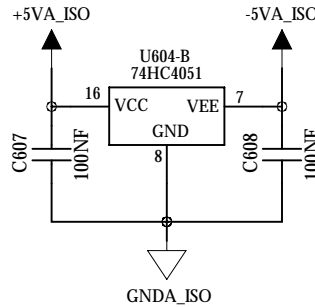
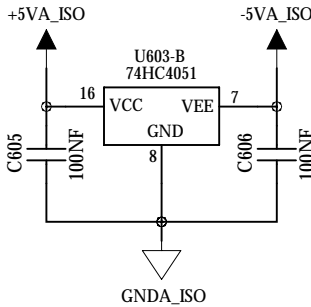
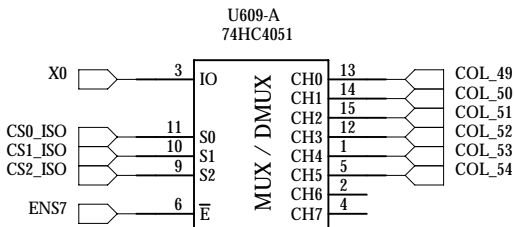
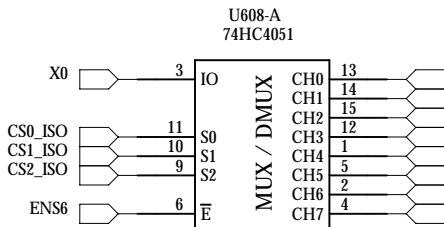
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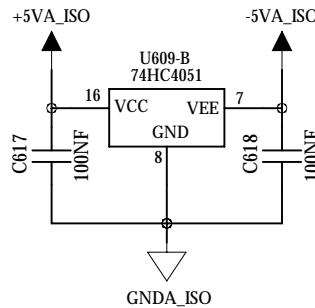
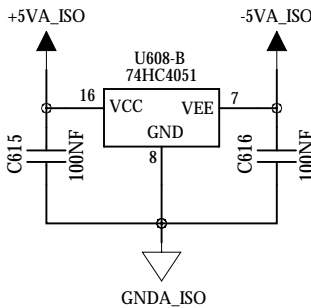
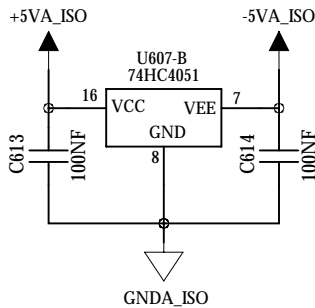
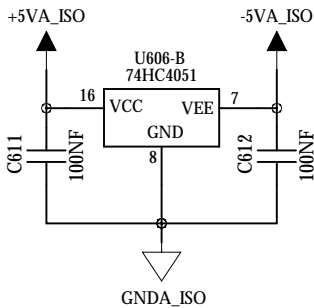
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SDMUX SECTION

TITLE:	OHMv1 SLAVE R0.0   DMUX_SECTION		
VOIDRON CO.			
DESIGN BY:	AS / MK	FILE:	OHMv1_SLAVE_R0.0_SCH
RELEASE:	24-DEC-2023	SHEET:	6 OF 7

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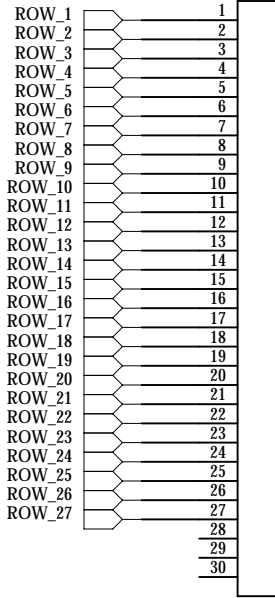
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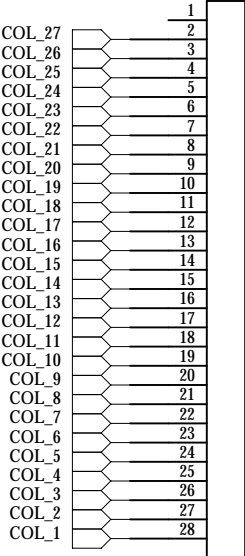
C

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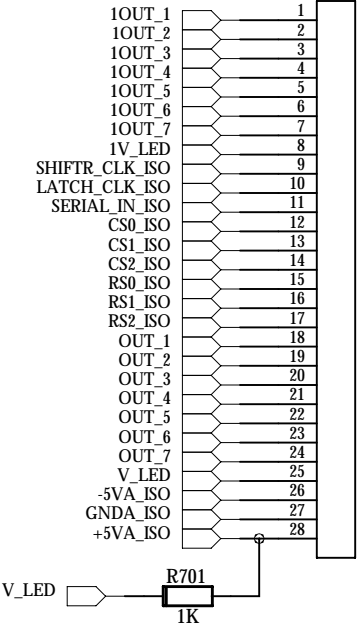
J703  
F51L-1A7H1-11030



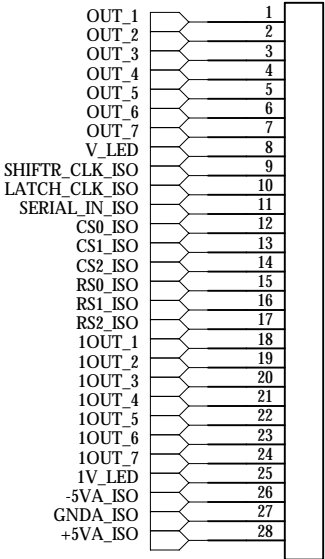
J705  
F51L-1A7H1-11028



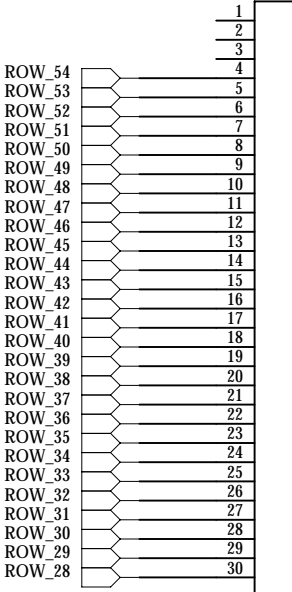
J701  
F51L-1A7H1-11028



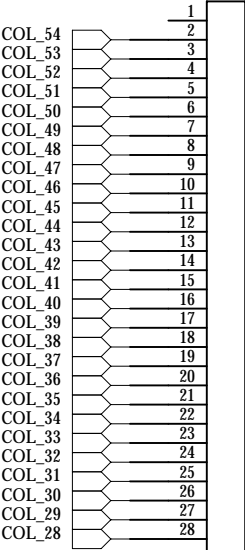
J702  
F51L-1A7H1-11028



J704  
F51L-1A7H1-11030



J706  
F51L-1A7H1-11028



ROWS

COLUMNS

MOUNTING HOLE

TITLE:	OHMv1 SLAVE R0.0   CONN SECTION		
VOIDRON CO.			
DESIGN BY:	AS / MK	FILE:	OHMv1_SLAVE_R0.0_SCH
RELEASE:	24-DEC-2023	SHEET:	7 OF 7