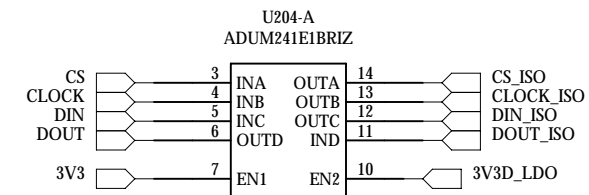
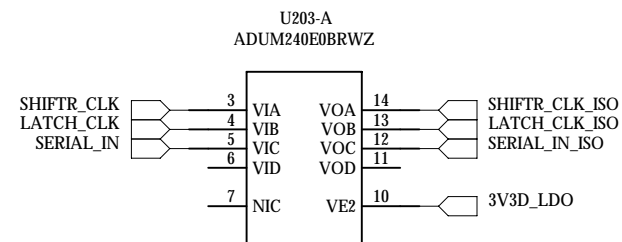
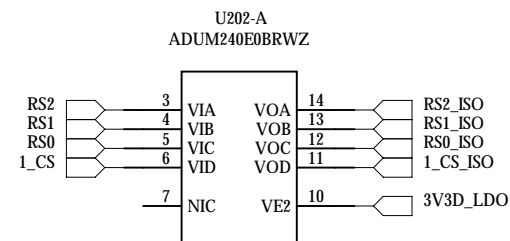
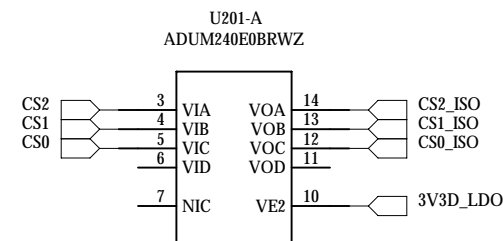




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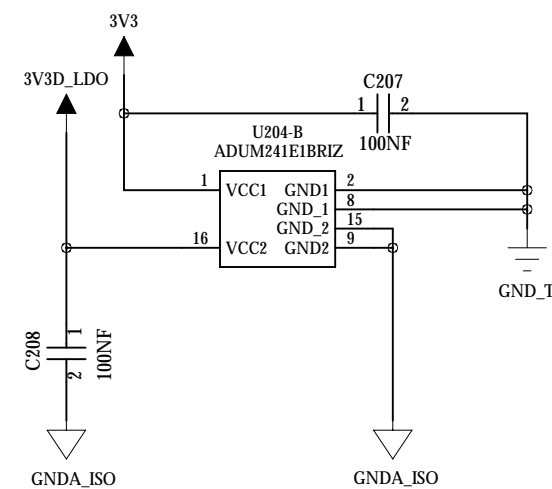
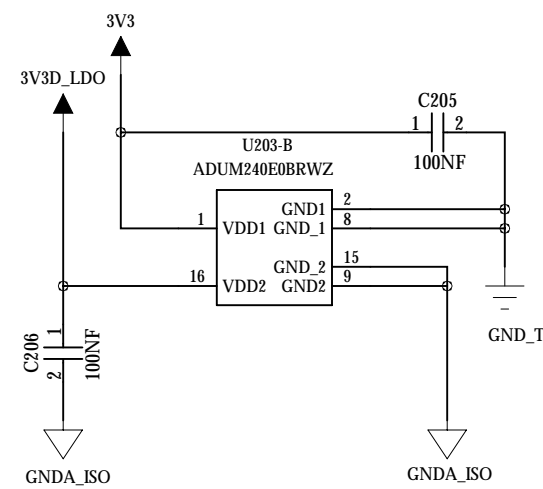
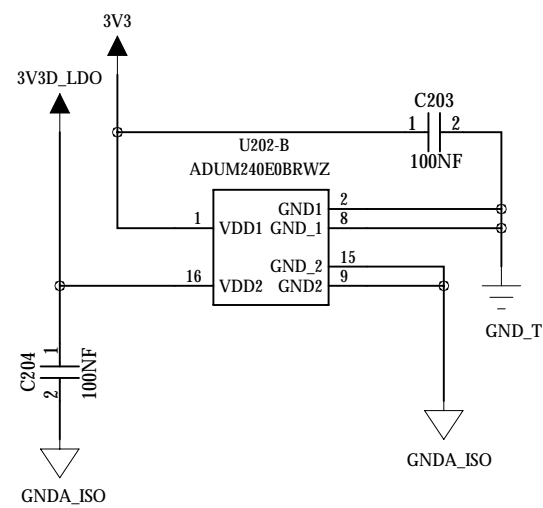
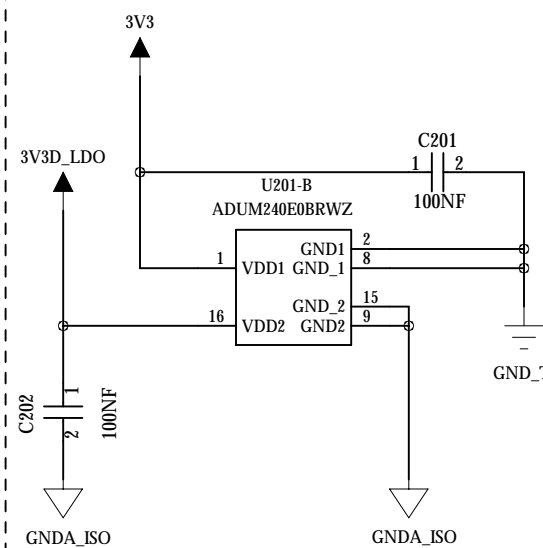
ISOLATION SECTION

SHIFT_R ISOLATION SECTION

ADC_ISOLATION SECTION

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D

TITLE:	OHMv1 MASTER R0.6 ISOLATION_SECTION		
VOIDRON CO.			
DESIGN BY:	AS / AA / MK	FILE:	OHMv1_MASTER_R0.6_SCH
RELEASE:	26 - FEB - 2024	SHEET:	2 OF 5



A

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C

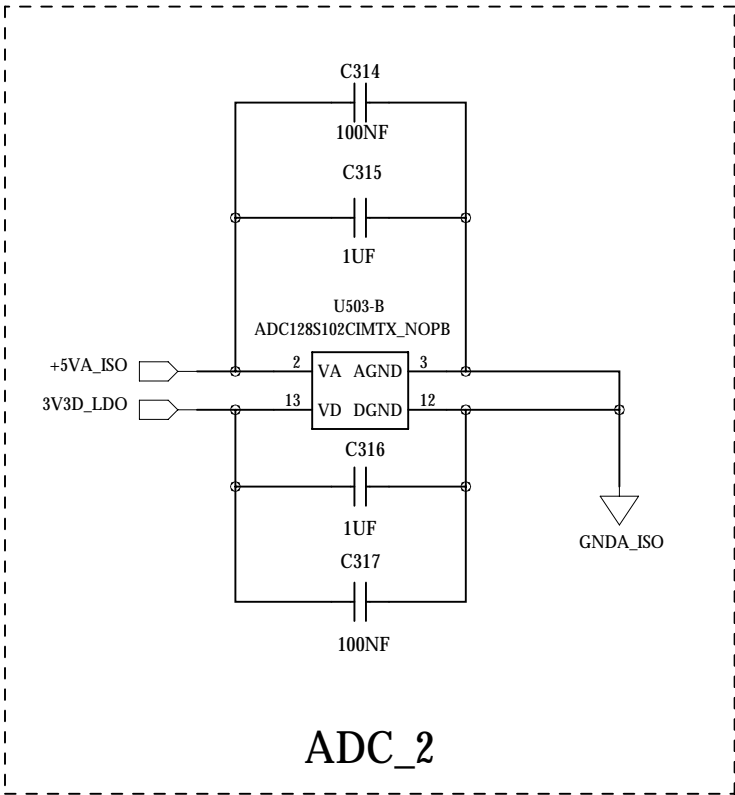
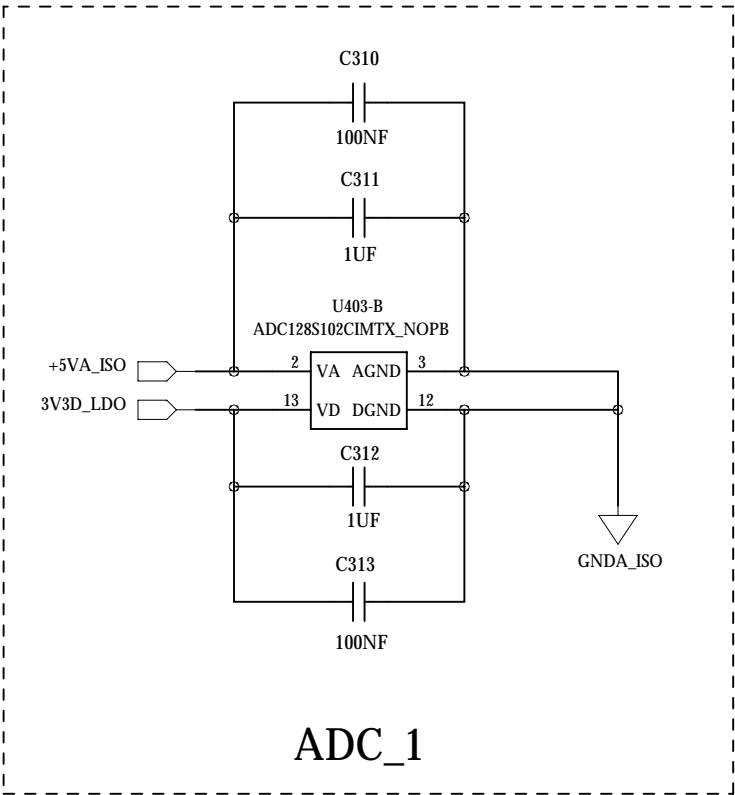
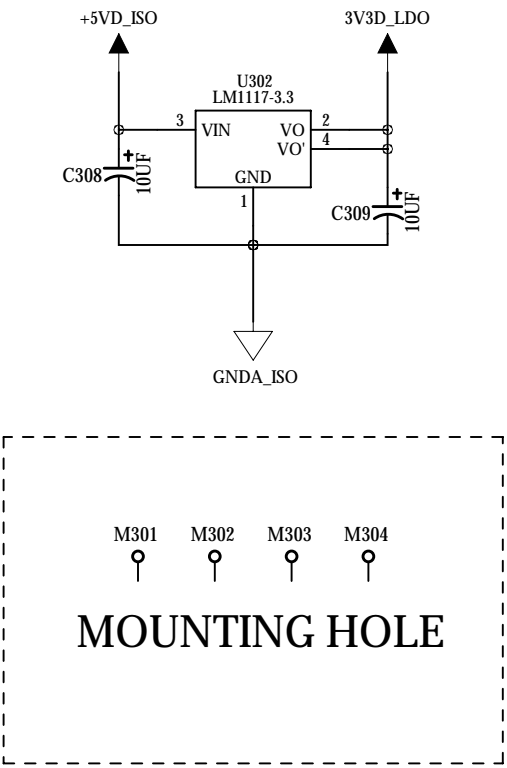
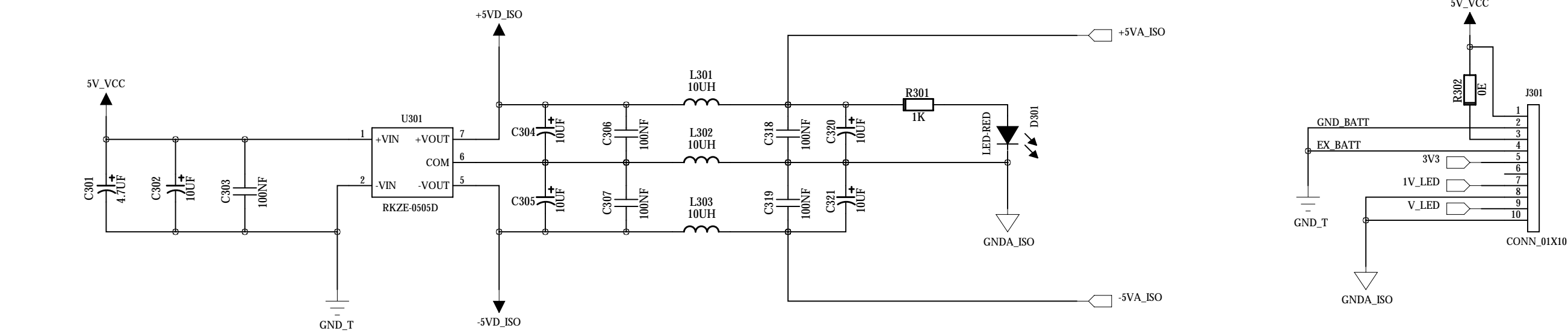
D

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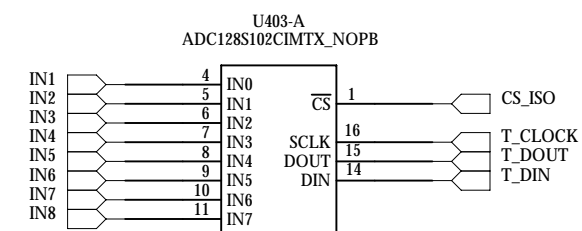
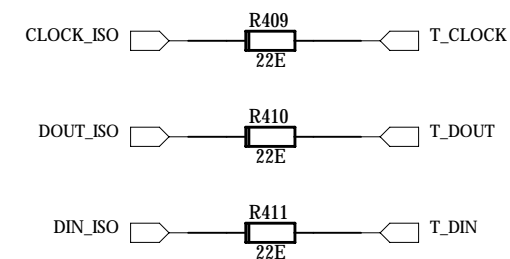
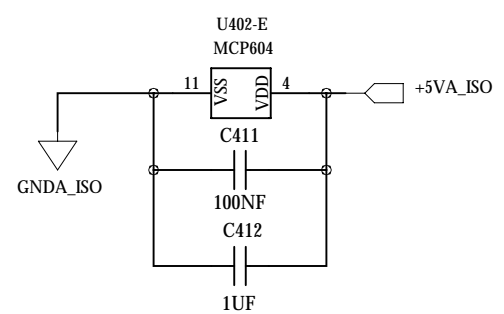
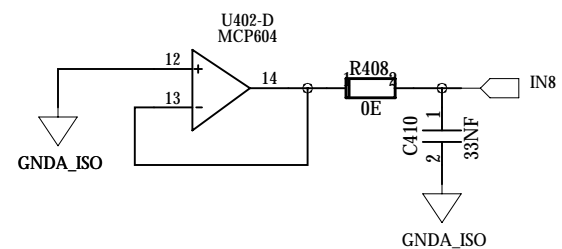
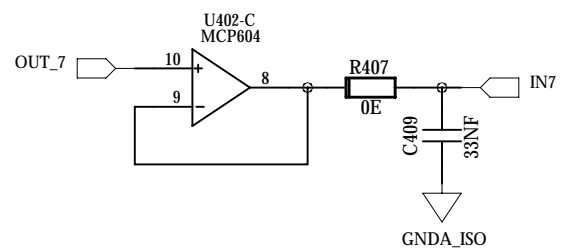
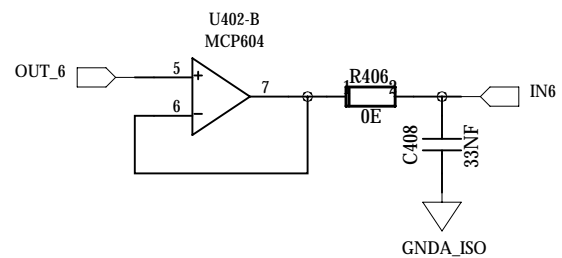
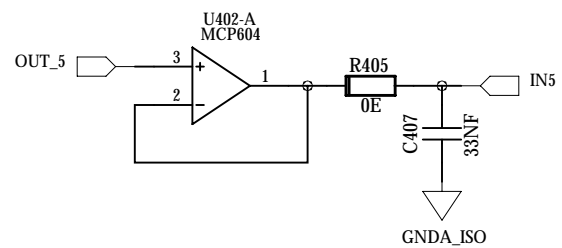
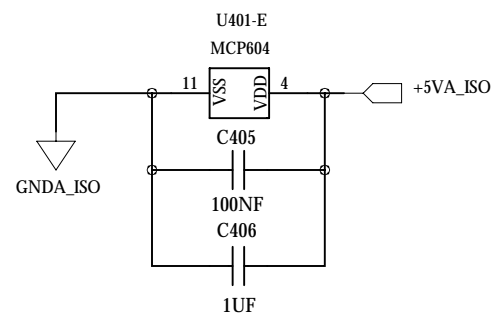
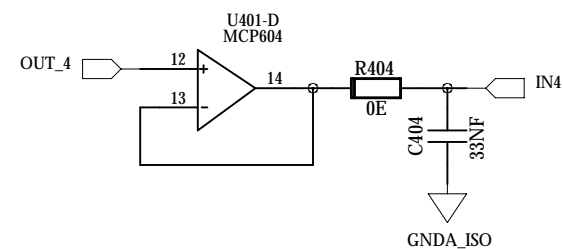
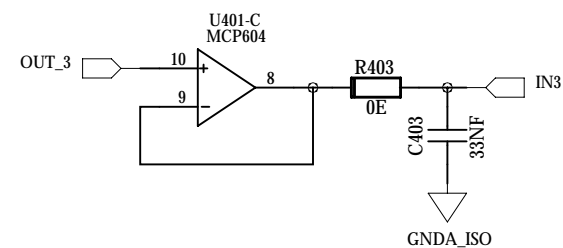
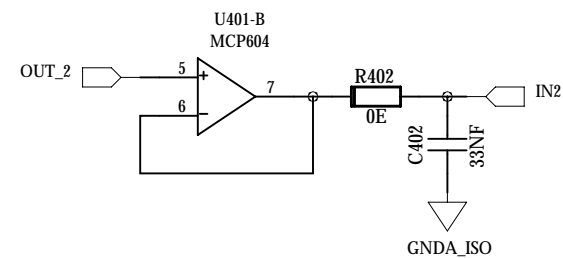
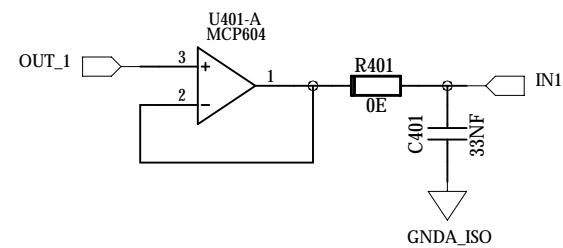
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TITLE:	OHMv1 MASTER R0.6 POWER_SECTION		
VOIDRON CO.			
DESIGN BY:	AS / AA / MK	FILE:	OHMv1_MASTER_R0.6_SCH
RELEASE:	26 - FEB - 2024	SHEET:	3 OF 5



ADC_1

TITLE:	OHMv1 MASTER R0.6 ADC1_SECTION		
VOIDRON CO.			
DESIGN BY:	AS / AA / MK	FILE:	OHMv1_MASTER_R0.6_SCH
RELEASE:	26 - FEB - 2024	SHEET:	4 OF 5

A

B

C

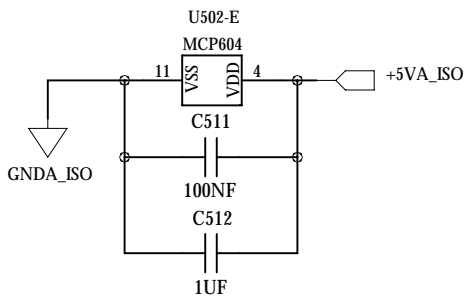
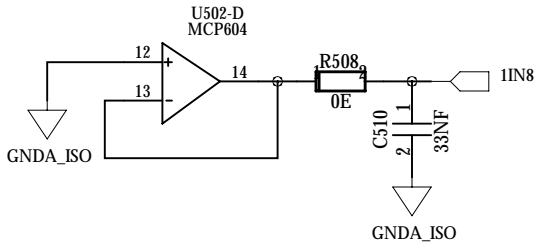
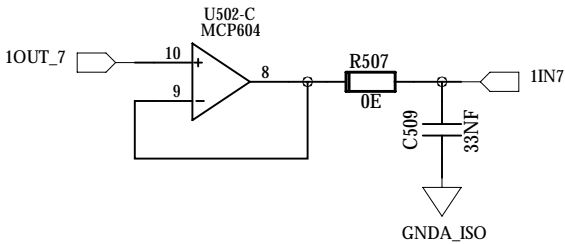
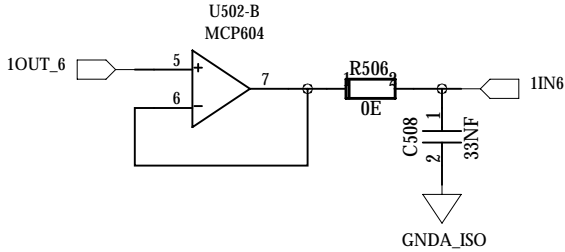
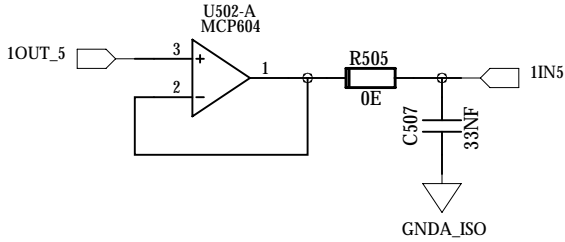
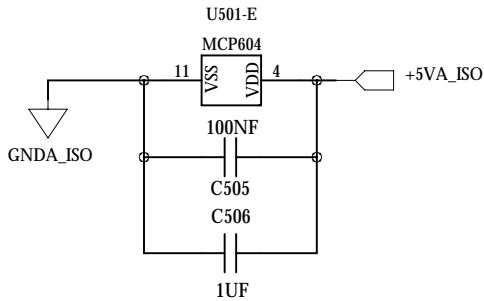
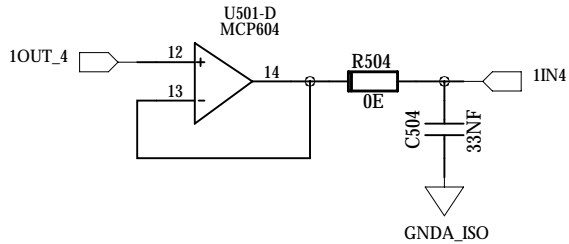
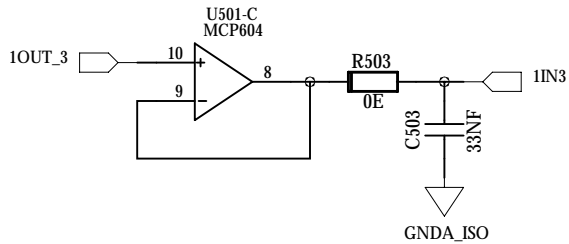
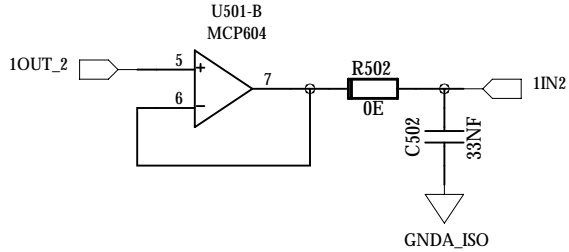
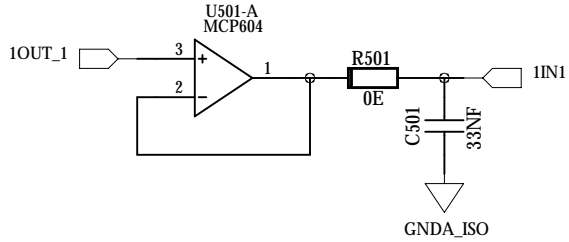
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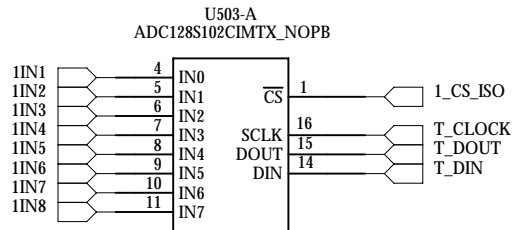
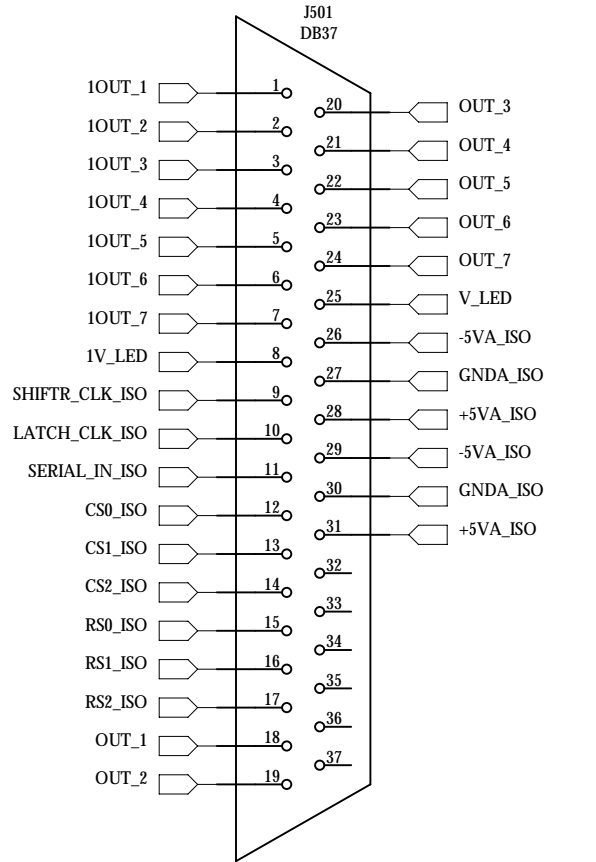
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ADC_2



TITLE:	OHMv1 MASTER R0.6 ADC2_SECTION		
VOIDRON CO.			
DESIGN BY:	AS / AA / MK	FILE:	OHMv1_MASTER_R0.6_SCH
RELEASE:	26 - FEB - 2024	SHEET:	5 OF 5