WORK EXPERIENCE

C&J Energy Services Inc.,

- Research & Technology Department, Houston
- Completed 2 important R&D firmware projects for the DsPIC and FPGA based Digital Processing Controller board (DPC).
- The embedded firmware development process included reading circuit schematics, extensive researching, learning new software, collaborating with the hardware and design teams, etc.
- Won the award for the Best Intern for excellent performance and completing 2 valuables projects in just 2 months.
- This portfolio contains:
  - ✓ A copy of the power point presentation made by me for the competition held at C&J to select the best intern.
  - ✓ Codes for DsPIC for my projects:
    1. Generator and Detector Synchronization Signal Generation.
    2. Enabling signals for PTX Telemetry.