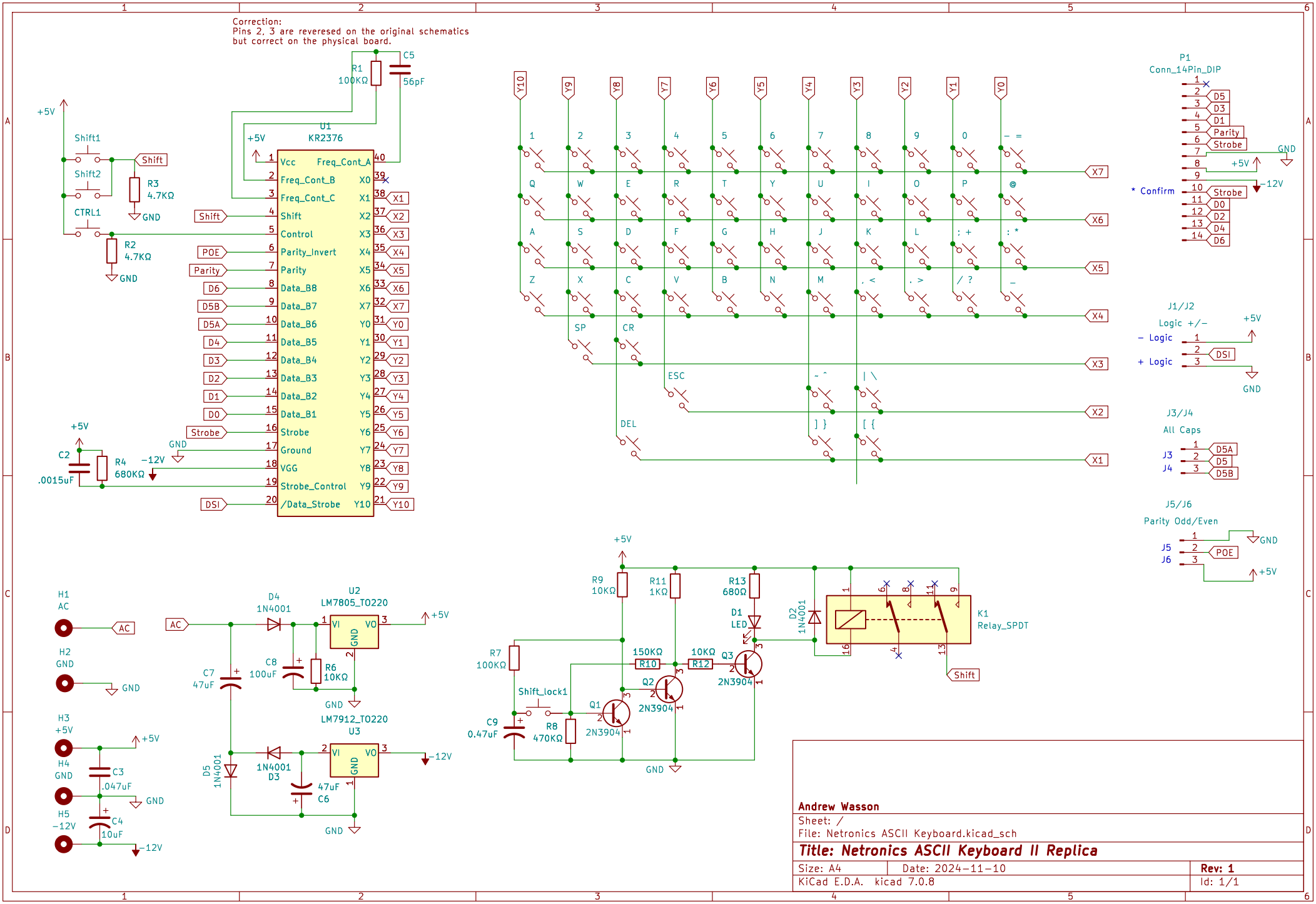
Correction:
Pins 2, 3 are reveresed on the original schematics
but correct on the physical board.
U1
KR2376
Vcc
Freq_Cont_A
Freq_Cont_B
Freq_Cont_C
Shift
Control
Parity_Invert
Parity
Data_B8
Data_B7
Data_B6
Data_B5
Data_B4
Data_B3
Data_B2
Data_B1
Strobe
Ground
VGG
Strobe_Control
/Data_Strobe
X0
X1
X2
X3
X4
X5
X6
X7
Y0
Y1
Y2
Y3
Y4
Y5
Y6
Y7
Y8
Y9
Y10
R1
100KΩ
C5
56pF
Shift1
Shift2
CTRL1
R3
4.7KΩ
R2
4.7KΩ
C2
.0015uF
R4
680KΩ
P1
Conn_14Pin_DIP
* Confirm
D5
D3
D1
Parity
Strobe
D0
D2
D4
D6
J1/J2
Logic +/−
− Logic
+ Logic
DSI
J3/J4
All Caps
J3
J4
D5A
D5
D5B
J5/J6
Parity Odd/Even
J5
J6
POE
H1
AC
H2
GND
H3
+5V
H4
GND
H5
−12V
C3
.047uF
C4
10uF
D4
1N4001
U2
LM7805_TO220
C7
47uF
C8
100uF
R6
10KΩ
LM7912_TO220
U3
D5
1N4001
1N4001
D3
47uF
C6
R9
10KΩ
R11
1KΩ
R13
680Ω
D1
LED
D2
1N4001
K1
Relay_SPDT
R7
100KΩ
150KΩ
R10
10KΩ
R12
Q3
2N3904
Q2
2N3904
Q1
2N3904
Shift_lock1
C9
0.47uF
R8
470KΩ
Andrew Wasson
Sheet: /
File: Netronics ASCII Keyboard.kicad_sch
Title: Netronics ASCII Keyboard II Replica
Size: A4
Date: 2024-11-10
Rev: 1
KiCad E.D.A. kicad 7.0.8
Id: 1/1