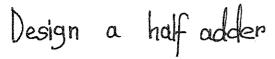
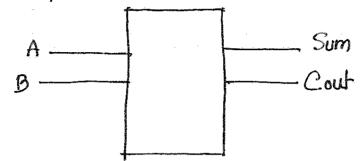
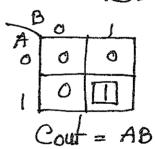
Arithmetic circuits:

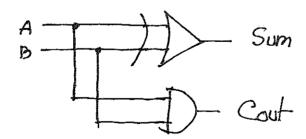


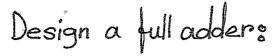


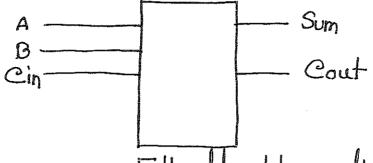
Α	В	Sum	Cout
0	0	0	٥
0	ţ		٥
	O		٥
1	ſ	0	1

B	, 0	
0	0	
		Ø
Si	m = A	B+AB
	= 4	AÐВ
6	b ~	į







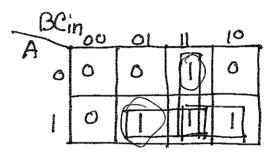


Full adder	block	diagram
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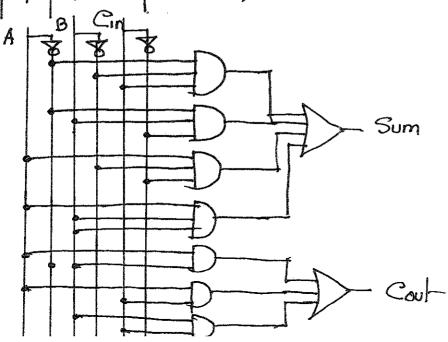
A	B	Cin	Sum	Cout
0	0	0	0	0
O	0	l	-	0
0		0	ı	٥
0	Cass (SE)		0	(
\$ Comments	0	0		O
Winest	0	d) manual	O	
	-	6	٥	a a a a a a a a a a a a a a a a a a a
аруста		1	1	

BC;	n 00	0	11	10	_
A o	0		0	Ш	- Control of the Cont
garas		O	Ausses	0	division and the second

Sum = ABCin + ABCin + ABCin + ABC



Cout = AB + ACin + BCin



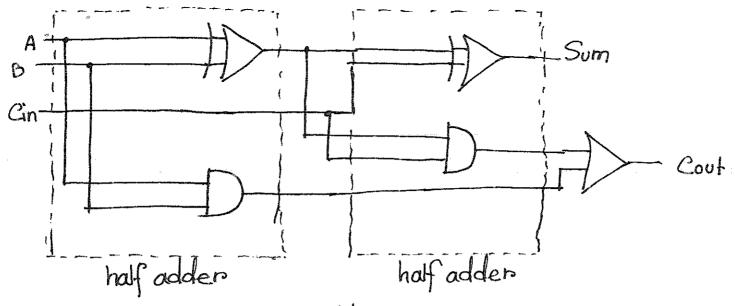
Design a full-adder with two half-adders.

Bein oo ol 11 10

A o o o III o

Cout =
$$ABCin + ABCin + AB$$

= $Cin(AB + AB) + AB$
= $Cin(A \oplus B) + AB$



Full-adder logic diagram