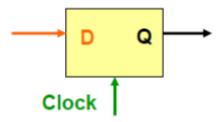
Take Home

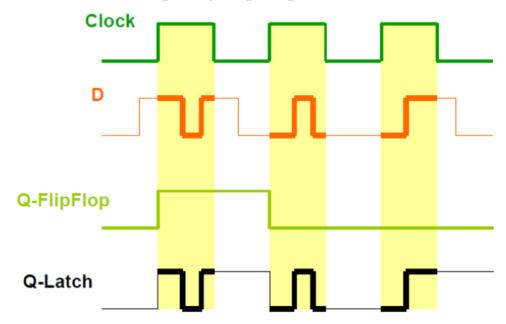
You are expected to solve homework problems individually. If needed, you may seek help from your friends. However, do not copy. Show all steps with your solutions for full credit.

/ 50 Name: Key

1) (10 points) Complete the timing diagram below to compare between a Latch and a Flipflop



Comparing Flip-flop and Latch



2) (15 points) Complete the following Truth tables. Current state (q) and the next expected state (q*) are given, you are asked to find the input value(s) to D, SR, T, and JK flipflops. Make any reasonable assumptions.

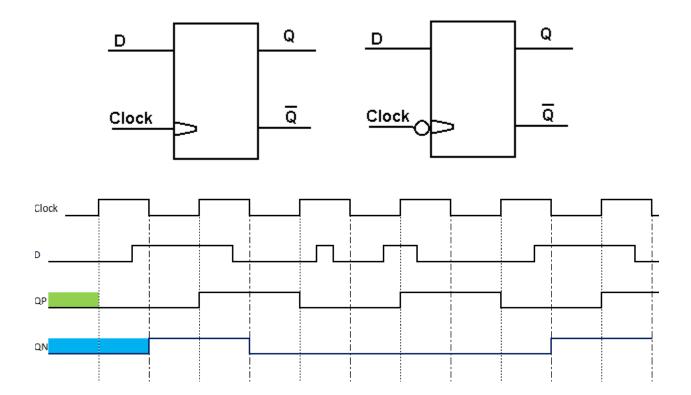
D flip-flop				
q	q*	D		
0	0	0		
0	1	1		
1	0	0		
1	1	1		

SR flip-flop					
q	q*	S	R		
0	0	0	X		
0	1	1	0		
1	0	0	1		
1	1	X	0		

T flip-flop				
q	q*	T		
0	0	0		
0	1	1		
1	0	1		
1	1	0		

JK flip-flop					
q	q*	J	K		
0	0	0	X		
0	1	1	X		
1	0	X	1		
1	1	X	0		

- 3) (10 points) For the following edge-triggered D flip-flops show the timing diagram
 - a. Assuming it is a positive-edge- triggered D flip-flop
 - b. Assuming it is a negative-edge-triggered D flip-flop



HW-10

4) (15 points) Consider the JK flip flop shown below and complete the timing diagram showing Q for as far as you can

