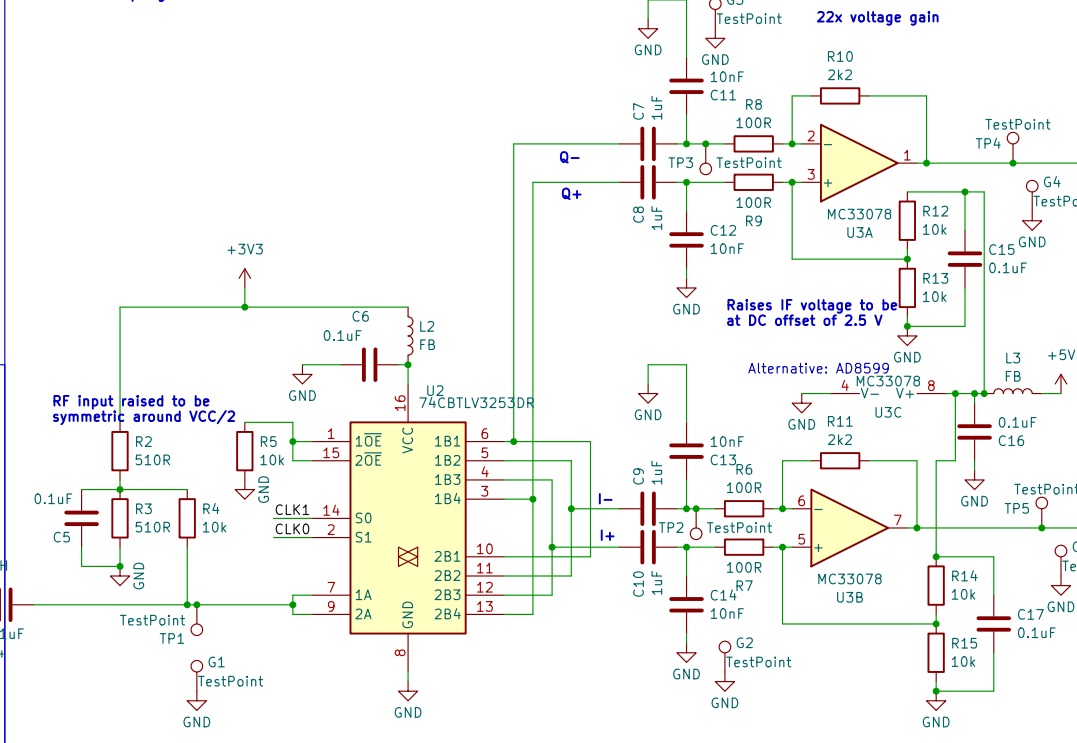
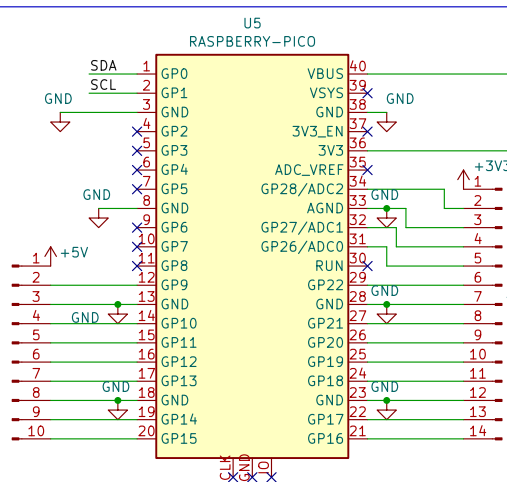
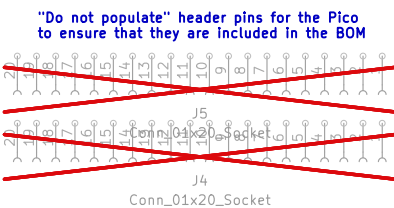
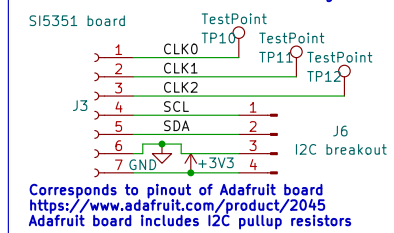


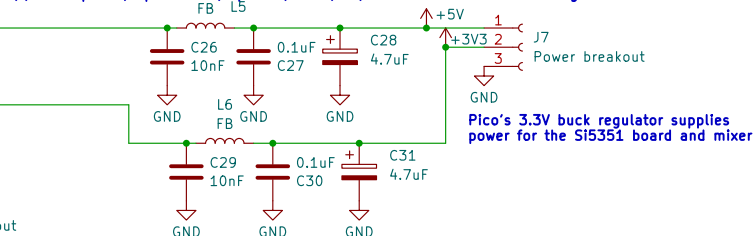
Quadrature Sampling Detector



Quadrature LO clock generator



Following Figure 2.5 of link below to minimize noise from USB power
https://ftdichip.com/wp-content/uploads/2020/08/AN_146_USB_Hardware_Design_Guidelines_for_FTDI_Cs.pdf



T41 V12 schematics:
https://github.com/DRWJSCHMIDT/T41/tree/main/T41_V012_Files_01-15-24/T41_V012_Schematic_PDFs
Designed by Oliver KI3P
Heavily derived from T41 V12 RF board

Sheet: /	File: VWS-SDR-ref-design.kicad_sch
Title: VWS HF SDR	
Size: A4	Date: 2025-01-06
KiCad E.D.A. 8.0.7	Rev: V3
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