

# Chapter 1

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## Introduction to Computers and Java

- 1.1 Computer Basics
- 1.2 A Sip of Java
- 1.3 Programming Basics
- 1.4 Graphics Supplement(Optional)



# Objectives of Chap 1

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- 1) a brief overview of computer hardware and software
- 2) basic techniques of program design In general and object oriented programming in particular.
- 3) an overview of the java programming language.
- 4) introduce to applets and some graphics basics.



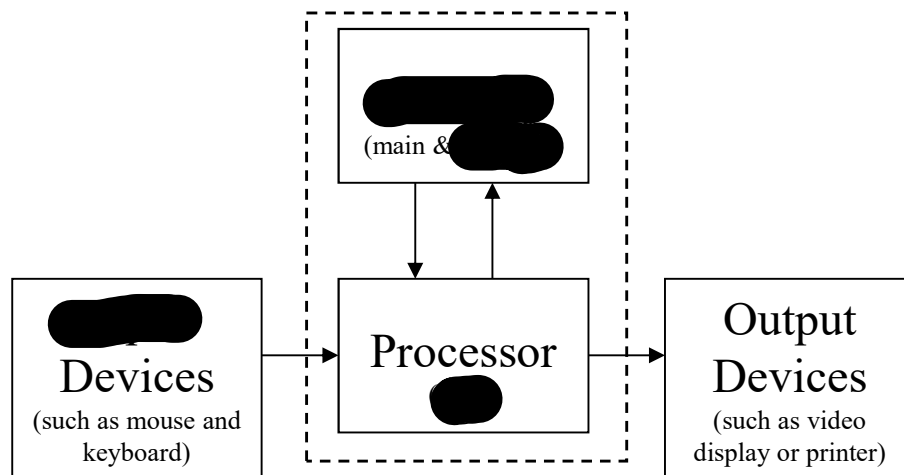
# 1.1 Computer Basics

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- Computer system: hardware + software
- Hardware: the physical components(machine)
- Program : a set of instructions for computer
- Software: All the kinds of programs used to give instructions to the computer

# Common Hardware Components

## Standard Hardware Organization




- Processor (CPU)
  - » Central [redacted] Unit
  - » [redacted] and [redacted] the instructions
- Memory
  - » main & auxiliary
  - » holds [redacted] and [redacted]
- Input device(s)
  - » mouse, keyboard, etc.
- Output device(s)
  - » video display, printer, etc.
- CPU and memory are physically [redacted] together



# Physical Organization

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- Keyboard
- Monitor
- 
  - » CPU
  - » memory
  - » disk drives
  - » I/O connectors
  - » etc.



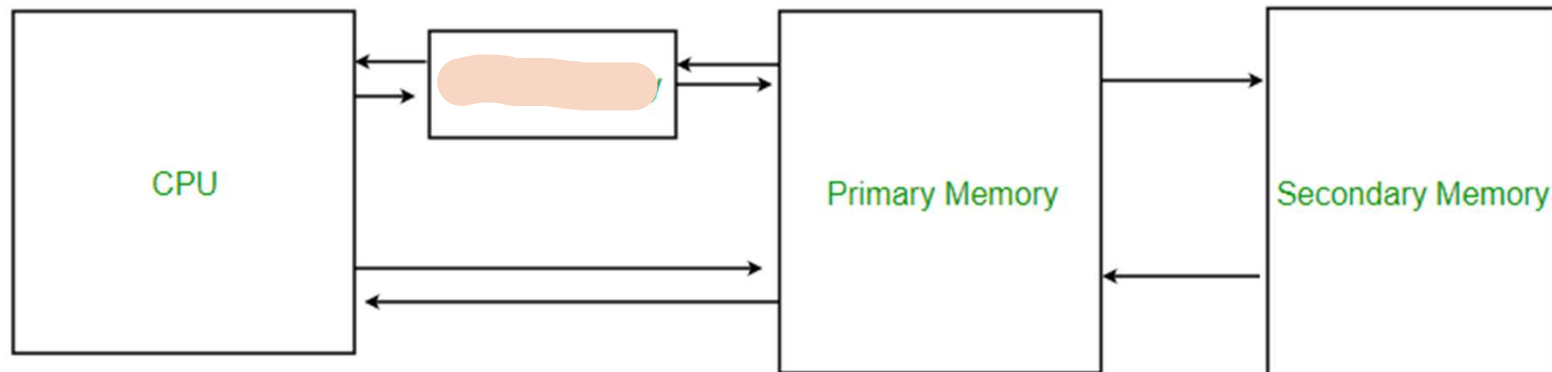
# Two Kinds of Memory

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- Main
  - » working area
  - » ( ) stores program and data (while program is )
- ( )
  - » (more or less) memory
  - » Secondary memory
  - » saves program and results
  - » includes floppy & hard disk drives, CDs, tape, etc.
- (Cache Memory??)—Program Code

# Cache memory

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# Main Memory Organization

- **Bit** = one binary digit
  - » Binary digit can have only one of two values, 0 or 1
- **Byte** = 8 bits
- “Byte Addressable”
  - » Main memory is a list of numbered locations that contain one of data in each location
- Number of bytes per data item may vary

Address	Data Byte	
3021	1111 0000	Item 1: 2 bytes stored
3022	1100 1100	
3023	1010 1010	Item 2: 1 byte stored
3024	1100 1110	Item 3: 3 bytes stored
3025	0011 0001	
3026	1110 0001	
3027	0110 0011	Item 4: 2 bytes stored
3028	1010 0010	
3029	...	Next Item, etc.



•			
•			
•			
byte 3021	11110000	2-byte memory location at address 3021	
byte 3022	11001100		
byte 3023	10101010	1-byte memory location at address 3023	
byte 3024	11001110	3-byte memory location at address 3024	
byte 3025	00110001		
byte 3026	11100001		
byte 3027	01100011	2-byte memory location at address 3027	
byte 3028	10100010		
byte 3029	01111111		
byte 3030	10000001		
byte 3031	10111100		
•			
•			
•			

Display 1.1

Main Memory



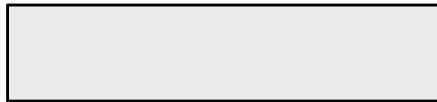
# Why Just Zero and Ones?

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- It is easy to make a physical device that has only two stable states.

- 전산전자가 제일 좋아하는 노래

»



- 전산전자가 제일 좋아하는 친구

»



냥만에 대하여



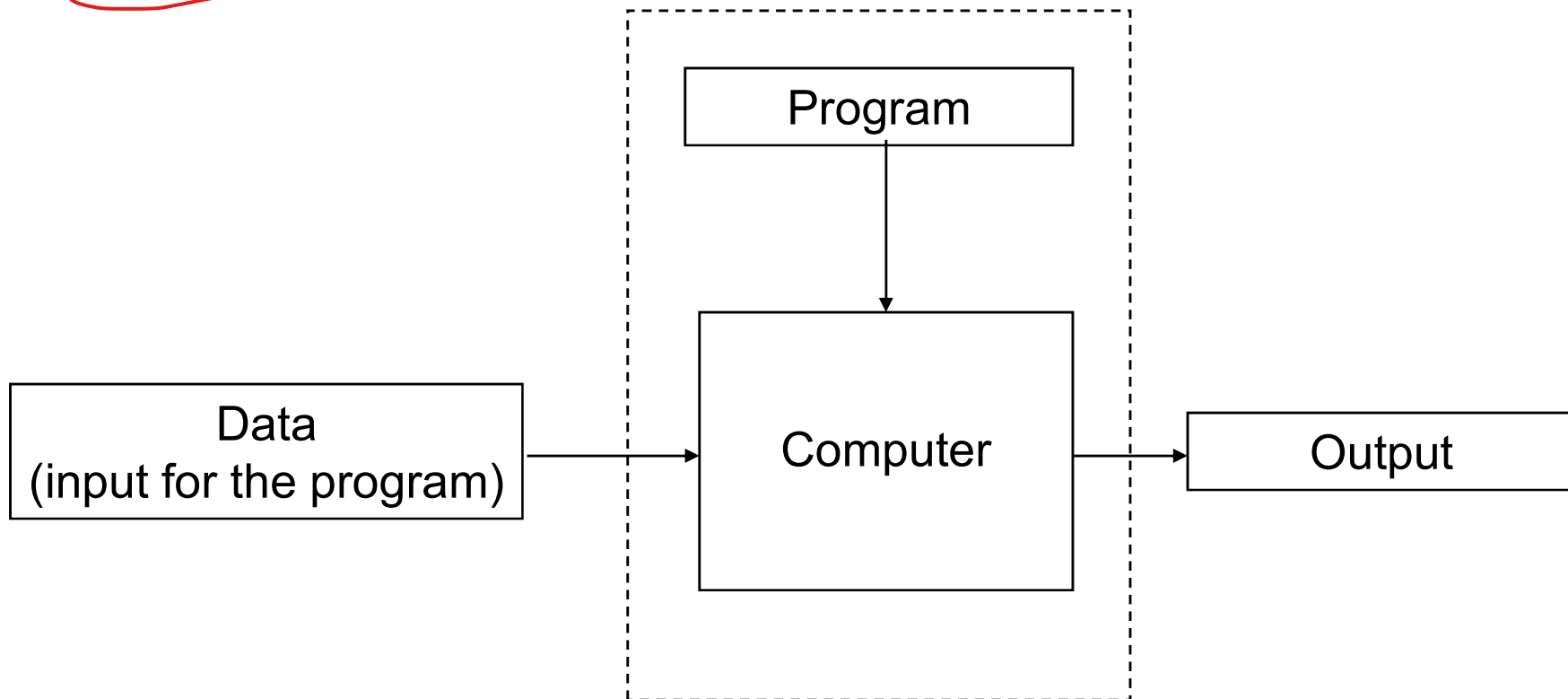
# Questions

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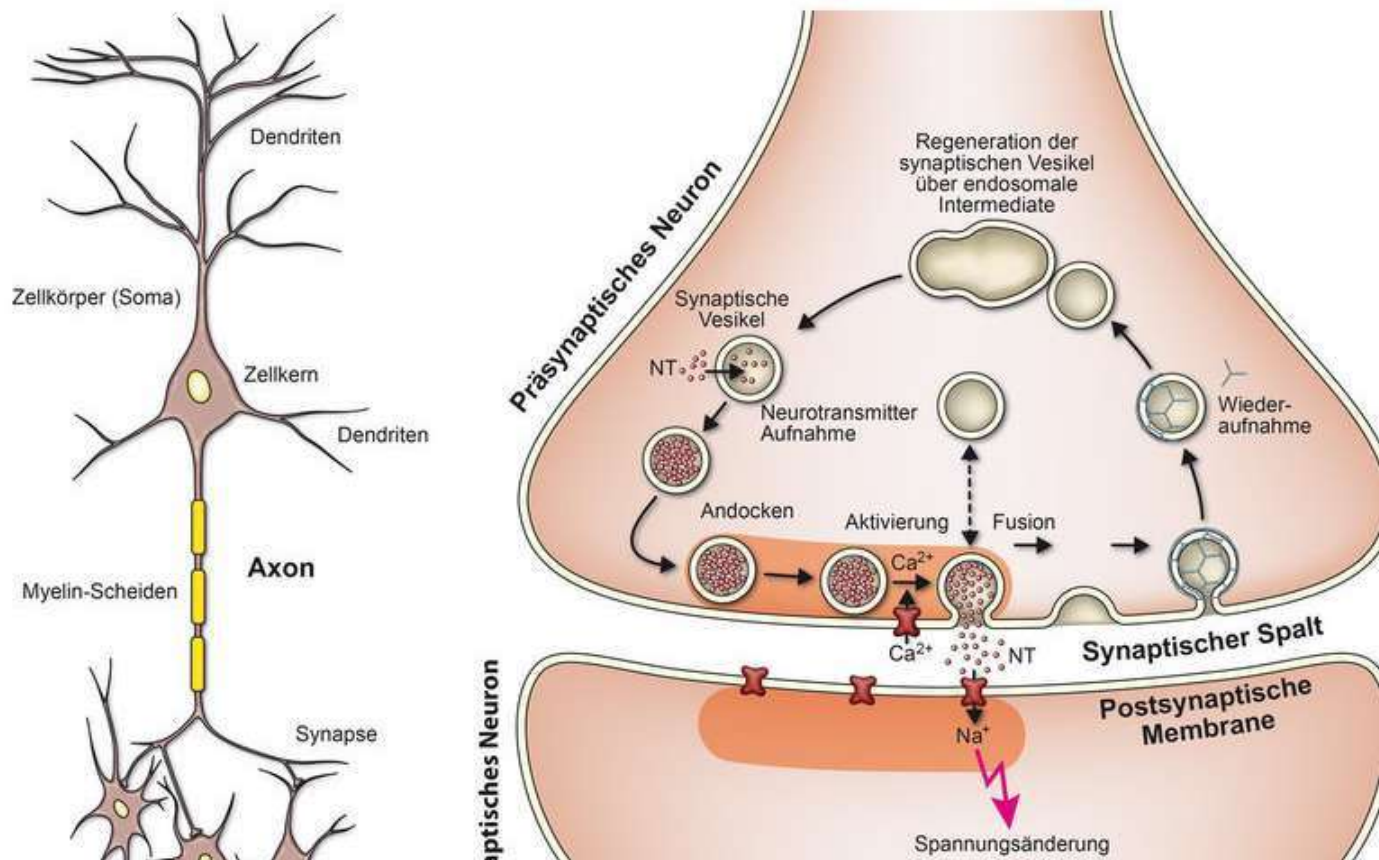
- Von-neumann architecture ?
  - » merits and demerits ?
- Non Von neumann architecture ?
- Brain-like architecture ?
- Stored Program ?

# Display 1.2 Running a Program

—a set of instructions for a computer to follow



# Non-von Neumann architecture brain like computer



# Many Types of

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- User-created applications
- Existing applications
  - » word-processor/editor
  - » web browser
  - » compiler or assembler , etc.
- Operating System
  - » DOS, Microsoft Windows, MacOS, Linux, UNIX, etc.
  - » MacOS ??

# Various Types of

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- Command-line
  - » type in key words and letters
  - » DOS and UNIX
- Menu
  - » parts of DOS and Windows
- (Graphical User Interface)
  - » click on icon
  - » also called “event-driven”
  - » MacOS, Windows



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Welcome to FreeDOS

CuteMouse v1.9.1 alpha 1 [FreeDOS]  
Installed at PS/2 port  
C:\>ver

FreeCom version 0.82 pl 3 XMS\_Swap [Dec 10 2003 06:49:21]

C:\>dir  
Volume in drive C is FREEDOS\_C95  
Volume Serial Number is 0E4F-19EB  
Directory of C:\

FDOS		<DIR>	08-26-04	6:23p
AUTOEXEC	BAT	435	08-26-04	6:24p
BOOTSECT	BIN	512	08-26-04	6:23p
COMMAND	COM	93,963	08-26-04	6:24p
CONFIG	SYS	801	08-26-04	6:24p
FDOSBOOT	BIN	512	08-26-04	6:24p
KERNEL	SYS	45,815	04-17-04	9:19p
	6 file(s)		142,038 bytes	
	1 dir(s)		1,064,517,632 bytes free	

C:\>\_



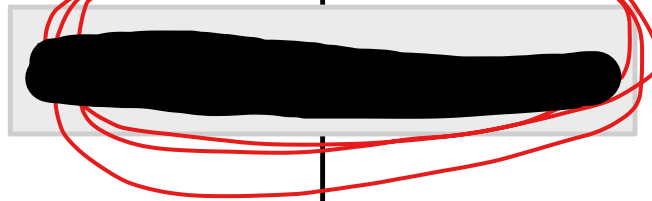


# Programming Language Hierarchy

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High-Level Language (HLL)

Assembly Lanuage



Hardware

# The highs and lows of programming languages ...

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## High-Level Language (HLL)

- » closest to natural language
- » words, numbers, and math symbols
- » not directly understood by hardware
- » “portable” source code (hardware [REDACTED])
- » Java, C, C++, COBOL, FORTRAN, BASIC, Lisp, Ada, etc.

## Machine Language


(lowest level)

- » least natural language for humans, most natural language for hardware
- » just 0s and 1s
- » directly understood by hardware
- » not portable (hardware [REDACTED])



(middle level)

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- a more or less human readable version of machine language
  - words, abbreviations, letters and numbers replace 0s and 1s
  - easily translated from human readable to machine executable code
  - like machine code, not portable (hardware
- 

# Examples & Questions ??

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TABLE 6-6 Fortran Program to Add Two Numbers

```
INTEGER A, B, C  
DATA A, 83 B, -23  
C = A + B  
END
```

TABLE 6-2 Binary Program to Add Two Numbers

Location	Instruction code
0	0010 0000 0000 0100
1	0001 0000 0000 0101
10	0011 0000 0000 0110
11	0111 0000 0000 0001
100	0000 0000 0101 0011
101	1111 1111 1110 1001
110	0000 0000 0000 0000

TABLE 6-3 Hexadecimal Program to Add Two Numbers

Location	Instruction
000	2004
001	1005
002	3006
003	7001
004	0053
005	FFE9
006	0000

TABLE 6-4 Program with Symbolic Operation Codes

Location	Instruction	Comments
000	LDA 004	Load first operand into AC
001	ADD 005	Add second operand to AC
002	STA 006	Store sum in location 006
003	HLT	Halt computer
004	0053	First operand
005	FFE9	Second operand (negative)
006	0000	Store sum here

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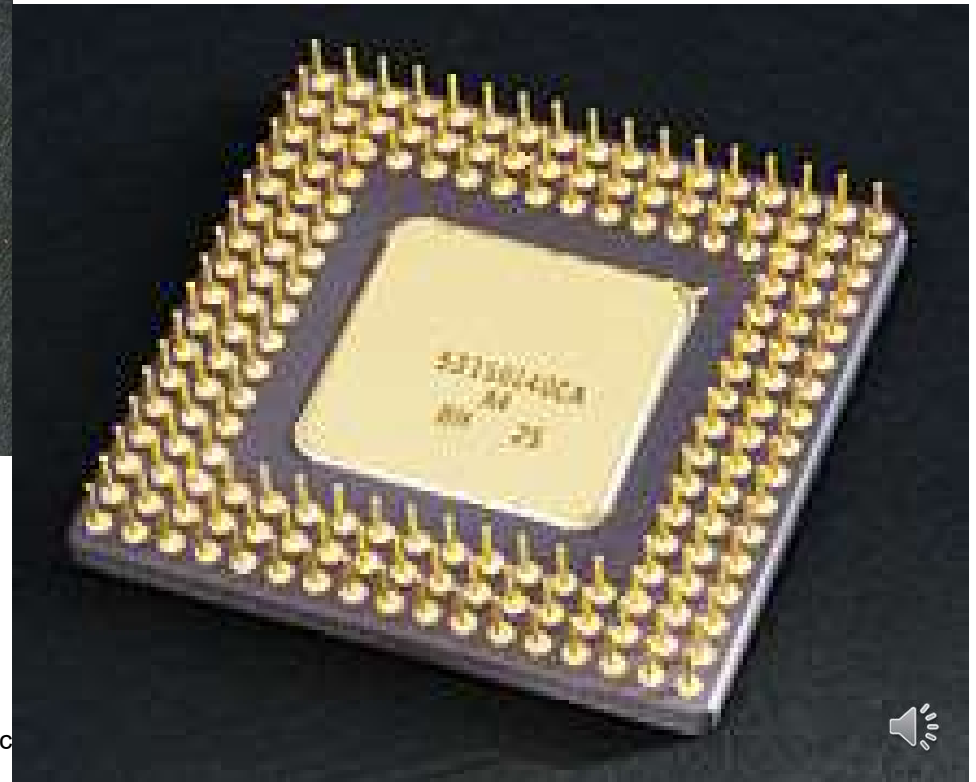
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TABLE 6-5 Assembly Language Program to Add Two Numbers

	ORG 0	/Origin of program is location 0
	LDA A	/Load operand from location A
	ADD B	/Add operand from location B
	STA C	/Store sum in location C
	HLT	/Halt computer
A,	DEC 83	/Decimal operand
B,	DEC -23	/Decimal operand
C,	DEC 0	/Sum stored in location C
	END .	/End of symbolic program



# CPU & instruction Set



# 갤럭시 S/S2의 CPU

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- 갤럭시S
  - » 삼성전자가 자체 개발한 1GHz급 CPU인 'S5PC111'이 탑재돼 있음
- 갤럭시 S2
  - » 엑시노스 (**EXYNOS 4210, 4212**)
  - » 삼성의 ARM Coretex-A9 계열 프로세서의 SoC (System on Chip)

## 10 32-비트 프로세서: 80386 범위

10.1 80386DX

10.2 80386SX

10.3 80376

10.4 80386SL

10.5 80386EX

## 11 32 비트 프로세서: 80486 계열

11.1 80486DX

11.2 80486SX

11.3 80486DX2

11.4 80486SL

11.5 80486DX4

## 13 32 비트 프로세서: P6/펜티엄 M 마이크로아키텍처

13.1 펜티엄 프로

13.2 펜티엄 II

13.3 셀러론 (펜티엄 II 기반)

13.4 펜티엄 III

13.5 펜티엄 II, III 제논

13.6 셀러론 (펜티엄 III 코퍼마인 기반)

13.7 셀러론 (펜티엄 III 투알라틴 기반)

13.8 펜티엄 M

13.9 셀러론 M

13.10 인텔 코어

13.11 듀얼 코어 제논 LV

13.12 인텔 펜티엄 듀얼 코어



## Original 8086/8088 instructions

[\[edit\]](#)

Instruction	Meaning	Notes
<a href="#">AAA</a>	ASCII adjust AL after addition	used with unpacked <a href="#">binary coded decimal</a>
AAD	ASCII adjust AX before division	8086/8088 datasheet documents only base 10 version of the AAD instruction ( <a href="#">opcode</a> 0xD5 0x0A), but any other base will work. Later Intel's documentation has the generic form too. NEC V20 and V30 (and possibly other NEC V-series CPUs) always use base 10, and ignore the argument, causing a number of incompatibilities
AAM	ASCII adjust AX after multiplication	Only base 10 version is documented, see notes for AAD
AAS	ASCII adjust AL after subtraction	
ADC	Add with carry	<code>destination := destination + source + <a href="#">carry_flag</a></code>
ADD	Add	
AND	<a href="#">Logical AND</a>	




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**Added with 80186/80188**

Instruction	Meaning	Notes
BOUND	Check array index against bounds	raises software interrupt 5 if test fails
ENTER	Enter stack frame	equivalent to <div><pre>PUSH BP MOV BP, SP SUB SP, n</pre></div>
INS	Input from port to string	equivalent to <div><pre>IN (E)AX, DX MOV ES:[(E)DI], (E)AX ; adjust (E)DI according to operand size and DF</pre></div>



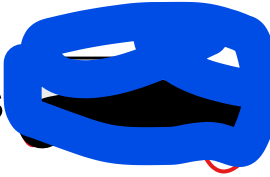
# Getting from Source to Machine Code

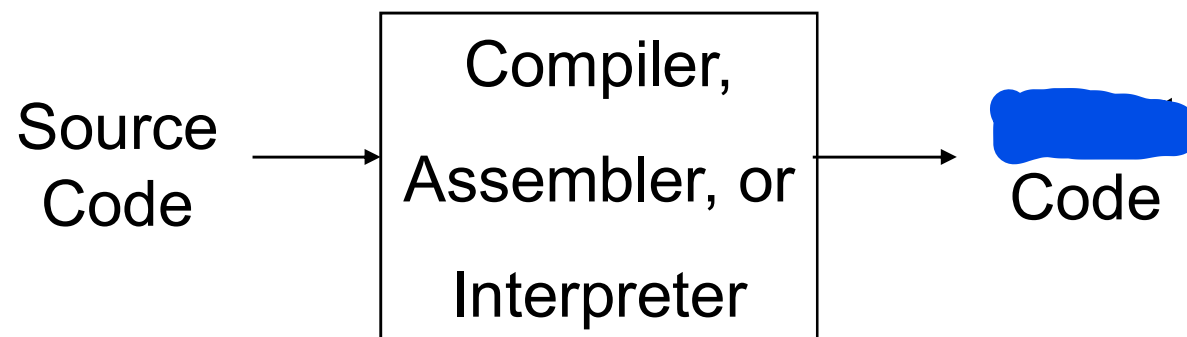
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- *a program*  
translating from a high-level language source code to machine (object, or executable) code.
-   
a program that translates HLL source code to machine (object, or executable) code.
-   
translating from assemble language source code to machine (object, or executable) code.
-   
a program that translates assembly source code to machine (object, or executable) code.
- Compilers need to know the specific target hardware

# Compilers vs. Assemblers vs. Interpreters

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- Compilers and Assemblers
  - » translation is a  user step
  - » translation is  .e. not at run time
- Interpreters - another way to translate source to object code
  - » interpretation (from source to object code) is not a separate user step
  - » translation is  .e. at run time



# Disadvantage of translation

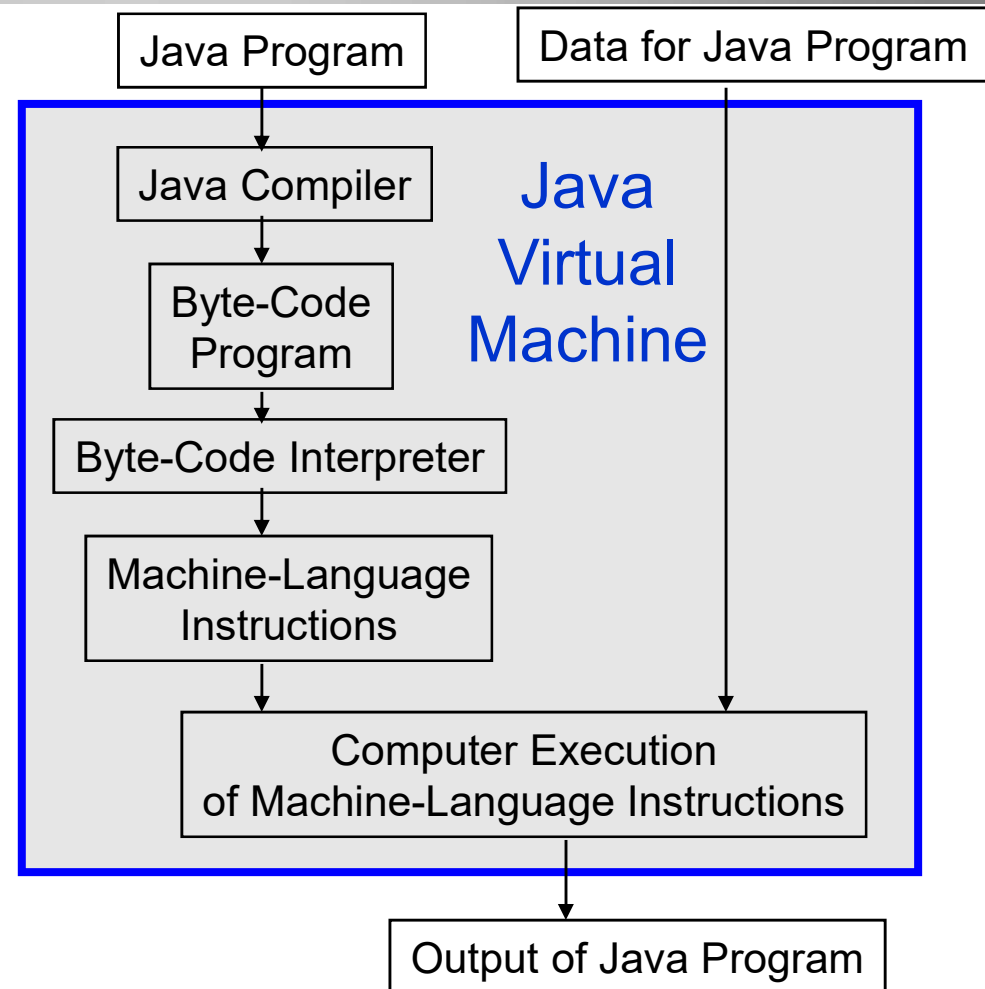
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- 1) need a  for each type of computer and each operating system
- 2) if a manufacturer comes out with a new type of computer, a team of programmers must write  for that computer.



# Java Program Translation

- Both Compilation and Interpretation
- Intermediate Code:
  - » similar to assembly code, but hardware
- Interpreter translates from generic code to hardware-specific code



# Questions ??

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- Java Virtual Machine?
- Virtual ?
- Virtual Machine ?
- Java Byte Code ?
  - » machine language  
for a [REDACTED] computer  
that is something like  
the [REDACTED] of all computers

# Java Byte Code

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- generated by [redacted]
  - » Instead of generating machine language as most compilers do, the Java compiler generates byte code.
- easily [redacted] machine language of various kinds of computers
- executed by [redacted]
- [redacted] to programmer
  - » You don't have to know anything about how byte code works to write a Java program.


# Why Use Byte Code?

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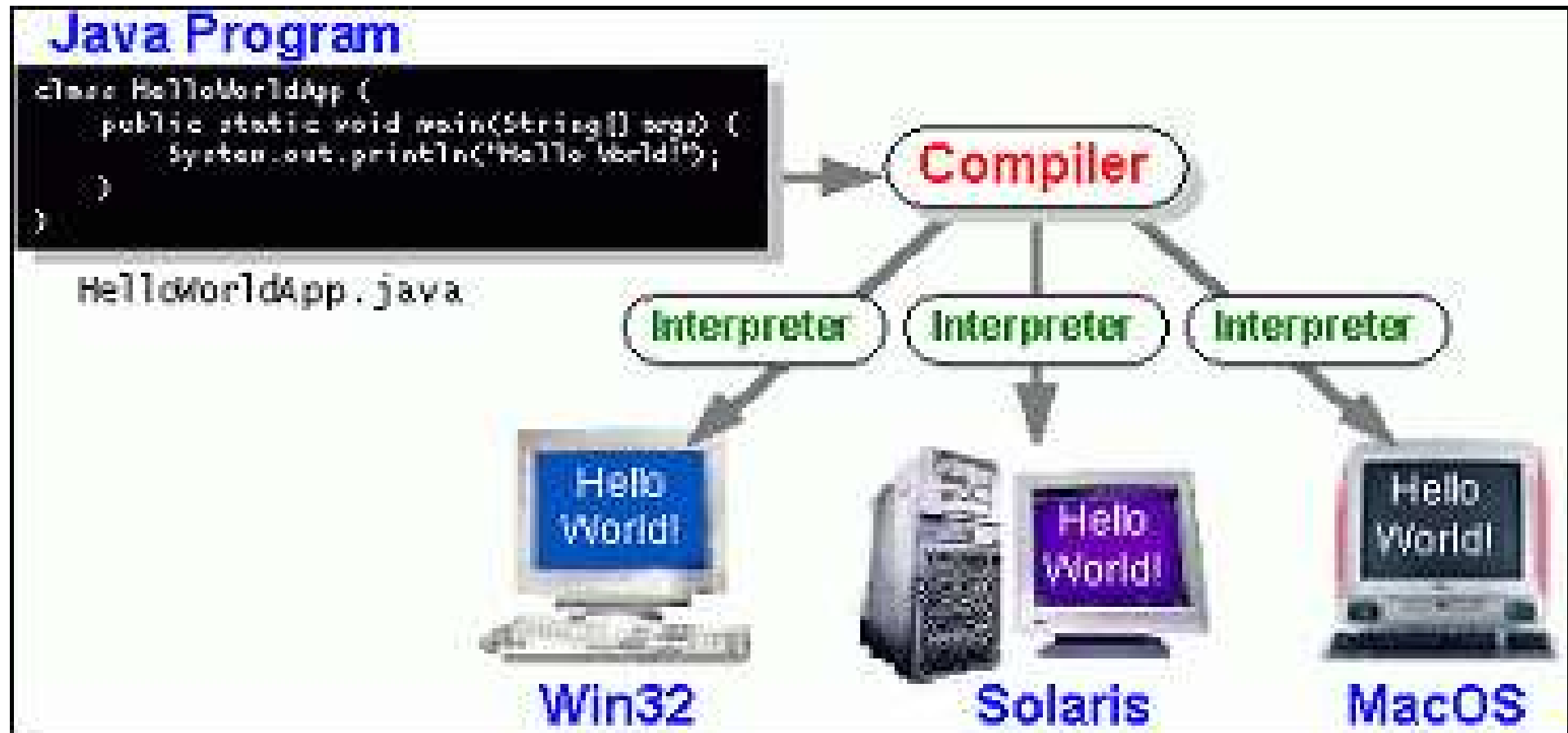
## Disadvantages:

- requires both 
-  program execution 

## Advantages:




- 
  - » very important
  - » same program can run on computers of different types (useful with the Internet)
  - » Java compiler for new types of computers can be made quickly → Java is good for internet applications.

# portability




# portability

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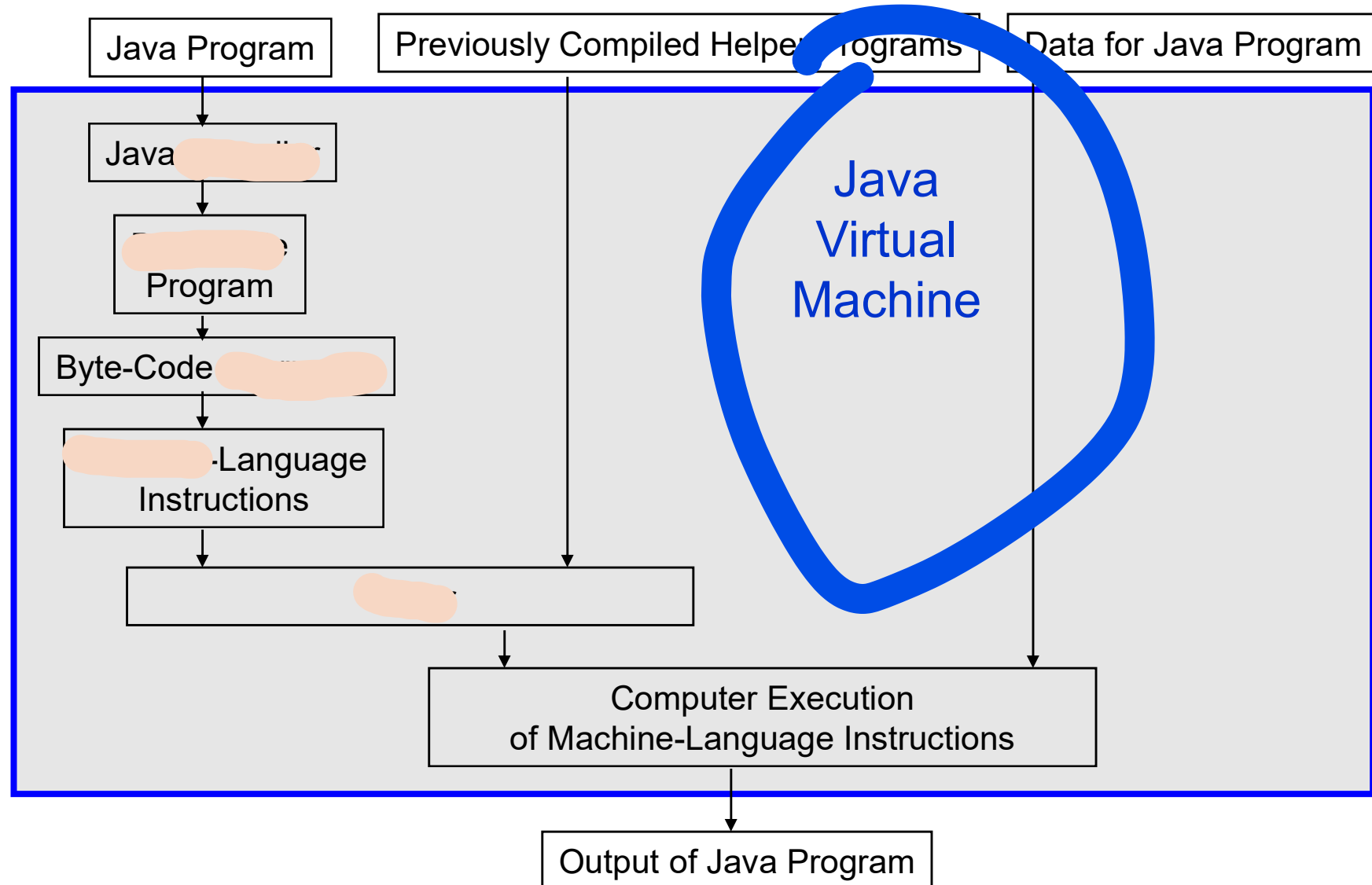
- When new type of computer
  - » do not have to design a new java 
  - » every type of computer must have its own byte-code 
  - » but these interpreter are  programs compared to a compiler
- new type of computer
  - » originally a language for programming home appliances
  - » ➔ Web Browser

# Class Loader

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- 
  - » the process of connecting them
  - » need connect several pieces of program
- » ➔ Class loader
- » ➔ Linker

# Java Program Translation Including Linker





# Questions ??

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- linking ??

- » 실행 class가 [ ] 중에 필요한 외부 class와 결합(linking)되도록 하는 방식

- 장점

- » 여러 클래스들이 한 프로그램을 구성하는 경우에, 한 클래스를 수정해야 할 일이 발생할 경우, [ ] 필요가 없이, 단지 변경된 클래스가 속한 파일만을 컴파일함

