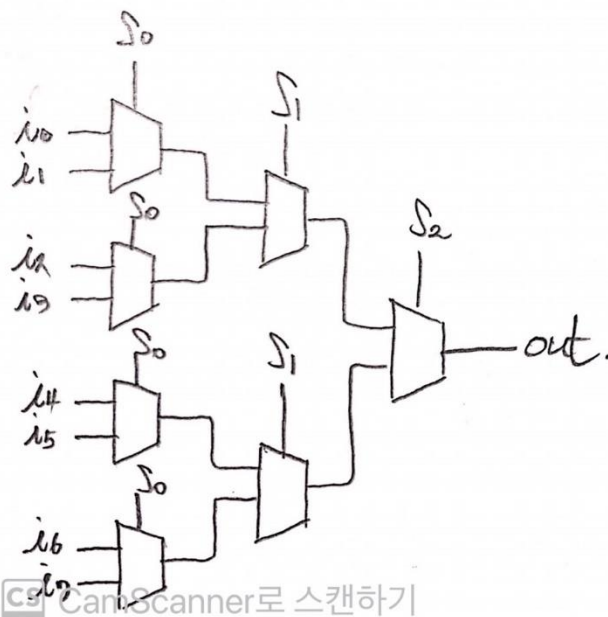


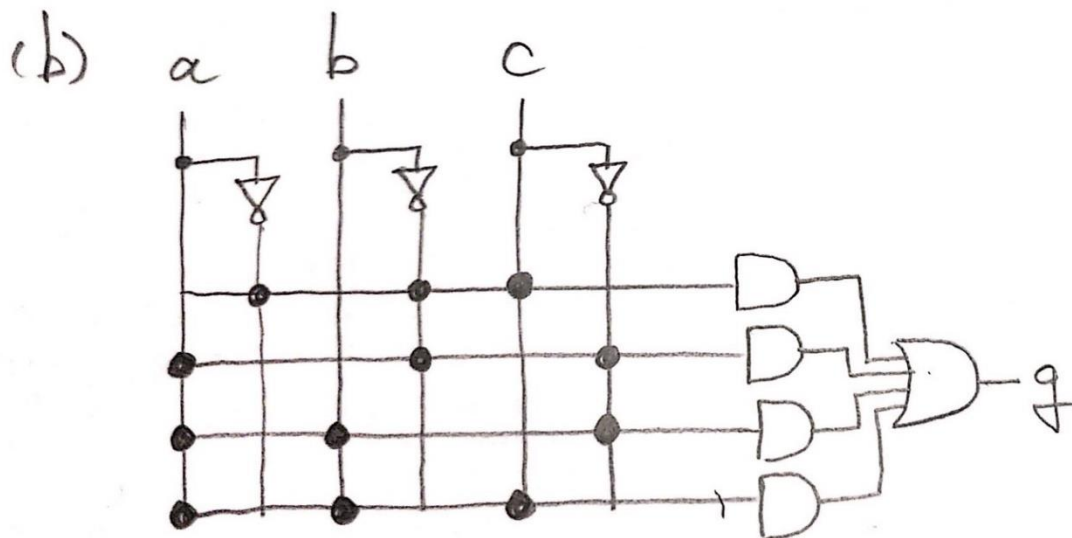
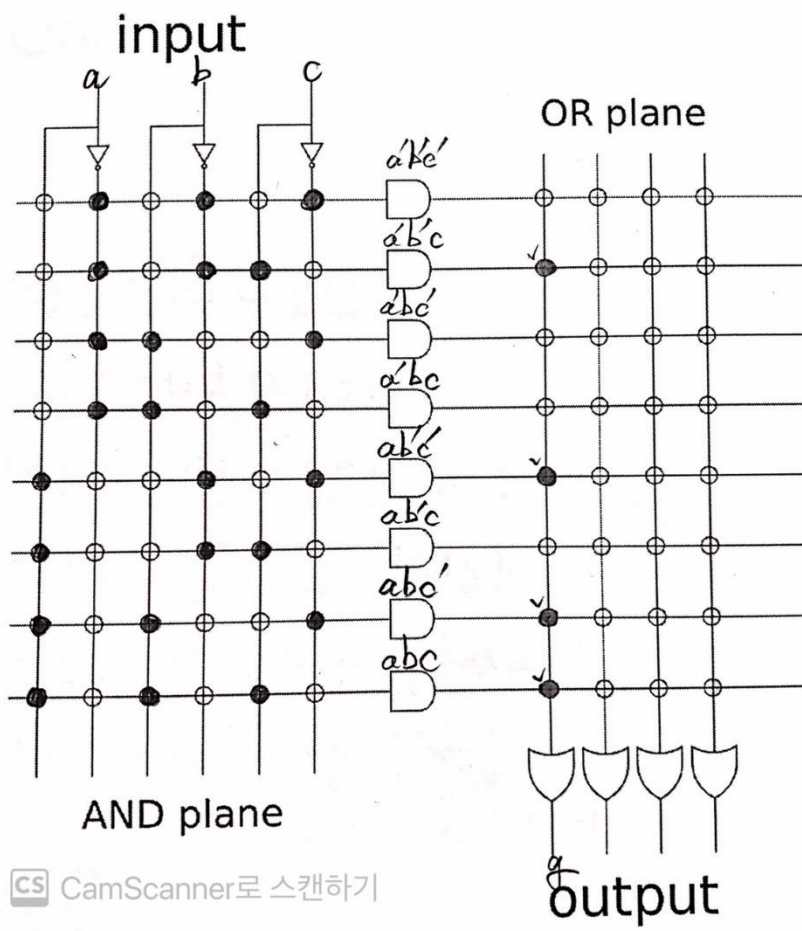
#1. Design 8-to-1 mux with 2-to-1 mux.



#2.

a	b	c	g	
0	0	0	0	m_0
0	0	1	1	m_1
0	1	0	0	m_2
0	1	1	0	m_3
1	0	0	1	m_4
1	0	1	0	m_5
1	1	0	1	m_6
1	1	1	1	m_7

$$g = a'b'c + ab'c' + abc' + abc$$



(c)

