Instructor: 蔡文錦 EC710 Ext. 54816

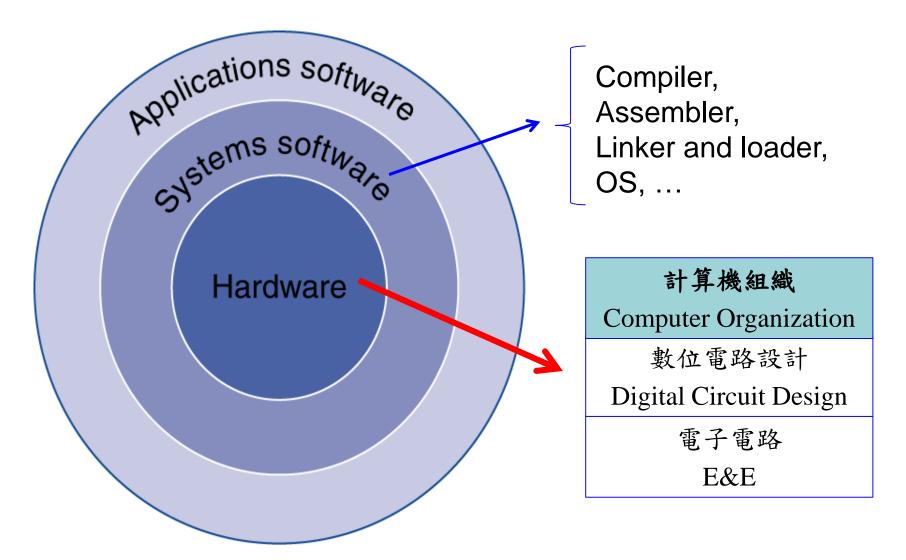
TAs: 王麗婷 <u>litingw.cs11@nycu.edu.tw</u>

王菱君 <u>lingjiun24@gmail.com</u>

余孟倫 <u>mlyu.cs11@nycu.edu.tw</u>

黃逸弘 m.acc.ch1230@gmail.com

EC637 Ext. 54749



Course outline

- Ch 1: Computer Abstractions and Technology
- Ch 2: Instructions: Language of the Computer
- Ch 3: Arithmetic for Computers
- Ch 4: The processor
- Ch 5: Memory Hierarchy
- Ch 6: Multiprocessor

Projects

- 1 term project with 4~5 stages
- Using verilog
- 1 member per team.

Course material

- Text Book: "Computer Organization & Design, The Hardware/Software Interface," David A. Patterson and John L. Hennessy, MORGAN KAUFMANN, 5th edition.
- Lecture handout (including this course outline)
 - Download from new E3 (http://e3new.nctu.edu.tw)
- Project assignment & announcement
 - on new E3 platform

Grading

- 3 exams: 60%
- Quizzes, 20%
- 1 term project (4~5 stages): 20%