**Computer Organization 2023 Lab 4**

**Finished part:**

**ALU.v, ALU\_Ctrl.v, Decoder.v, Simple\_Single\_CPU.v**

**Problems you met and solutions:**

**The problem I met is that the slt instruction need signed comparison. I have searched on the web for a long time and finally solve it.**

**Also I found that I am not familiar with the structure of single cycle CPU, so I spent some time to study it and finally complete this homework.**

**If you create additional module, please give a short explanation here:**

**No additional module.**