

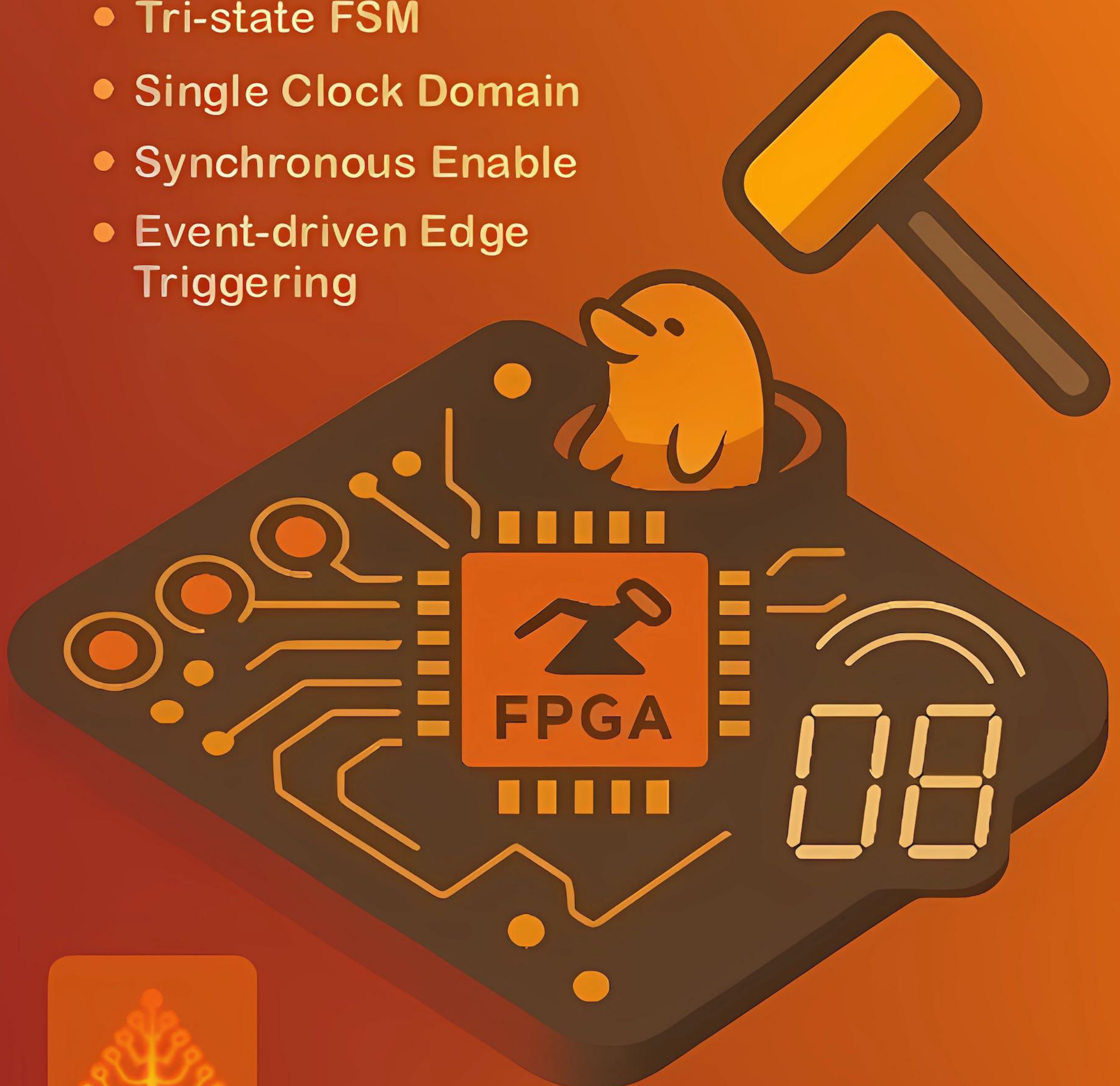
FPGA



WHACK-A-MOLE

Verilog · ACE1-75T · 100 MHz

- Tri-state FSM
- Single Clock Domain
- Synchronous Enable
- Event-driven Edge Triggering



INTERACTIVE GAME SYSTEM