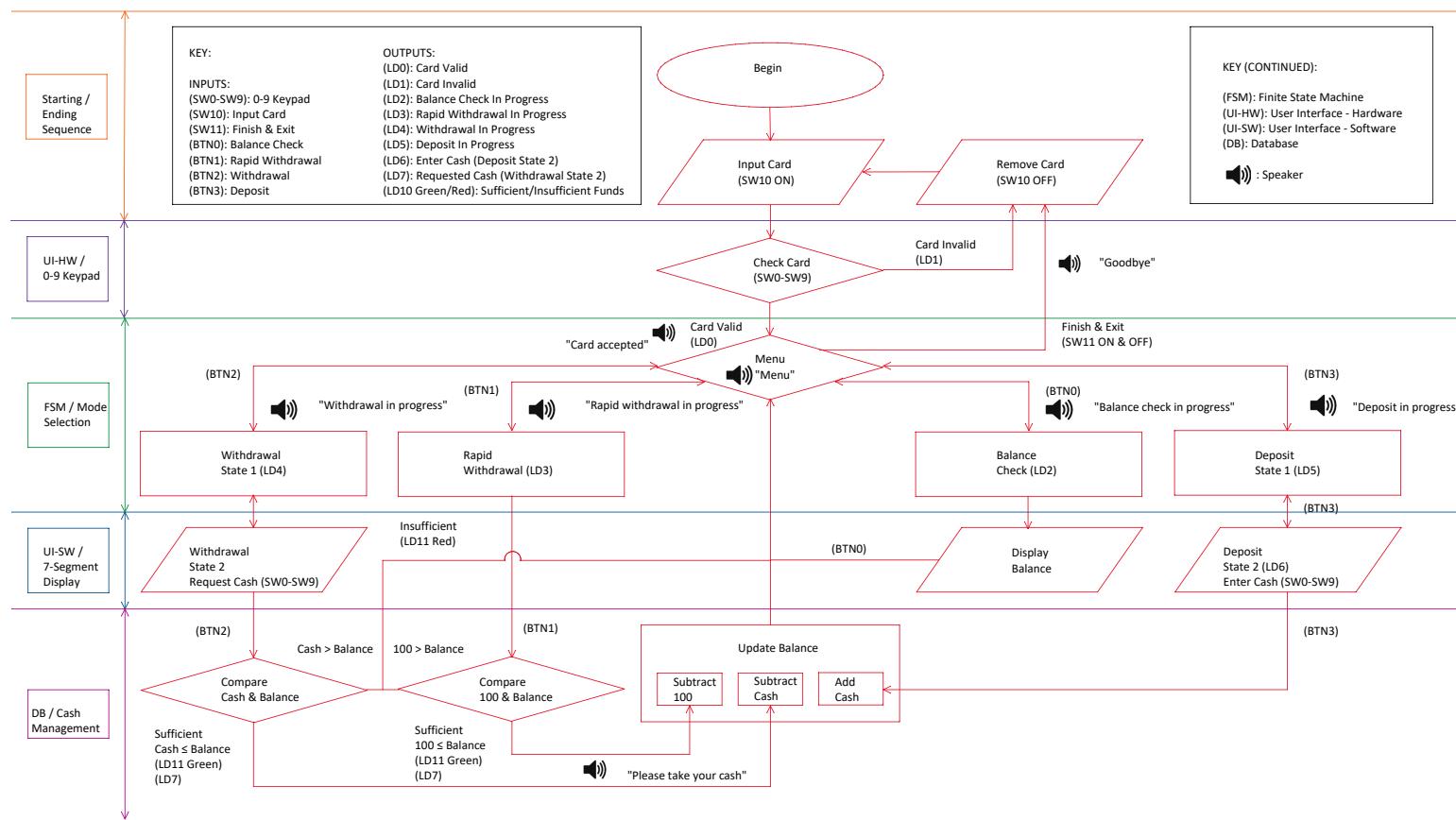


Algorithm Flowchart



FPGA Implementation

