## WORK EXPERIENCE

Digital Design and Verification Engineer Digital Verification Engineer Internship Electrical Engineer Internship 2/2016-today Summer 2015 Summer 2014

Texas Instruments

- Produced test cases using constrained-random techniques and relevant analysis to determine appropriate coverage
- Constructed testbenches, tests using principles of VMM and OVM with focus on re-use and supporting prior projects
- Developed and implemented bit and timing accurate models of digital design
- Tested RTL designs on FPGAs to uncover broader system-level/human-interface issues.
- Proactively worked with RTL owner to identify issues and verify all fixes

System Administrator

11/2014 - 2/2015

7/2012-11/2014

General Desktop Support Work-Study

Office of Information Technology at University of Colorado at Boulder

- Deployment of new software to labs, management of departmental servers
- Customer service experience with students, staff and faculty.
- Troubleshot a wide variety of issues; helped users remove viruses, set up dual-booting on their computers, and regularly attempted recovery of data on failing hard drives and other minor hardware repairs.

## SKILLS

- Design and verification of digital systems in Systemverilog and Verilog
- C (embedded), C#, TCL, Python, Perl, and shell scripting
- Modeling systems, writing reusable (OVM, UVM-style) self-checking testbenches in SystemVerilog, Verilog, and VHDL
- Cadence Tools, NCsim, SimVision, vManager, vPlanner, Virtuoso
- Tools such as: \*NIX, MATLAB/Octave, Mathematica, Xilinx Vivado, NI Microwave Office, Altium, Eclipse, LATEX, and Microsoft products

## **EDUCATION**

Master of Engineering (part-time) — exp. graduation: 2023

Study: Embedded Systems Engineering GPA: 3.925/4.0

Bachelor of Science — graduated 2015

Electrical Engineering, focus on DSP and Electromagnetics. Dean's List: 2014–2015

University of Colorado, Boulder, CO 80309

## INTERESTS

- Extra-Class Amateur Radio Operator License (callsign: KOØI)
- 3D Printing 2010–today
- Gardening