

<b>WORK EXPERIENCE</b>	<i>Digital Design and Verification Engineer</i>	<i>2/2016-today</i>
	<i>Digital Verification Engineer Internship</i>	<i>Summer 2015</i>
	<i>Electrical Engineer Internship</i>	<i>Summer 2014</i>
	Texas Instruments	
	<ul style="list-style-type: none"><li>• Designed the digital logic for a non-volatile memory control block.</li><li>• Produced test cases using constrained-random techniques and relevant analysis to determine appropriate coverage.</li><li>• Supported other teams as a borrowed resource, experiencing and absorbing the benefits and drawbacks of a variety of contexts from building models to working in UVM environments.</li><li>• Constructed testbenches, tests using principles of OVM and UVM with focus on re-use and supporting backwards compatibility.</li><li>• Developed and implemented bit and timing accurate models of digital design</li><li>• Validated RTL on FPGAs to uncover broader system-level/human-interface issues.</li></ul>	
	<i>System Administrator</i>	<i>11/2014 - 2/2015</i>
	<i>General Desktop Support Work-Study</i>	<i>7/2012-11/2014</i>
	Office of Information Technology at University of Colorado at Boulder	
	<ul style="list-style-type: none"><li>• Deployed new software to labs, management of departmental servers</li><li>• Assisted students, staff and faculty to troubleshoot a wide variety of issues; helped users remove viruses, replaced screens, set up dual-booting, and regularly attempted recovery of data on failing hard drives.</li></ul>	
<b>SKILLS</b>	<ul style="list-style-type: none"><li>• Design and verification of digital systems in Systemverilog and Verilog</li><li>• C (embedded), C#, TCL, Python, Perl, and shell scripting</li><li>• Modeling systems, writing reusable (OVM, UVM-style) self-checking testbenches in SystemVerilog, Verilog, and VHDL</li><li>• Cadence Tools, NCsim, SimVision, vManager, vPlanner, Virtuoso</li><li>• Tools such as: *NIX, MATLAB/Octave, Mathematica, Xilinx Vivado, NI Microwave Office, Altium, Eclipse, L<sup>A</sup>T<sub>E</sub>X, and Microsoft products</li></ul>	
<b>EDUCATION</b>	<i>Master of Engineering (part-time) — exp. graduation: 2023</i> <i>Study: Embedded Systems Engineering GPA: 3.925/4.0</i> <i>Bachelor of Science — graduated 2015</i> <i>Electrical Engineering, focus on DSP and Electromagnetics. Dean's List: 2014–2015</i> University of Colorado, Boulder, CO 80309	
<b>INTERESTS</b>	<ul style="list-style-type: none"><li>• Extra-Class Amateur Radio Operator License (callsign: KOØI)</li><li>• 3D Printing 2010–<i>today</i></li><li>• Gardening</li><li>• Music Composition and instrument creation</li></ul>	