

WORK EXPERIENCE

Digital Design and Verification Engineer 2/2016-*today*
Digital Verification Engineer Internship Summer 2015
Electrical Engineer Internship Summer 2014
Texas Instruments

- Designed the digital logic for a non-volatile memory control block.
- Produced test cases using constrained-random techniques and relevant analysis to determine appropriate coverage.
- Supported other teams as a borrowed resource, experiencing and absorbing the benefits and drawbacks of a variety of contexts from building models to working in UVM environments.
- Constructed testbenches, tests using principles of OVM and UVM with focus on re-use and supporting backwards compatibility.
- Developed and implemented bit and timing accurate models of digital design
- Validated RTL on FPGAs to uncover broader system-level/human-interface issues.

System Administrator 11/2014 - 2/2015
General Desktop Support Work-Study 7/2012-11/2014
Office of Information Technology at University of Colorado at Boulder

- Deployed new software to labs, management of departmental servers
- Assisted students, staff and faculty to troubleshoot a wide variety of issues; helped users remove viruses, replaced screens, set up dual-booting, and regularly attempted recovery of data on failing hard drives.

SKILLS

- Design and verification of digital systems in Systemverilog and Verilog
- C (embedded), C#, TCL, Python, Perl, and shell scripting
- Modeling systems, writing reusable (OVM, UVM-style) self-checking testbenches in SystemVerilog, Verilog, and VHDL
- Cadence Tools, NCsim, SimVision, vManager, vPlanner, Virtuoso
- Tools such as: *NIX, MATLAB/Octave, Mathematica, Xilinx Vivado, NI Microwave Office, Altium, Eclipse, L^AT_EX, and Microsoft products

EDUCATION

Master of Engineering (part-time) — exp. graduation: 2023
Study: Embedded Systems Engineering GPA: 3.925/4.0
Bachelor of Science — graduated 2015
Electrical Engineering, focus on DSP and Electromagnetics. Dean's List: 2014–2015
University of Colorado, Boulder, CO 80309

INTERESTS

- Extra-Class Amateur Radio Operator License (callsign: KO0I)
- 3D Printing 2010-*today*
- Gardening
- Music Composition and instrument creation