

<b>WORK EXPERIENCE</b>	<i>Digital Design and Verification Engineer</i>	<i>2/2016-today</i>
	<i>Digital Verification Engineer Internship</i>	Summer 2015
	<i>Electrical Engineer Internship</i>	Summer 2014
	Texas Instruments	
	<ul style="list-style-type: none"><li>• Produced test cases using constrained-random techniques and relevant analysis to determine appropriate coverage</li><li>• Constructed testbenches, tests using principles of VMM and OVM with focus on re-use and supporting prior projects</li><li>• Developed and implemented bit and timing accurate models of digital design</li><li>• Tested RTL designs on FPGAs to uncover broader system-level/human-interface issues.</li><li>• Proactively worked with RTL owner to identify issues and verify all fixes</li></ul>	
	<i>System Administrator</i>	11/2014 - 2/2015
	<i>General Desktop Support Work-Study</i>	7/2012-11/2014
	Office of Information Technology at University of Colorado at Boulder	
	<ul style="list-style-type: none"><li>• Deployment of new software to labs, management of departmental servers</li><li>• Customer service experience with students, staff and faculty.</li><li>• Troubleshoot a wide variety of issues; helped users remove viruses, set up dual-booting on their computers, and regularly attempted recovery of data on failing hard drives and other minor hardware repairs.</li></ul>	
<b>SKILLS</b>	<ul style="list-style-type: none"><li>• Design and verification of digital systems in Systemverilog and Verilog</li><li>• C (embedded), C#, TCL, Python, Perl, and shell scripting</li><li>• Modeling systems, writing reusable (OVM, UVM-style) self-checking testbenches in SystemVerilog, Verilog, and VHDL</li><li>• Cadence Tools, NCsim, SimVision, vManager, vPlanner, Virtuoso</li><li>• Tools such as: *NIX, MATLAB/Octave, Mathematica, Xilinx Vivado, NI Microwave Office, Altium, Eclipse, L<sup>A</sup>T<sub>E</sub>X, and Microsoft products</li></ul>	
<b>EDUCATION</b>	<i>Master of Engineering (part-time) — exp. graduation: 2023</i>	
	<i>Study: Embedded Systems Engineering GPA: 3.925/4.0</i>	
	<i>Bachelor of Science — graduated 2015</i>	
	<i>Electrical Engineering, focus on DSP and Electromagnetics. Dean's List: 2014-2015</i>	
	University of Colorado, Boulder, CO 80309	
<b>INTERESTS</b>	<ul style="list-style-type: none"><li>• Extra-Class Amateur Radio Operator License (callsign: KOØI)</li><li>• 3D Printing 2010–today</li></ul>	